Risk Factors

Today’s presentations contain forward-looking statements. All statements made that are not historical facts are subject to a number of risks and uncertainties, and actual results may differ materially. Please refer to our most recent Earnings Release and our most recent Form 10-Q or 10-K filing available on our website for more information on the risk factors that could cause actual results to differ.
Performance tests and ratings are measured using specific computer systems and/or components and reflect the approximate performance of Intel products as measured by those tests. Any difference in system hardware or software design or configuration may affect actual performance. Buyers should consult other sources of information to evaluate the performance of systems or components they are considering purchasing. For more information on performance tests and on the performance of Intel products, visit Intel Performance Benchmark Limitations (http://www.intel.com/performance/resources/limits.htm).
The Relentless Pursuit of Moore's Law
Moore’s Law

Amazing products
... done efficiently
... with the industry
... for the customer.
2006 - An Amazing Year

SERVERS
- Intel® Xeon® Processor LV
- Intel® Xeon® Processor 5000 Series
- Intel® Xeon® Processor 9000 Series
- Intel® Xeon® Processor 5100 Series
- Intel® Xeon® Processor 7100 Series
- Intel® Xeon® Processor 3000 Series
- Clovertown

CLIENTS
- Intel® Core™ 2 Extreme Processor
- Intel® Core™ 2 Duo Processor
- Intel® vPro™ Technology based PC’s
- Intel® Core™ 2 Extreme Processor QX6700

Q1 Q2 Q3 Q4
2006

Projected

Some dates estimated and subject to change without notice.
Intel® Core™ 2 Duo Launch
July 27, 2006

The Core 2 Contribution

World’s Best Process Technology
World’s Best Microprocessor Design
World’s Biggest Factory Network

World’s Best Processors

Intel® Core™ 2 Duo
Intel® Core™ 2 Extreme
Intel® Xeon™
Intel® Core™ 2 Duo Technology

Normalized to Intel® Pentium®4 Processor 631 with HT Technology

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Intel® Pentium®4 Processor 631</th>
<th>Intel® Core™2 Duo Processor</th>
</tr>
</thead>
<tbody>
<tr>
<td>SYSmark® 2004 SE</td>
<td>1.0</td>
<td>1.48</td>
</tr>
<tr>
<td>SPECint_int_base2000</td>
<td>1.0</td>
<td>2.11</td>
</tr>
<tr>
<td>Thermal Design Power</td>
<td>84W</td>
<td>65W</td>
</tr>
</tbody>
</table>

"... didn't lose a single benchmark in our comparison, NOT A SINGLE ONE"
Susan M. Whitney
IBM Systems Technology Group
General Manager, System x
<table>
<thead>
<tr>
<th>IBM Systems Agenda/Intel Relationship</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Collaborative Innovation</strong></td>
</tr>
<tr>
<td>- BladeCenter* design</td>
</tr>
<tr>
<td>- Performance, scalability</td>
</tr>
<tr>
<td>- Power &amp; cooling</td>
</tr>
<tr>
<td><strong>Openness</strong></td>
</tr>
<tr>
<td>- IBM BladeCenter open spec.</td>
</tr>
<tr>
<td>- Blade.org</td>
</tr>
<tr>
<td>- PCI &amp; PCI Express</td>
</tr>
<tr>
<td><strong>Virtualization</strong></td>
</tr>
<tr>
<td>- Intel® Xeon® Processor 5100</td>
</tr>
<tr>
<td>- 3X performance</td>
</tr>
<tr>
<td>- Intel® Xeon® Processor 7100</td>
</tr>
<tr>
<td>- Enterprise scalability</td>
</tr>
<tr>
<td>- Peak performance factor</td>
</tr>
</tbody>
</table>

Other names and brands may be claimed as the property of others.
Intel® Xeon® Processor 7100 Series

Up to 2X Performance Improvement\(^1\)

Leading TPC-H & SAP SD 2-tier Results\(^2\)

Dual Core w/16MB Cache

Up to 3X Performance per Watt Improvement\(^3\)

Platform Compatible

Advanced RAS Features

Intel® Cache Safe Technology

---

1: As measured by SPE Cint2006 score of Xeon 7140M vs. Xeon 7041 * as of 08/06. 2: Based on published/submitted results at tpc.org and sap.com 08/06. Comparing Xeon 7140M to Xeon 7041, based on internal Intel measurements running SPE Cint2006 as of 07/06. For further information see: http://www.intel.com/technology/processors/xeon/spec/index.htm. Other names and brands may be claimed as property of others.
IBM Investment In Innovation

- Intel® Xeon® 7100-based systems:
  - 20% better TPC-C performance than any other 4-socket x86 server
- X-Architecture – 101st #1 leadership score
- Commitment to 4th gen. technology – supporting quad-core Xeon® MP

Other names and brands may be claimed as the property of others.
CoolBlue*
Energy Management Framework

Power Configurator

PowerExecutive™

Rear Door Heat Exchanger
PowerExecutive™

Budget
Plan
Save

Built Upon Intel Demand Based Switching Technology

IBM
intel

Other names and brands may be claimed as the property of others
PowerExecutive* in Action!

- Manage Power at the rack and server level
- Compare actual vs. name plate power at system level
- View inlet and exhaust temperature
- Compare rack actual power vs. Label Power
- Track heat emitted
- Trend power use over time
- Trend temperature over time

Other names and brands may be claimed as the property of others
The World’s First IA Quad Core Processor

Up to 55% Performance Increase

Socket & Platform Compatible

Intel® Core Micro-architecture

High Volume Server & Desktop

Available in Q4’06

Dates subject to change without notice
The World’s First IA Quad Core Processor

- 4MB L2 Cache
- Core
- Core
- Core
- Core

- Cache
- Die Selection
- Compatibility
- Cost
- Capacity
- Customers

1066/1333 MHz

intel
Exceptional Performance And Energy Efficiency

Performance

5X  >4.5X  5X
4X  >4X
3X
2X  Intel Xeon Processor 5000 Series
1X  Dual-Core Intel Xeon Processor 2.8GHz
    Single-Core Intel Xeon Processor

Perf/Watt

5X
4X
3X
2X
1X

Based on published/measured SPECint_rate_base2000, Intel Xeon Processor 3.60 GHz 4GB, Dual-Core Intel Xeon Processor 5160 3.00GHz 8GB Clovertown 2.67 GHz 8GB. Perf/Watt based on SPECint_rate_base2000 benchmark using system power measurements. Intel internal data. Published AMD Opteron 2220 SE (2.3GHz, socket F), spec.org. Other names and brands may be claimed as the property of others.
Itanium® 2 Processor 9000 Series

Up to 2X Better Performance

Up to 20% Lower Power

Up to 2.5X Better Performance per Watt

Hitachi® Cold Fusion-3e/4S-4U Server (Itanium® 2 Processor 9050, 16GB) and Intel® SR870BN4 Server System (with Itanium® 2 Processor w/9M L3 Caches, 16GB). Measured power. StarCD, Workload Version: V3.22 (64bit). Other names and brands may be claimed as the property of others.
Itanium® 2 Flexibility & TCO Advantage

#1 Oracle
IA64 Native

#2 Oracle
Clients
SPARC

#3 IBM
DB2/MQ
SPARC

#4 DB2 Clients
SPARC

#5 Apache
IA64 Native

Seven SQL Server DB’s

Sharing Processor & I/O Resources

Virtual Environment #1

Virtual Environment #2

Hitachi Virtualization Monitor

SMP Environment #1

SMP Environment #2

Hitachi Server
Moore’s Law

In 1965, Intel co-founder Gordon Moore predicted that the number of transistors on a piece of silicon would double every couple of years—an insight later dubbed “Moore’s Law.” His prediction has held true, as ever-shrinking transistor sizes have allowed exponential growth in the number of transistors on a single chip.

Moore’s Law is now a benchmark for the electronics industry, and Intel applies its principles like a company has come about as a whole new way for people to play, learn, and have fun. The company has found new ways for the customer to benefit... for the customer.

Amazing products... done efficiently... with the industry... for the customer
Benchmarking:
Performance & Energy Efficiency

MobileMark®
2002 - 2005

*Other names and brands may be claimed as the property of others.
Benchmarks: Performance & Energy Efficiency


September 25, 2006

*Other names and brands may be claimed as the property of others*
Benchmarking: Performance & Energy Efficiency

MobileMark* 2002 - 2005

EECoMark*

Spec* “Power” (1H’2007)

*Other names and brands may be claimed as the property of others
Benchmarking: Performance & Energy Efficiency

EDS is aligned with Intel in the need for a methodology to measure meaningful energy efficiency and performance based on the way people actually use PCs.”

Matt Trevorrow
Vice President of Workplace Services
Performance and Energy Cost

<table>
<thead>
<tr>
<th>Processor</th>
<th>Annual Energy Cost</th>
<th>Power Measured At The Platform Level $0.10 Per kW-hr</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel® Pentium® D processor 960</td>
<td>$23.48</td>
<td>234</td>
</tr>
<tr>
<td>Intel® Core™2 Duo processor E6300</td>
<td>$14.44</td>
<td>259</td>
</tr>
<tr>
<td>Intel® Core™2 Duo processor T7600</td>
<td>$9.80</td>
<td>275</td>
</tr>
<tr>
<td>Intel® Core™2 Duo processor E6700</td>
<td>$13.94</td>
<td>333</td>
</tr>
</tbody>
</table>

SYSmark* 2004 SE Score
"PG&E is looking forward to working with Intel and its server providers to deliver programs that educate customers about energy efficiency, and to financially support their purchasing decisions."

Mark Bramfitt
High Tech Segment Supervisor

IBM* HS21
Dell* PowerEdge 2950
HP* ProLiant* BL460c

Other names and brands may be claimed as the property of others
Amazing products
... done efficiently
... with the industry
... for the customer
Can the 386 Architecture Keep Up?
John Hennessy and Pat Gelsinger Debate the Future of RISC vs. CISC

December 4, 1991
Evolution of the ISA

- **8086**
  - 16 bit ISA
  - Floating point

- **80886**
  - Expanded Addressability
  - Protected Mode
  - Virtual Memory

- **80386**
  - Full 32 bit architecture
  - Paging
  - SW debug

- **80386 SL**
  - Chipset integration
  - System Management Mode

- **80486**
  - Integrated FPU

- **Pentium**
  - VM86 model
  - Symmetric multiprocessing
  - Performance monitors

- **Pentium MMX**
  - Intel MMX™ SIMD instructions

- **Pentium II, III**
  - Power management
  - SSE

- **Pentium 4**
  - SSE2 & SSE3
  - Hyperthreading Technology
  - Extended Memory 64 Technology

Timeline:
- '80
- '85
- '90
- '95
Emerging Workloads

- High Performance Computing
- Media Processing
- Search & Compare
- String Processing
- Data Security
- Pattern Recognition for Large Data Sets
Announcing the Next Generation

Extending the World's Most Popular Processor Architecture

New innovations that improve the performance and energy efficiency of Intel® architecture
New Instructions

Vectorizing Compiler

Media

String and Text Processing

Application Targeted Accelerators
"Intel's instruction innovation offers Adobe the opportunity to reap significant performance improvements to our products. Adobe plans to utilize new instruction capabilities in our future products."

Bill Hensler  
Vice President  
Adobe Systems Incorporated

"Intel and Microsoft have a long history of working together on Instruction Set optimizations. We look forward to continuing our work to harness the benefits of these new instructions in future releases of Windows OS, tools and applications."

Amir Majidimehr  
Corporate Vice President  
Consumer Media Technologies  
Microsoft Corporation
Novell Introduces New Virtualization Solution Optimized for Intel Technology

Press Release
Novell and Intel collaborate to deliver first Xen-enabled Linux enterprise solution and drive virtualized Linux solution deployments

NEW YORK (InfoWorld Virtualization Executive Forum)—25 Sep 2006—Novell today announced the industry’s first enterprise Linux-based virtualization solution built on Xen®, optimized for Intel® Virtualization Technology. SUSE® Linux Enterprise Server 10 from Novell® running on Dual-Core Intel Xeon® platforms will provide customers with a low-cost, high-performing virtualization solution that has the ability to host Linux® environments without the need to modify the guest operating systems. With the integration of Intel Virtualization Technology within Xen, Novell also announced plans to offer enterprise support for virtualized SUSE Linux Enterprise Server 9 and Red Hat® Enterprise Linux 4 running on SUSE Linux Enterprise Server 10, allowing Red Hat customers to migrate to Novell service and support while still running Red Hat Enterprise Linux in a virtualized environment.

“Being first to provide Xen virtualization with enterprise Linux means our customers will be first to reap the benefits of running virtualized Linux platforms, including Red Hat, on SUSE Linux Enterprise Server 10,” said Jeff Jaffe, Novell executive vice president and chief technology officer. “This cross-platform approach to virtualization means both Novell and Red Hat customers will be able to take advantage of the cost and flexibility benefits of virtualization at a fraction of the cost of existing virtualization solutions.”

Kevin Nagle, general manager, Core Software Division, said, “Intel and Novell have worked extensively
Virtualize ASAP Program

Tech Resources for ISV Best Practices

Virtualization Deployment Guidance

Platform Consolidation Performance

Virtual Appliance Configurations
Ranjan Das
Senior Vice President
Emerging Solutions
SAP Americas
Looking Ahead

- Multi-Core and Many-Core
- Virtualization
- Manageability
- Security

intel + SAP = Innovation

Other names and brands may be claimed as the property of others.
Enabling Innovation – FSB Licensing

“Altera believes that FSB support, as demonstrated by Intel on Altera’s Stratix FPGA, can enable exciting acceleration for applications such as medical imaging, financial services, and data analytics.”

“Our continued alliance with Intel in support of next-generation interfaces, such as the Front Side Bus, will lead to new opportunities for Xilinx FPGA products and solutions to address and accelerate critical computing applications.”

Other names and brands may be claimed as the property of others.
PCI & PCIE Growth

100% of Graphics Suppliers
Fiber Channel Storage Products
SCSI/RAID
SAS
SATA
Dual and Multiport 1Gb
All 10Gb
Television Tuners
Accelerators
T.M.S. Bradicich, Ph.D.
IBM Fellow,
Chief Technology Officer,
System x / BladeCenter Servers
Growing Interest in Accelerators

Math-Intensive Transactions
Financial, scientific, economic models

Visualization and Media Processing
Graphics, video, speech

Embedded Content Processing
Data mining, encryption, compression, XML
As a leading provider in high performance networking, QLogic supports Intel in its pursuit to provide higher bandwidth and lower latency access to processor platforms in order to address the I/O interconnect requirements of the future.
Moore’s Law

In 1965, Intel co-founder Gordon Moore predicted that the number of transistors on a piece of silicon would double every couple of years—an insight later dubbed “Moore’s Law.” His prediction has held true, as ever-shrinking transistor sizes have allowed exponential growth in the number of transistors on a single chip.

Moore’s Law is now a basis for growth in the electronics industry, and Intel applies its principles like a business model. Whole new ways for people to play, learn, and work have come about as a result of Moore’s Law.

Amazing products
... done efficiently
... with the industry
... for the customer
Years of Research

Customer Concerns Go Beyond Price and Performance

- Security
  - $81M in losses reported in 2004 due to virus & DoS attacks\(^2\)
  - Time to exploit after fix availability rapidly shrinking\(^3\)
  - Over 15% of assets not discoverable down-the-wire due to removed agents, OS/power state\(^4\)
  - 5% of support incidents are desk side visits resulting in 52% of support costs

- TCO
  - Cutting hardware configurations in half can lower support costs by 10% to 55%\(^5\)
  - Additional OS version can increase TCO by $135 to $203 per PC/year\(^1\)

- Complexity
  - 90% of business process lost to lag time-task travel from one person to another\(^6\)

- Productivity & Connectivity

---

\(^1\) Source: 2003 IT total inventory of assets
\(^2\) Source: 2004 New Insights on PC Management: Benefits of Controlling PC Hardware Diversity
\(^3\) Source: 2005 HP NerveWire Study, "New Insights on PC Management: Benefits of Controlling PC Hardware Diversity"
Intel® vPro™ Technology-based Platform

vPro: The Next Generation of Business PCs

Fight Worms While You Sleep

Intel Secures the Desktop

VARs, MSPs Bank on Intel’s vPro To Reduce On-Site Service Calls

Intel’s Great Leap Forward

Intel vPro Promises IT Managers Less Needy Desktops

Other names and brands may be claimed as the property of others.
2006 Usages

Audit a Powered-down PC
Remotely Repair A Down System
Keep Software Agents Active
Securely Wake & Update Systems
Virtual Security and Management Appliances
Stunning Performance
Energy Efficiency
Todd Bradley
Executive Vice President
HP Personal Systems Group
“...adoption of Intel vPro technology will significantly improve (our) ability to cost-effectively, more securely, and efficiently manage our global fleet.”

BMW Group

“There’s a whole range of things that Symantec and Intel can accomplish given the new Intel vPro technology ...”

Symantec

“Capgemini is well positioned to take advantage of the remote management capabilities embedded in Intel® vPro™ technology. We believe that it will provide our customers with an exceptional user experience with less downtime, lower support costs, increased performance and a significant reduction in power consumption.”

Capgemini
Looking Ahead ... 2007

Go Mobile & Wireless

New Hardware Based Security

Additional Deployment Options

Support for Web Services Management Interface
Traditional Client Compute Models

**Thin Clients**
- Terminal/Server
- **Claims:**
  - Data security
  - TCO
  - Reduced complexity

**SaaS Enabled Client**

**Thick Client**
- Desktops/Notebooks
- **Claims:**
  - Increased Productivity
  - Rich User Experience
  - Mobility
SaaS Enabling Architecture

Client PCs

1. Authenticate with Service

2. Application & Data Stream

3. On Exit, Data Saved To Server

... with the industry

Adobe, Citrix, AppStream, Ardence, Softlicity

... for the customers

Credit Suisse, First Boston, Xerox, Merrill Lynch, Northeastern, Edison, Raytheon

Other names and brands may be claimed as the property of others.
David Wadhwani
Vice President
Product Development
Flex Product Line
Adobe Systems, Inc.
Rich Internet Applications with Apollo

Reaches Audiences Anytime, Anywhere

Simplifies Development & Deployment

Engaging Application Experiences
Improving the SaaS Experience

Intel and ISV Collaboration

Intel® vPro™ Technology

Trusted Execution Technology

Robson Technology & Application Pinning
SaasCon

SaaSCon
The Business to Business event for Software as a Service

SETTING THE AGENDA FOR AN INDUSTRY IN TRANSITION

SEPTEMBER 25-26, 2006
SAN FRANCISCO MARRIOTT IN SAN FRANCISCO, CA

Enhancing SaaS Through Platform Innovation

09/26/2006, 2:15 PM - 3:00 PM

Speaker:
Steve Grobman, Director, Strategic Planning - Digital Office Platform Division, Intel Corporation.

Software as a Service is changing the landscape of enterprise computing. Streaming OS and applications to clients promises to reduce total cost of ownership (TCO) of managing business clients. Through this change there are a number of challenges to insure secure and manageable products to address these challenges. This session will highlight the challenges of SaaS, overview Intel platform innovations and Intel® vPRO™ technology, and demonstrate how these advances overcome the challenges and deliver on the promise of improved security and manageability for reduced TCO.

Other names and brands may be claimed as the property of others.
Amazing products
... done efficiently
... for the customer
... with the industry
MobileMark* 2002 - 2005

EECoMark*

Amazing products... done efficiently

... for the customer

... with the industry
Amazing products
... done efficiently
... for the customer
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Amazing products
... done efficiently
... for the customer
... with the industry