## Revision History

<table>
<thead>
<tr>
<th>Date</th>
<th>Revision</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>November 2016</td>
<td>003</td>
<td>Updated block diagram, and CR2450 battery holder label on Power schematic.</td>
</tr>
<tr>
<td>February 2016</td>
<td>002</td>
<td>Updated for Fab D board.</td>
</tr>
<tr>
<td>December 2015</td>
<td>001</td>
<td>Initial release.</td>
</tr>
</tbody>
</table>
INTEL QUARK M CROCONTROLLER D2000 DEVELOPER BOARD (FAB D)

H79866- 204 PB
H79863- 400 PBA
H97185- 400 TA

INTEL CORPORATION
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TITLE PAGE
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<table>
<thead>
<tr>
<th>PAGE</th>
<th>SHEET NAME</th>
<th>PAGE</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>TITLE PAGE</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>DISCLAIMER</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>TABLE OF CONTENTS</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>BLOCK DIAGRAM</td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>INTEL QUARK D2000</td>
<td></td>
</tr>
<tr>
<td>6</td>
<td>HEADERS, ANALOG TERMINATION</td>
<td></td>
</tr>
<tr>
<td>7</td>
<td>SENSOR, USR DEFINED LED AND PUSHBUTTON</td>
<td></td>
</tr>
<tr>
<td>8</td>
<td>FTDI UART/JTAG SELECT</td>
<td></td>
</tr>
<tr>
<td>9</td>
<td>USB CONNECTOR, EEPROM</td>
<td></td>
</tr>
<tr>
<td>10</td>
<td>POWER</td>
<td></td>
</tr>
</tbody>
</table>
PSV IS NOT AVAILABLE WITH BATTERY OPERATION.
BMC150 I2C 7 BIT ADDRESS SELECT
USER DEFINED
PUSHBUTTON
J5 [1-2] USR (DEFAULT)
J5 [2-3] SCK

USER DEFINED LED

BMC150 I2C 7 BIT ADDRESS SELECT
SDO ACCELEROMETER MAGNETOMETER
0 0x10 0x12 (DEFAULT)
1 0x11 0x13

USER DEFINED LED

SW2
J9  JTAG (DEFAULT) UART
J10  JTAG (DEFAULT) UART
J11  JTAG (DEFAULT) UART

J15 AND J17 JUMPER IS INSTALLED FOR BOTH JTAG AND UART BY DEFAULT

REMOVE ALL SLEEVES FROM JUMPERS J9, J10, J11, J15, J17

D2000 FROM THE FTDI FT232HL TO ISOLATE THE INTEL QUARK DESIGN SHEET 8 OF 10
PLACE J26 JUMPER AT [1-2] (DEFAULT) WHEN THE USB PORT IS IN USE.

PLACE J27 JUMPER AT [1-2] (DEFAULT) WHEN THE USB PORT IS IN USE.