What if you could gain deeper insight to pursue new discoveries, drive business innovation or shape the future using advanced analytics? Breakthroughs in computational capability, data accessibility and algorithmic innovation are driving amazing new possibilities. One key to unlocking these deeper insights is the new Intel® Xeon Phi™ processor – Intel's first processor to deliver the performance of an accelerator with the benefits of a standard host CPU.

Designed to help solve your biggest challenges faster and with greater efficiency, the Intel® Xeon Phi™ processor enables machines to rapidly learn without being explicitly programmed, in addition to helping drive new breakthroughs using high performance modeling and simulation, visualization and data analytics. And beyond computation, as a foundational element of Intel® Scalable System Framework (Intel® SSF), the Intel® Xeon Phi™ processor is part of a solution that brings together key technologies for easy-to-deploy and high performance clusters.

**Solve Your Biggest Challenges Faster**

Designed from the ground up to eliminate bottlenecks, the Intel® Xeon Phi™ processor is Intel’s first bootable host processor specifically designed for highly parallel workloads, and the first to integrate both memory and fabric technologies. Featuring up to 72 powerful and efficient cores with ultra-wide vector capabilities (Intel® Advanced Vector Extensions or AVX-512), the Intel® Xeon Phi™ processor raises the bar for highly parallel computing. With no dependency on the PCIe bus, the versatile processor is capable of handling a wide variety of workloads and configurations that are not supported by accelerators. Integration of 16GB of high-bandwidth memory delivers up to 490 GB/s of sustained memory bandwidth for memory-bound workloads, and the available dual-port Intel® Omni-Path Fabric further reduces solution cost, power and space utilization.
As organizations undertake more complex and time-sensitive computational tasks, scalability becomes critical. The server-class Intel® Xeon Phi™ processor can scale rapidly like an Intel® Xeon® processor across large distributed systems. This capability is important for running high-performance workloads in the data center, especially analytics and machine learning, where scaling efficiency is critical for rapid training of complex neural networks. Moreover, as a foundational element of Intel® SSF, the Intel® Xeon Phi™ processor is part of a complete system architecture that makes it easy to deploy even higher performance clusters in your organization.

**Realize Compelling Value**

The Intel® Xeon Phi™ processor delivers compelling long-term value, enabling researchers, engineers and data scientists to solve the biggest challenges of our time with an architecture that provides the scalability and efficiency demanded by today’s IT managers. The higher degree of parallelism in the Intel® Xeon Phi™ processor results in greater compute per unit of energy consumed for highly-parallel applications.

The processor is binary-compatible with Intel® Xeon® processors, which allows it to run any x86 workload to optimize asset utilization across the data center. By contrast, special-purpose accelerators, like GPUs, often remain underutilized since applications are not always suitable or optimized for them. Standardizing on Intel® architecture means you can use a single programming model for your code, helping increase efficiency through a shared developer base and code reuse.

**Maximize Future Potential**

Investments in software can last a decade or longer, so as your high performance computing (HPC) needs grow—whether in research or business—you need a system architecture that offers great flexibility, the most reusability and broad ecosystem support. The Intel® Xeon Phi™ processor is a general purpose CPU built on open standards, making your software investment portable to many similar architectures into the future. A broad ecosystem of partners, programming languages and tools supporting the Intel® architecture to provide added confidence in your path forward.

And with the Intel® Xeon Phi™ processor as a foundational element of Intel® SSF, you have the support of a comprehensive technology roadmap for other key technologies, such as storage, connectivity and memory that are critical to your high performance systems.

Across a wide range of applications and environments – from machine learning to high-performance computing – you can now realize greater efficiency and scale by implementing a single, unified architecture in your data center that works for all your analytics needs.

The Intel® Xeon Phi™ processor is a true evolution in design and architecture that delivers the performance of an accelerator with the benefits of a standard CPU for your most demanding tasks. Unlock deeper insights to solve your biggest challenges faster with the Intel® Xeon Phi™ Processor, a foundational element of Intel® SSF.

**Intel® Xeon Phi™ processor, Your Path to Deeper Insight.**
Product Specifications
For complete information about the Intel® Xeon Phi™ processor, visit [http://ark.intel.com](http://ark.intel.com).

<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>7290[^*] Best Performance/Node</strong></td>
<td>72</td>
<td>1.5</td>
<td>16GB</td>
<td>Yes</td>
<td>384GB 2400 MHz</td>
<td>245W +15W fabric</td>
</tr>
<tr>
<td><strong>7250 Best Performance/Watt</strong></td>
<td>68</td>
<td>1.4</td>
<td>16GB</td>
<td>Yes</td>
<td>384GB 2400 MHz</td>
<td>215W +15W fabric</td>
</tr>
<tr>
<td><strong>7230 Best Memory Bandwidth/Core</strong></td>
<td>64</td>
<td>1.3</td>
<td>16GB</td>
<td>Yes</td>
<td>384GB 2400 MHz</td>
<td>215W +15W fabric</td>
</tr>
<tr>
<td><strong>7210 Best Value</strong></td>
<td>64</td>
<td>1.3</td>
<td>16GB 6.4 GT/s</td>
<td>Yes</td>
<td>384GB 2133 MHz</td>
<td>215W +15W fabric</td>
</tr>
</tbody>
</table>

Learn more: [www.intel.com/xeonphi](http://www.intel.com/xeonphi)

About Intel® Scalable System Framework (Intel® SSF)
Architected for HPC clusters to supercomputer-scale systems, Intel® SSF brings together the key technologies critical to achieve higher performance computing systems and makes them easier to deploy. These include:

- **Connectivity**: Intel® Xeon Phi™ processors now offer the ability to deploy Intel's next-generation fabric architecture, Intel® Omni-Path Architecture, as a standard, integrated option. The combination provides the foundation for powerful and efficient data traffic control by using an advanced "on-load" design that cost-effectively maximizes performance. Intel® Omni-Path Architecture automatically scales to tens of thousands of nodes with extremely low power consumption, making the solution ideal for increasingly demanding HPC and analytics workloads.

- **Storage**: Intel® Solutions for Lustre* software is a parallel file system purpose-built for HPC to provide fast, "massively scalable storage software" for accelerating performance on complex workloads[^4].

- **Memory**: MCDRAM is the integrated memory on Intel® Xeon Phi™ processors, providing up to 5x the bandwidth of DDR4 to deliver breakthrough performance for memory-bound workloads[^5].

- **Software**: Intel® HPC Orchestrator simplifies the complexity of installation, configuration and ongoing maintenance of implementing an HPC software stack.

Learn more: [www.intel.com/SSF](http://www.intel.com/SSF)
Note: Software and workloads used in performance tests may have been optimized for performance only on Intel microprocessors. Performance tests, such as SYSmark and MobileMark, are measured using specific computer systems, components, software, operations and functions. Any change to any of those factors may cause the results to vary. You should consult other information and performance tests to assist you in fully evaluating your contemplated purchases, including the performance of that product when combined with other products. Intel processors of the same SKU may vary in frequency or power as a result of natural variability in the production process. Performance claims in this document are based on Intel measured results as of April 2016. For more complete information visit: http://www.intel.com/performance

1 Intel® Xeon Phi™ processor delivers over 3 Teraflops of dual-precision peak theoretical performance, which is faster than the over 1 Teraflop of dual-precision peak theoretical performance for the Intel® Xeon Phi™ Coprocessor x100 Family

2 Frequency listed is nominal (non-AVX) TDP frequency. For all-tile turbo frequency, add 100 MHz. For single-tile turbo frequency, add 200 MHz. For high-AVX instruction frequency, subtract 200 MHz.

3 For more information, visit www.intel.com/lustre

4 Measured sustained memory bandwidth (STREAM) for integrated memory is >490GB/s, or 5.4X the measured sustained memory bandwidth of platform memory (DDR4) at ~90GB/s

INFORMATION IN THIS DOCUMENT IS PROVIDED IN CONNECTION WITH INTEL® PRODUCTS. NO LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE, TO ANY INTELLECTUAL PROPERTY RIGHTS IS GRANTED BY THIS DOCUMENT. EXCEPT AS PROVIDED IN INTEL'S TERMS AND CONDITIONS OF SALE FOR SUCH PRODUCTS, INTEL ASSUMES NO LIABILITY WHATSOEVER, AND INTEL DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY, RELATING TO SALE AND/OR USE OF INTEL PRODUCTS INCLUDING LIABILITY OR WARRANTIES RELATING TO FITNESS FOR A PARTICULAR PURPOSE, MERCHANTABILITY, OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT. UNLESS OTHERWISE AGREED IN WRITING BY INTEL, THE INTEL PRODUCTS ARE NOT DESIGNED NOR INTENDED FOR ANY APPLICATION IN WHICH THE FAILURE OF THE INTEL PRODUCT COULD CREATE A SITUATION WHERE PERSONAL INJURY OR DEATH MAY OCCUR.

Intel may make changes to specifications and product descriptions at any time, without notice. Designers must not rely on the absence or characteristics of any features or instructions marked “reserved” or “undefined.” Intel reserves these for future definition and shall have no responsibility whatsoever for conflicts or incompatibilities arising from future changes to them. The information here is subject to change without notice. Do not finalize a design with this information.

The products described in this document may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request. Contact your local Intel sales office or your distributor to obtain the latest specifications and before placing your product order. Copies of documents which have an order number and are referenced in this document, or other Intel literature, may be obtained by calling 1-800-548-4725, or by visiting Intel's Web site at www.intel.com.