Driver Support
Microsoft Windows 7*
Microsoft Windows Vista* (32 & 64 bit)
Microsoft Windows XP* (32 & 64 bit)
Microsoft Windows CE*
Microsoft Windows NT*
Microsoft Windows 2000 Server*
Microsoft Windows Server 2003* (32 & 64 bit)
SCO UnixWare* 7.x
OpenLinux* 8.0
Novell ODI*
Novell Netware*
SUSE Linux Enterprise Server* 9.0
Red Hat Enterprise Linux* 4.0
Linux*
FreeBSD*
Sun Solaris*
Microsoft IIS*
VMware ESX*

Intel® Advanced Networking Services
Teaming
Initiate a team with up to eight NICs.
Multi-Vendor Teaming
Allows teaming with third-party NICs and LOM solutions.
Adapter Fault Tolerance
Provides automatic redundancy for the server’s network connection should the primary adapter fail.
Transmit and Receive Load Balancing
Balances network traffic across teamed network connections.
802.1q VLAN Tagging
Allows teaming in multiple sub-networks by creating virtual adapters.
Link Aggregation
Supports Intel® Link Aggregation, Fast EtherChannel, Gigabit EtherChannel, and IEEE 802.3ad standards.
Continues to balance traffic even if one of the teamed connections loses link.

Intel® Ethernet Controllers and PHYs

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<table>
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<tr>
<th>Product</th>
<th>Codename</th>
<th>Similar</th>
<th>Package Type/ Pinout</th>
<th>Model</th>
<th>Performance Features</th>
<th>Target Applications</th>
<th>Management Features</th>
<th>Power (10G)</th>
<th>Power (3.3V)</th>
<th>Power (2.5V)</th>
<th>Power (1.05V)</th>
<th>Physical Package</th>
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<tr>
<td>Intel® 82567LM</td>
<td></td>
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<td></td>
<td>10/100/1000 Silicon</td>
<td>Memory and SERDES, embedded systems</td>
<td>16 Tx and 16 Rx queues, transmit and receive jumbo frames, support for Intel® VT-c (VMDq), transmit and receive checksum offloads, send and receive flow control, receive side scaling, complex programmable logic, and receive flow control</td>
<td>5.4 W (KX4 Dual Port)</td>
<td>3.3 W (KX4/KR Dual Port)</td>
<td>4.5 W (KX4/KR Dual Port)</td>
<td>1.4 W (82573E)</td>
<td>XAUI, KX/KX4, CX4, SGMII, 4.5 W (KX4 Dual Port)</td>
</tr>
<tr>
<td>Intel® 82577LM</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>10 GbE Silicon</td>
<td>Memory and SERDES, embedded systems</td>
<td>8 Tx and 8 Rx queues per port, receive side scaling, Message Signal Interrupt Extension (MSI-X), UDP and TCP Transmit Segmentation Offload (TSO), SCTP receive and transmit checksum offloads, stateless offloads</td>
<td>5.4 W (KX4 Dual Port)</td>
<td>3.3 W (KX4/KR Dual Port)</td>
<td>4.5 W (KX4/KR Dual Port)</td>
<td>1.3 W (D3 Cold, WoL enabled, 1000 Mb/s)</td>
<td>~470 mW (D3 cold, WoL enabled, 100 Mb/s, EEE enabled)</td>
</tr>
<tr>
<td>Intel® 82579LM</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Memory and SERDES, embedded systems</td>
<td>8 Tx and 8 Rx queues per port, receive side scaling, Message Signal Interrupt Extension (MSI-X), UDP and TCP Transmit Segmentation Offload (TSO), SCTP receive and transmit checksum offloads, stateless offloads</td>
<td>5.4 W (KX4 Dual Port)</td>
<td>3.3 W (KX4/KR Dual Port)</td>
<td>4.5 W (KX4/KR Dual Port)</td>
<td>1.3 W (D3 Cold, WoL enabled, 1000 Mb/s)</td>
<td>~612 mW 108 mW (D3 cold, WoL enabled, 1000 Mb/s, EEE Enabled)</td>
</tr>
<tr>
<td>Intel® 82574L</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Memory and SERDES, embedded systems</td>
<td>8 Tx and 8 Rx queues per port, receive side scaling, Message Signal Interrupt Extension (MSI-X), UDP and TCP Transmit Segmentation Offload (TSO), SCTP receive and transmit checksum offloads, stateless offloads</td>
<td>5.4 W (KX4 Dual Port)</td>
<td>3.3 W (KX4/KR Dual Port)</td>
<td>4.5 W (KX4/KR Dual Port)</td>
<td>1.3 W (D3 Cold, WoL enabled, 1000 Mb/s)</td>
<td>~790 mW (D3 cold, WoL enabled, 100 Mb/s)</td>
</tr>
<tr>
<td>Intel® I210-AT</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Memory and SERDES, embedded systems</td>
<td>8 Tx and 8 Rx queues per port, receive side scaling, Message Signal Interrupt Extension (MSI-X), UDP and TCP Transmit Segmentation Offload (TSO), SCTP receive and transmit checksum offloads, stateless offloads</td>
<td>5.4 W (KX4 Dual Port)</td>
<td>3.3 W (KX4/KR Dual Port)</td>
<td>4.5 W (KX4/KR Dual Port)</td>
<td>1.3 W (D3 Cold, WoL enabled, 1000 Mb/s)</td>
<td>~5.2 W (KX4/KR Dual Port)</td>
</tr>
</tbody>
</table>

*Intel products are not recommended for use in medical, life support, or mission-critical systems. *10/100/1000 with three supported operating systems. *-10°C to 85°C with three supported operating systems. *-10°C to 85°C with three supported operating systems.