In customer's design, please consult with HyperRAM vendor to decide if need to assemble this parallel resistor (R243) with specified HyperRAM design.
Arduino Power Output Capability
3.3V: J4.2, J4.4 100mA Max. total
5V: J4.5, J18.2 500mA Max. total
Using external adaptor power input (J12)
PMOD Specification not specified module power consumption but assumed no more than approximately 100mA.

2x20pin GPIO Header

Header 2x20 Shrouded
Cyclone 10 LP FPGA Evaluation Kit Board

DIP Switches

ON = 0
OFF = 1

Logic 0 = Virtual JTAG Bypass
Logic 1 = Virtual JTAG Enable

POWER LED

Configuration LED

User LED

Push Buttons

Intel Corporation, 301, Bibo Rd #888, Shanghai, China, 201203

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Use Clock GUI to Program Si5351A
Default Frequency:
CLK0: 125MHz
CLK1: 100MHz
CLK2: 50MHz
I2C Address: 0x60

50MHz Oscillator
5V Input Selection, Protection and Control

5V DC Input
ID=2.1mm, OD=5.5mm

5V DC Input Jack

USB_5V

DC Input Jack