NOTES:
1. Project Drawing Numbers:
   - Raw PCB
   - Fab Drawing
   - Schematic Drawing
   - PCB File
   - Bill of Materials
   - Functional Specification
   - PCB Layout Guidelines
   - Assembly Rework

Title, Notes, Block Diagram, Rev. History

Page: 1
Rev: 2
Date: 10/12/17
Pg. 4, 6
Description:
Delete comment which may confuse users

Title: Cyclone 10 LP FPGA Evaluation Kit Board
Size: B
Document Number: 150-0321321-A1
(6XX-44504R)

Power Diagram
Cyclone 10 Bank 1-4
Cyclone 10 Bank 5-8
Cyclone 10 Power
MAX10 - UBII-A
MAX10 - UBII-B
MAX10 - ADC
HyperRAM
Ethernet
Arduino Header
PMOD, 2x20 GPIO
LED, PB, DIP SW
Clock
Power Input
12V, 1.8V, 2.5V, 3.3V

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Cyclone 10 LP FPGA
U256 Package
14x14mm
In customer's design, please consult with HyperRAM vendor to decide if need to assemble this parallel resistor (R243) with specified HyperRAM design.

Two compatible parts:
- ISSI - IS66WVH16M8ALL-166B1LI
- Cypress - S70KS1281DPBHI020

Reserved For MCP/Flash

HBUS_CSn: For Flash (Reserved)
HBUS_C2n: For RAM

HyperRAM

HBUS_DQ[7:0]
HBUS_CKp
HBUS_CKn
HBUS_CS2n
HBUS_RSTn
HBUS_RWDS
HBUS_INTn
HBUS_RSTOn
HBUS_RSTn

VCC_1.8V

1.8V I/O HyperRAM (HyperBUS)
Reserved For MCP/Flash
Arduino Power Output Capability
3.3V: J4.2, J4.4 100mA Max. total
5V: J4.5, J18.2 500mA Max. total
Using external adaptor power input (J12)
PMOD Specification not specified module power consumption but assumed no more than approximately 100mA.

2x20pin GPIO Header

Header 2x20 Shrouded
ON = 0  Logic 0 = Virtual JTAG Bypass
OFF = 1  Logic 1 = Virtual JTAG Enable

- Configuration LED
- User LED
- DIP Switches
- Push Buttons

- POWER LED
- Configuration LED
- User LED
- DIP Switches
- Push Buttons

- Push Buttons
Use Clock GUI to Program Si5351A
Default Frequency:
CLK0: 125MHz
CLK1: 100MHz
CLK2: 50MHz
I2C Address: 0x60

50MHz Oscillator
5V Input Selection, Protection and Control

5V DC Input
ID=2.1mm, OD=5.5mm

5V Input Selection, Protection and Control