**Performance increases for the 510T compared to the Nallatech 385:**

» **16X Peak TFLOPS**  
Altera Arria 10 FPGAs include thousands of IEEE754-compliant floating-point DSP cores that deliver up to 2.8 TeraFLOPs, and provide best performance per computing Watt

» **3.6X More FPGA Resources**  
Increased FPGA fabric provides more room for additional accelerator functions

» **11X More Memory Bandwidth**  
290 GB/s Peak Aggregate Memory Bandwidth

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**Datacenter Co-Processing Power**  
**Super Fast & Energy Efficient**

» GPU Form Factor  
» (2) Arria 10 1150 GX FPGAs  
» Up to 290 GBytes/s Peak Aggregate Memory Bandwidth  
- 85GB/s Peak DDR4 Memory Bandwidth per FPGA (4 Banks per FPGA)  
- 30GB/s Write + 30GB/s Read Peak HMC Bandwidth per FPGA  
» Available w/ 2 GB Hybrid Memory Cube
OpenCL Programmable

Nallatech leads the industry with OpenCL™ programmable FPGA solutions, opening the performance of FPGA computing to Software Engineers.

OpenCL is the first open-source, unified C-based programming model for accelerating algorithms on heterogeneous CPU-GPU FPGA platforms.

The Altera SDK for OpenCL allows the easy implementation of applications onto FPGAs, providing a much faster and higher level software development flow. Learn more, visit: www.Altera.com/OpenCL

Several Nallatech FPGA Accelerators are available with OpenCL Board Support Packages (BSPs), optimized for Compute and Network acceleration. Learn more, visit: www.Nallatech.com/OpenCL

Turnkey Integrated Platforms Available

The Altera SDK for OpenCL allows the easy implementation of applications onto FPGAs, providing a much faster and higher level software development flow. Learn more, visit: www.Altera.com/OpenCL

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Technical specifications (e.g. FPGA type, size, external memory capacity etc.) can be modified to meet the exact needs of commercial customer applications as off the-shelf product available to the general market.