

Cyclone V Hard IP for PCI Express IP Core in the Altera Complete Design Suite Version 14.0

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The Cyclone[®] V Hard IP for PCI Express[®] and Avalon[®]-MM Cyclone V Hard IP for PCI Express have separate user guides. New in the Altera Complete Design Suite version 14.0 is the V-Series Avalon-MM DMA for PCI Express IP Core. This variant automatically includes high-performance DMA Read and DMA Write engines. Click on the link below for the appropriate user guide.

- [Cyclone V PCIe Avalon-ST Interface for PCIe Solutions User Guide](#)
- [Cyclone V PCIe Avalon-MM Interface for PCIe Solutions User Guide](#)
- [V-Series Avalon-MM DMA Interface for PCIe Solutions User Guide](#)

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