Designed to replace obsolete ASSP IC's, NDR created a Virtual ASSP Platform using Altera's MAX® 10 FPGAs to quickly prototype and extend the life cycle of electronic sub-systems. NDR configures the FPGA to the same function as the obsolete ASSP (license agreement or royalties are not required). The original equipment manufacturer (OEM) does not need any knowledge or design experience with FPGAs.

FPGAs provide customers benefits that ASSP's can't; some examples include:

- More hardware and embedded software customization options
- Bug fixes to the circuit, found during prototyping, can be made without PCB respin
- Multiple functions in a single FPGA (reducing cost, board space, R&D effort, as well as time to market)

Evaluate the Virtual ASSP functions with only a 5-volt power supply and connecting pin header. See Figure 1.

Figure 1. Evaluating Virtual ASSP Functions with a 5-volt Power Supply and Connecting Pin Header
The ADIS801MT01 is a Sync SPI Master requiring multichannel sensor sampling at simultaneous and fast speed.

The DC3A01MT01 is a single-package software-compatible combination of 3 discontinued IC’s.

Description:

Ordering Details

Description:

Contact Details

• Web: http://ndr.co.jp/en/virtual-assap
• Email: info-fpga@ndr.co.jp

Ordering Details

• FPGA board p/n: NDR-ADIS801MT01
• Evaluation board p/n: NDR-ADIS801MT01-KIT01

Ordering Details

• FPGA board p/n: NDR-DC3A01MT01
• Evaluation board p/n: NDR-DC3A01MT01-KIT02