
You must either have previously installed the Quartus® II software Arria 10 Edition Version 13.1 or must install the Quartus II software Arria 10 Edition Version 13.1 before installing this update. Otherwise, the update will not be installed correctly and the Quartus II software will not run properly.


Issues Addressed in Update 2

The Altera Complete Design Suite Arria 10 Edition Version 13.1 Update 2 addresses the following software issues:

**Quartus II Compilation Flows**

- Eliminates an internal error that could occur when an output buffer is fed by a constant ground (GND) or power (vcc) signal.
- Fixes Fitter errors that could occur when a wildcard placement location constraint matches internally generated instance names.
- Fixes an issue that causes compilation of a design using the ALTFP_MATRIX_MULT or ALTFP_MATRIX_INV megafunction to fail with the following error message:
  Error (272006): Cannot use ALTMULT_ADD wrapper: The ALTMULT_ADD is officially EOL for Arria 10 device family

**Simulation**

Issues Addressed in Update 1

The Altera Complete Design Suite Arria 10 Edition Version 13.1 Update 1 addresses the following software issues:

**Device Support**
- Enables final pinout for the following Arria 10 devices: 10AX115S3F45I2SGES and 10AX115S4F45I3SGES.
- Corrects pinout data for powering up some HSSI, LVDS, and external memory interface pins. Some HSSI pins might be powered up even though their corresponding HSSI channels are not used. This is a characteristic of Arria 10 devices.

**Chip Planner**
- Eliminates an internal error that could occur when you expand or select a timing path in the Chip Planner with the **Show Delay** option turned on.
- Eliminates an internal error that could occur when you open the Chip Planner or Change Manager window.

**Netlist Viewers**
- Eliminates an internal error that could occur in Netlist Viewers when you view a SystemVerilog design that includes a bidirectional pin.

**IP**

**Ethernet IP Cores**
- For the GIGE 1588 IP core, adds a missing port for `rx_std_bitslipboundarysel` to the preset, which is used by the 1588 logic to calculate latency. You should check all variant files generated that use the GIGE 1588 preset to ensure that this `rx_std_bitslipboundarysel` port is enabled.

**EMIF IP Cores**
- Eliminates an internal error that could occur during compilation of a design that targets a 10AS066 or 10AX066 device in which an external memory interface or PHYLite interface is placed in the hard processor system (HPS) column above the unbonded 3V tile.
Software Issues Resolved

The following Customer Service Requests were fixed or otherwise resolved in the Altera Complete Design Suite Arria 10 Edition Version 13.1 Update 2:

<table>
<thead>
<tr>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>11006402</td>
</tr>
</tbody>
</table>

No Customer Service Requests were fixed or otherwise resolved in the Altera Complete Design Suite Arria 10 Edition Version 13.1 Update 1.

Software Patches Included in this Release

No software patches released for previous versions of the Quartus II software version are included in the Altera Complete Design Suite Arria 10 Edition Version 13.1 Update 2 and Update 1.

Latest Known Altera Complete Design Suite Issues

<table>
<thead>
<tr>
<th>Description</th>
<th>Workaround</th>
</tr>
</thead>
<tbody>
<tr>
<td>In the Quartus II Software Arria 10 Edition Version 13.1 Update 2, the IP Upgrade table incorrectly displays that an update is available for many IP cores. After you update an affected core, the IP Upgrade table continues to display that an update is available.</td>
<td>No action is required. The continuous display indicating that an update is available affects only the GUI. Even though the IP Upgrade table continues to display that an update is available, the IP core will be at the most current version.</td>
</tr>
</tbody>
</table>

For more information about known software issues, look for information on the Quartus II Software Support page at the following URL:

http://www.altera.com/support/software/sof-quartus.html

You can find known issue information for previous versions of the Quartus II software on the Knowledge Database page at the following URL:

http://www.altera.com/support/kdb/kdb-index.jsp

Document Revision History

The following table shows the revision history for this document.

<table>
<thead>
<tr>
<th>Date</th>
<th>Version</th>
<th>Changes</th>
</tr>
</thead>
<tbody>
<tr>
<td>February 2014</td>
<td>Arria 10 Edition v13.1a10.2.0</td>
<td>Added Update 2 information</td>
</tr>
<tr>
<td>January 2014</td>
<td>Arria 10 Edition v13.1a10.1.0</td>
<td>Initial release</td>
</tr>
</tbody>
</table>