



Intel FPGA IP

Release Notes

Updated for Intel® Quartus® Prime Design Suite: **19.3**



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Latest document on the web: [PDF](#) | [HTML](#)



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1. About the Intel® FPGA IP Release Notes

This document is obsolete. For a complete list of all Intel FPGA IP release notes, refer to the *Intel FPGAs and Programmable Devices Release Notes* web page.

Related Information

- [Intel FPGAs and Programmable Devices Release Notes](#)
- [Introduction to Intel FPGA IP Cores](#)
- [Intel FPGA Software Installation and Licensing](#)
- [Intel Quartus Prime Pro Edition Software and Device Support Release Notes](#)
- [Intel Quartus Prime Standard Edition Software and Device Support Release Notes](#)

1.1. Errata

Errata are functional defects or errors, which may cause the product to deviate from published specifications. Documentation errata include errors, unclear descriptions, or omissions from current published specifications or product documents.

For full information on errata and the versions affected by errata, refer to the Knowledge Base on the Intel FPGA website.

Related Information

[Knowledge Base](#)