



F-Tile Auto-Negotiation and Link Training for Ethernet Intel® FPGA IP Release Notes

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1. Release Notes

The Intel® FPGA IP version (X.Y.Z) number can change with each Intel Quartus® Prime software version. A change in:

- X indicates a major revision of the IP. If you update the Intel Quartus Prime software, you must regenerate the IP.
- Y indicates the IP includes new features. Regenerate your IP to include these new features.
- Z indicates the IP includes minor changes. Regenerate your IP to include these changes.

Related Information

- [Intel Quartus Prime Design Suite Update Release Notes](#)
- [F-Tile Ethernet Intel FPGA Hard IP User Guide](#)
- [F-Tile Ethernet Intel FPGA Hard IP Design Example User Guide](#)

1.1. F-Tile Auto-Negotiation and Link Training for Ethernet Intel FPGA IP v3.0.0

Table 1. v3.0.0 2021.10.04

Intel Quartus Prime Version	Description	Impact
21.3	Added new parameter: Enable ECC protection on reset	When selected, enables ECC protection in Nios® II DRAM memory.
	Added multi-instance and multi-port auto-negotiation and link training support.	The Intel Quartus Prime software version 21.3 supports a single instance of the F-Tile Auto-Negotiation and Link Training for Ethernet Intel FPGA IP per FGT quad.
	Added VHDL file format support.	—

1.2. F-Tile Auto-Negotiation and Link Training for Ethernet Intel FPGA IP v2.0.0

Table 2. v2.0.0 2021.06.21

Intel Quartus Prime Version	Description	Impact
21.2	Initial release in the Intel FPGA IP library.	—