



# Shift Register (RAM-based) Intel® FPGA IP Release Notes



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## 1. Shift Register (RAM-based) Intel® FPGA IP Release Notes

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If a release note is not available for a specific IP version, the IP has no changes in that version. For information on IP update releases up to v18.1, refer to the *Intel® Quartus® Prime Design Suite Update Release Notes*.

Intel FPGA IP versions match the Intel Quartus Prime Design Suite software versions until v19.1. Starting in Intel Quartus Prime Design Suite software version 19.2, Intel FPGA IP has a new versioning scheme.

The Intel FPGA IP version (X.Y.Z) number can change with each Intel Quartus Prime software version. A change in:

- X indicates a major revision of the IP. If you update the Intel Quartus Prime software, you must regenerate the IP.
- Y indicates the IP includes new features. Regenerate your IP to include these new features.
- Z indicates the IP includes minor changes. Regenerate your IP to include these changes.

### Related Information

- [Intel Quartus Prime Design Suite Update Release Notes](#)
- [Intel Agilex™ Embedded Memory User Guide](#)
- [Intel Stratix® 10 Embedded Memory User Guide](#)
- [Errata for the Shift Register \(RAM-based\) Intel FPGA IP in the Knowledge Base](#)

### 1.1. Shift Register (RAM-based) Intel FPGA IP v19.1.0

**Table 1. v19.1.0 2020.12.14**

Intel Quartus Prime Version	Description	Impact
20.4	Initial release for Intel Agilex™ devices.	—

**Table 2. v19.1.0 2020.10.12**

Intel Quartus Prime Version	Description	Impact
20.3	Added "X" propagation support in simulation model for Intel Stratix® 10 devices.	—



## 1.2. Intel Agilex Embedded Memory User Guide Archives

If an IP core version is not listed, the user guide for the previous IP core version applies.

IP Core Version	User Guide
19.3	<a href="#">Intel Agilex Embedded Memory User Guide</a>

## 1.3. Intel Stratix 10 Embedded Memory User Guide Archives

If an IP core version is not listed, the user guide for the previous IP core version applies.

IP Core Version	User Guide
20.1	<a href="#">Intel Stratix 10 Embedded Memory User Guide</a>
19.1	<a href="#">Intel Stratix 10 Embedded Memory User Guide</a>
18.1	<a href="#">Intel Stratix 10 Embedded Memory User Guide</a>
18.0	<a href="#">Intel Stratix 10 Embedded Memory User Guide</a>
17.1	<a href="#">Intel Stratix 10 Embedded Memory User Guide</a>
17.0	<a href="#">Intel Stratix 10 Embedded Memory User Guide</a>