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1 Turbo IP Core Release Notes

If a release note is not available for a specific IP core version, the IP core has no changes in that version. Information on the latest update releases is in the Intel Quartus Prime Design Suite Update Release Notes.

Related Links
Intel Quartus Prime Design Suite Update Release Notes

1.1 Turbo IP Core v17.1
Table 1. v17.1 November 2017

<table>
<thead>
<tr>
<th>Description</th>
<th>Impact</th>
</tr>
</thead>
<tbody>
<tr>
<td>Added support for Intel® Cyclone® 10 devices</td>
<td>-</td>
</tr>
<tr>
<td>Added support for Intel Stratix® 10 devices</td>
<td>-</td>
</tr>
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</table>

Related Links
- Turbo IP Core User Guide
- Introduction to Altera IP Cores
- Errata for Turbo IP core in the Knowledge Base

1.2 Turbo IP Core v15.1
Table 2. v15.1 November 2015

<table>
<thead>
<tr>
<th>Description</th>
<th>Impact</th>
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</thead>
<tbody>
<tr>
<td>First release</td>
<td></td>
</tr>
</tbody>
</table>

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- Errata for Turbo IP core in the Knowledge Base

1.3 Turbo IP Core v16.1
Table 3. v16.1 November 2016

<table>
<thead>
<tr>
<th>Description</th>
<th>Impact</th>
</tr>
</thead>
<tbody>
<tr>
<td>Added 16 and 32 to Number of processors parameter. Output bit width varies depending on Number of processors</td>
<td>-</td>
</tr>
<tr>
<td>Added Number of LLRs per input parameter</td>
<td>-</td>
</tr>
</tbody>
</table>

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