

RLDRAM II Controller with UniPHY Intel® FPGA IP and RLDRAM 3 UniPHY Intel® FPGA IP Core Release Notes

2019.04.01

RN-1115



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If a release note is not available for a specific IP version, the IP has no changes in that version. For information on IP update releases up to v18.1, refer to the *Intel® Quartus® Prime Design Suite Update Release Notes*.

Related Information

[Intel Quartus Prime Design Suite Update Release Notes](#)

RLDRAM II Controller with UniPHY FPGA IP and RLDRAM 3 UniPHY FPGA IP Core v19.1

Table 1: v19.1 April 2019

Description	Impact
Verified in the Intel Quartus Prime software v19.1	—

Related Information

- [External Memory Interface Handbook](#)
- [Errata for RLDRAM II Controller with UniPHY and RLDRAM 3 PHY-Only IP in the Knowledge Base](#)

RLDRAM II Controller with UniPHY Intel FPGA IP and RLDRAM 3 UniPHY Intel FPGA IP Core v18.1

Table 2: v18.1 September 2018

Description	Impact
Verified in the Intel Quartus Prime software v18.1	—

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- [External Memory Interface Handbook](#)
- [Errata for RLDRAM II Controller with UniPHY and RLDRAM 3 PHY-Only IP in the Knowledge Base](#)

RLDRAM II Controller with UniPHY Intel FPGA IP and RLDRAM 3 UniPHY Intel FPGA IP Core v18.0

Table 3: v18.0 May 2018

Description	Impact
Verified in the Intel Quartus Prime software v18.0	—

Related Information

- [External Memory Interface Handbook](#)
- [Errata for RLDRAM II Controller with UniPHY and RLDRAM 3 PHY-Only IP in the Knowledge Base](#)

RLDRAM II Controller with UniPHY and RLDRAM 3 PHY-Only IP Core v17.1

Table 4: v17.1 November 2017

Description	Impact
Verified in the Intel Quartus Prime software v17.1.	—

Related Information

- [External Memory Interface Handbook](#)
- [Errata for RLDRAM II Controller with UniPHY and RLDRAM 3 PHY-Only IP in the Knowledge Base](#)

RLDRAM II Controller with UniPHY and RLDRAM 3 PHY-Only IP Core v17.0

Table 5: v17.0 May 2017

Description	Impact
Verified in the Quartus Prime software v17.0	—

Related Information

- [External Memory Interface Handbook](#)
- [Errata for RLDRAM II Controller with UniPHY and RLDRAM 3 PHY-Only IP in the Knowledge Base](#)

RLDRAM II Controller with UniPHY and RLDRAM 3 PHY-Only IP Core v16.1

Table 6: v16.1 November 2016

Description	Impact
Verified in the Quartus Prime software v16.1	—

Related Information

- [External Memory Interface Handbook](#)
- [Errata for RLDRAM II Controller with UniPHY and RLDRAM 3 PHY-Only IP in the Knowledge Base](#)