About These Release Notes

These release notes cover versions 8.1 through 9.1 SP2 of the Altera® Nios® II Embedded Design Suite (EDS). These release notes describe the revision history and errata for the Nios II EDS.

Errata are functional defects or errors, which might cause the product to deviate from published specifications. Documentation issues include errors, unclear descriptions, or omissions from current published specifications or product documents.

For the most up-to-date errata for this release, refer to the latest version of the Nios II Embedded Design Suite Release Notes and Errata on the Altera website. You can refer to release notes and errata for the Nios II processor core in the Nios II Processor chapter of the MegaCore IP Library Release Notes and Errata.

Revision History

Table 1 shows the revision history for the Nios II EDS, including the Nios II C2H Compiler.

Table 1. Nios II Embedded Design Suite Revision History

<table>
<thead>
<tr>
<th>Version</th>
<th>Date</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>9.1 SP2</td>
<td>March 2010</td>
<td>Maintenance release</td>
</tr>
<tr>
<td>9.1 SP1</td>
<td>January 2010</td>
<td>Maintenance release</td>
</tr>
<tr>
<td>9.1</td>
<td>November 2009</td>
<td>The Nios II Software Build Tools (SBT) for Eclipse, an integrated development environment based on Eclipse. Recommended for all new Nios II projects.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>The Nios II Integrated Development Environment (IDE) is available in the Nios II EDS, but not recommended for new projects.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>The Nios II Studio is discontinued.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>The vectored interrupt controller (VIC) core offers high-performance, low-latency interrupt handling.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Nios II Design Examples with memory management unit (MMU):</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Nios II 3C120 Hardware Reference Design with MMU, Cyclone® III Edition</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Nios II 4S230 Hardware Reference Design with MMU, Stratix® IV GX Edition</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Commercial and open-source support for embedded Linux is available for these systems.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>The Embedded Systems Lab for DE1 for Quartus® and Nios II EDS v9.1 is available for download from the Nios Community Wiki (<a href="http://www.nioswiki.com">www.nioswiki.com</a>).</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Tcl scripting for software packages and drivers enhanced to support callback functions.</td>
</tr>
<tr>
<td>9.0</td>
<td>March 2009</td>
<td>Added the nios2-convert-ide2sbt tool, which converts projects from the Nios II IDE development flow to the Nios II SBT development flow</td>
</tr>
<tr>
<td>8.1 SP1</td>
<td>January 2009</td>
<td>Nios Community Wiki URL changing (<a href="http://www.nioswiki.com">www.nioswiki.com</a>)</td>
</tr>
<tr>
<td>8.1</td>
<td>November 2008</td>
<td>Maintenance release</td>
</tr>
</tbody>
</table>
For more information on new features and changes, refer to the Nios II Processor Reference Handbook, the Nios II Software Developer’s Handbook, and the Nios II C2H Compiler User Guide. For information about changes to the Nios II processor core, refer to the Nios II Processor chapter of the MegaCore IP Library Release Notes and Errata.

Revision Details

This section provides details about some of the Nios II EDS revisions shown in Table 1.

Nios II Software Build Tools for Eclipse

The Nios II Software Build Tools (SBT) for Eclipse is a set of plugins based on the popular Eclipse framework and the Eclipse C/C++ development toolkit (CDT) plugins. The Nios II SBT for Eclipse provides a consistent development platform that works for all Nios II processor systems. You can accomplish most Nios II software development tasks within Eclipse, including creating, editing, building, running, debugging, and profiling programs. The Nios II SBT for Eclipse is built from the ground up as a set of plugins to standard Eclipse. The Nios II SBT for Eclipse is based on the Nios II Software Build Tools, a set of powerful commands, utilities and scripts for creating applications, board support packages and software libraries, and managing build options.

Table 2 compares the Nios II Integrated Development Environment (IDE) with the Nios II SBT for Eclipse.

Table 2. Differences Between Nios II SBT for Eclipse and Nios II IDE (Part 1 of 2)

<table>
<thead>
<tr>
<th>Feature</th>
<th>Nios II IDE</th>
<th>Nios II SBT for Eclipse</th>
</tr>
</thead>
<tbody>
<tr>
<td>Types of projects</td>
<td>Application</td>
<td>Application</td>
</tr>
<tr>
<td></td>
<td>Board support package (BSP)—Called a system library in the IDE</td>
<td>BSP</td>
</tr>
<tr>
<td></td>
<td>User Library (optional)</td>
<td>User Library (optional)</td>
</tr>
<tr>
<td>Handling updates to the SOPC Builder system</td>
<td>Updates the BSP automatically</td>
<td>You must initiate the BSP update (in the Nios II menu, click Generate BSP)</td>
</tr>
<tr>
<td>Options for creating projects</td>
<td>Application from template. BSP is created automatically as part of application.</td>
<td>New Application and BSP from template</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Standalone application</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Standalone BSP</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Standalone library</td>
</tr>
<tr>
<td>Handling updates to BSP</td>
<td>Entire BSP is rebuilt. This process can be time-consuming.</td>
<td>Smart rebuild only compiles changed files</td>
</tr>
<tr>
<td>Compatibility with command-line flow</td>
<td>None</td>
<td>Full compatibility with the Nios II SBT in the Nios II Command Shell</td>
</tr>
<tr>
<td>Modifying BSP properties</td>
<td>Limited control over BSP properties through the System Library page of the Properties dialog box.</td>
<td>Full control over BSP properties through the BSP Editor</td>
</tr>
</tbody>
</table>
Converting IDE Projects to the SBT for Eclipse

If you have a pre-existing Nios II IDE project, before you can continue development in the Nios II SBT for Eclipse, you must convert it to the SBT and import it to the Eclipse environment. To convert and import an IDE project to the Nios II SBT for Eclipse, execute the following steps:

1. Convert the IDE project to an SBT-based project with the `nios2-convert-ide2sbt` command line utility.
2. Import the converted project to the Nios II SBT for Eclipse.

For a detailed description of the conversion process, refer to “Porting Nios II IDE Projects to the Software Build Tools” in the Using the Nios II Integrated Development Environment appendix of the Nios II Software Developer’s Handbook. For a description of the process of importing a project, refer to “Importing a Command-Line Project” in the Getting Started with the Graphical User Interface chapter of the Nios II Software Developer’s Handbook.

Learning about the Nios II SBT for Eclipse

Refer to the following documents to help you get acquainted with the Nios II SBT for Eclipse and related tools:

- Getting Started with the Graphical User Interface chapter of the Nios II Software Developer’s Handbook
- Nios II Hardware Development Tutorial
- The Comprehensive Nios II Embedded Design Suite support page
- The Nios II Software Build Tools for Eclipse support page

Table 2. Differences Between Nios II SBT for Eclipse and Nios II IDE (Part 2 of 2)

<table>
<thead>
<tr>
<th>Feature</th>
<th>Nios II IDE</th>
<th>Nios II SBT for Eclipse</th>
</tr>
</thead>
<tbody>
<tr>
<td>Nios II C2H Compiler</td>
<td>Supported</td>
<td>Not supported</td>
</tr>
<tr>
<td>Nios II Flash Programmer</td>
<td>Limited control over the contents of the flash image</td>
<td>Allows you to create complex flash images containing Executable and Linking Format Files (.elf), SRAM Object Files (.sof), and binary data files in specified locations</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Allows you to save flash programmer settings for repeatable flash programming</td>
</tr>
<tr>
<td>Scripting and regression testing</td>
<td>Not Available</td>
<td>Project creation and build can be scripted using the SBT command line</td>
</tr>
</tbody>
</table>
Errata Summary

Table 3 summarizes the issues that affect the Nios II Embedded Design Suite versions 8.1 through 9.1 SP2. For a detailed description of each issue, click on the issue name.

Refer to the Altera Knowledge Database for older errata and solutions.

Table 3. Nios II EDS Errata (1) (Part 1 of 5)

<table>
<thead>
<tr>
<th>Added or Updated</th>
<th>Issue</th>
<th>Page</th>
<th>9.1</th>
<th>9.1</th>
<th>9.1</th>
<th>9.0</th>
<th>8.1</th>
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<td>SP2</td>
<td>SP1</td>
<td></td>
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<tr>
<td>May 2010</td>
<td>Errors Creating or Importing Software Projects</td>
<td>8</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td></td>
<td>Nios II Options Do Not Appear in Eclipse</td>
<td>9</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
<td>—</td>
<td>—</td>
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<tr>
<td>Feb 2010</td>
<td>Build Errors on Software for Pre-Existing Design with SG-DMA</td>
<td>9</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td></td>
<td>Spurious System ID Mismatch Error</td>
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<td>✓</td>
<td>✓</td>
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<td></td>
<td>Errors Debugging as Local C/C++ Application</td>
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<td>✓</td>
<td>✓</td>
<td>✓</td>
<td>—</td>
<td>—</td>
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<td>Nov 2009</td>
<td>Error Messages on Console When Debugging</td>
<td>10</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
<td>—</td>
<td>—</td>
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<tr>
<td></td>
<td>java.lang.NullPointerException Error When Running Application</td>
<td>11</td>
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<td>✓</td>
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<td></td>
<td>Update Failure When Setting Empty Properties in the Nios II Properties Page</td>
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<td>✓</td>
<td>✓</td>
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<tr>
<td></td>
<td>Errors Converting Nios II IDE Multiprocessor Project</td>
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<td>✓</td>
<td>✓</td>
<td>✓</td>
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<td>—</td>
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<tr>
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<td>DHCP Time-Out on EP3C120</td>
<td>12</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
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<td>—</td>
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<tr>
<td></td>
<td>No Nios II Multiprocessor Collection Run Configuration</td>
<td>12</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
<td>—</td>
<td>—</td>
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<tr>
<td></td>
<td>Stop on Startup Option in Run Configuration Has No Effect</td>
<td>12</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td></td>
<td>Project Imported From Command Line Fails to Build Correctly</td>
<td>13</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td></td>
<td>Debugger Breaks in crt0.s Instead of main()</td>
<td>13</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td></td>
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<tr>
<td>C2H Compiler</td>
<td>Error &quot;java.lang.NoClassDefFoundError&quot; During C2H Compilation</td>
<td>14</td>
<td>—</td>
<td>—</td>
<td>—</td>
<td>Fixed ✓</td>
<td></td>
</tr>
<tr>
<td></td>
<td>C2H Cache Coherency Issue with Global Variables</td>
<td>14</td>
<td>—</td>
<td>—</td>
<td>—</td>
<td>—</td>
<td>Fixed ✓</td>
</tr>
<tr>
<td>Nov 2009</td>
<td>Functions Declared Without a Return Type Are Not Supported</td>
<td>15</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Pre-7.1 Systems Are Not Supported</td>
<td>15</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
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<tr>
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<td>--src-dir SBT Argument Does Not Work With C2H</td>
<td>15</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
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<tr>
<td>Oct 2007</td>
<td>Accelerator Generation Failure If Tools Are Installed in Path With Spaces</td>
<td>15</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
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<tr>
<td>May 2007</td>
<td>The C2H Compiler Regenerates an Accelerator Unnecessarily</td>
<td>16</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
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<td>Error: c2h_accelerator_base_addresses.h: No such file or directory</td>
<td>16</td>
<td>✓</td>
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<td>✓</td>
<td>✓</td>
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<tr>
<td>May 2007</td>
<td>Java Heap Space Exception if Quartus II Compilation is Enabled</td>
<td>16</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
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<tr>
<td></td>
<td>Pointer Dereferences to Volatile Types</td>
<td>17</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
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<tr>
<td>Dec 2006</td>
<td>C2H Compiler Does Not Accelerate Subfunctions Located in a Separate File</td>
<td>17</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
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## Table 3. Nios II EDS Errata (1) (Part 2 of 5)

<table>
<thead>
<tr>
<th>Added or Updated</th>
<th>Issue</th>
<th>Page</th>
<th>Affected Version</th>
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</thead>
<tbody>
<tr>
<td>Jun 2006</td>
<td>Array Elements in Structures Do Not Copy Correctly</td>
<td>17</td>
<td>✔️ ✔️ ✔️ ✔️ ✔️</td>
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<td>Clean Build Causes Build Failure</td>
<td>18</td>
<td>✔️ ✔️ ✔️ ✔️ ✔️</td>
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<td>Changing Build Configurations Produces Unexpected Results</td>
<td>18</td>
<td>✔️ ✔️ ✔️ ✔️ ✔️</td>
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<td></td>
<td>Hardware Accelerators Remain After Deleting the Software Project (2)</td>
<td>19</td>
<td>✔️ ✔️ ✔️ ✔️ ✔️</td>
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<tr>
<td></td>
<td>Incorrect Results From Logical or Conditional Operation With Side-Effects</td>
<td>19</td>
<td>✔️ ✔️ ✔️ ✔️ ✔️</td>
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<td></td>
<td>Launch SOPC Builder Button in C2H View</td>
<td>20</td>
<td>✔️ ✔️ ✔️ ✔️ ✔️</td>
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<td></td>
<td>Development Boards</td>
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<td>Intermittent Failures While Accessing CompactFlash Card</td>
<td>20</td>
<td>✔️ ✔️ ✔️ ✔️ ✔️</td>
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<tr>
<td></td>
<td>Documentation Issues</td>
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<tr>
<td>May 2010</td>
<td>Incorrect Information about Nested Exceptions</td>
<td>21</td>
<td>✔️ ✔️ ✔️ ✔️ ✔️</td>
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<td>Feb 2010</td>
<td>Missing Documentation of Interrupt API Properties</td>
<td>22</td>
<td>✔️ ✔️ ✔️ ✔️ ✔️</td>
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<td>Nov 2009</td>
<td>Error Message After Renaming Project: “Resource is out of sync with the system”</td>
<td>23</td>
<td>✔️ ✔️ ✔️ ✔️ ✔️</td>
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<tr>
<td>Nov 2008</td>
<td>Obsolete Wiki URLs in Help Systems and Handbook</td>
<td>23</td>
<td>— — — Fixed ✔️</td>
</tr>
<tr>
<td>Nov 2008</td>
<td>Missing Source Control Information</td>
<td>24</td>
<td>— — — Fixed ✔️</td>
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<tr>
<td>Mar 2009</td>
<td>Compiler Flags for Building Custom Newlib</td>
<td>25</td>
<td>✔️ ✔️ ✔️ ✔️ ✔️</td>
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<td>Oct 2007</td>
<td>Nios II IDE Online Help Expand Buttons Do Not Work</td>
<td>27</td>
<td>✔️ ✔️ ✔️ ✔️ ✔️</td>
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<tr>
<td></td>
<td>Hardware Abstraction Layer</td>
<td></td>
<td></td>
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<tr>
<td>Feb 2010</td>
<td>Missing Structure Member Errors in alt_log_printf.c with Small JTAG UART Driver</td>
<td>27</td>
<td>✔️ ✔️ ✔️ ✔️ ✔️</td>
</tr>
<tr>
<td>Feb 2010</td>
<td>“unused variable” Warning When Building alt_main.c</td>
<td>28</td>
<td>✔️ ✔️ ✔️ ✔️ ✔️</td>
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<tr>
<td>Feb 2010</td>
<td>Build Warnings in Performance Counter Driver Files</td>
<td>28</td>
<td>✔️ ✔️ ✔️ ✔️ ✔️</td>
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<td>Hardware Example Designs</td>
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<td>Mar 2007</td>
<td>Incorrect PLL Phase Shift in Stratix II and Cyclone II Designs</td>
<td>29</td>
<td>✔️ ✔️ ✔️ ✔️ ✔️</td>
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<td>Software Examples</td>
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<td>Nov 2009</td>
<td>Hardware Tutorial Software Example Hangs on Some Boards</td>
<td>30</td>
<td>✔️ ✔️ ✔️ ✔️ ✔️</td>
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<td>Nov 2008</td>
<td>Simple Socket Server Example Displays Incorrect IP Address</td>
<td>30</td>
<td>✔️ ✔️ ✔️ ✔️ ✔️</td>
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<tr>
<td>Jun 2006</td>
<td>Networking Examples</td>
<td>31</td>
<td>✔️ ✔️ ✔️ ✔️ ✔️</td>
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<td></td>
<td>Flash Programmer</td>
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<tr>
<td>Feb 2010</td>
<td>Unable to Configure FPGA from Flash with Parallel Flash Loader</td>
<td>31</td>
<td>Fixed Fixed ✔️ ✔️ —</td>
</tr>
<tr>
<td>Feb 2010</td>
<td>Boot Failure or elf2flash Error Due to Incorrect Flash File Size</td>
<td>32</td>
<td>✔️ ✔️ ✔️ ✔️ ✔️</td>
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## Table 3. Nios II EDS Errata (1) (Part 3 of 5)

<table>
<thead>
<tr>
<th>Added or Updated</th>
<th>Issue</th>
<th>Page</th>
<th>9.1</th>
<th>9.1</th>
<th>9.1</th>
<th>9.0</th>
<th>8.1</th>
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<tr>
<td>May 2007</td>
<td>Delay When Creating New Flash Programmer Configuration</td>
<td>33</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
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<td>Dec 2006</td>
<td>elf2flash File Size Limit</td>
<td>33</td>
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<td>Error Message “No such file or directory” When Programming Flash</td>
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<td>✓</td>
<td>✓</td>
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<tr>
<td>May 2010</td>
<td>Vectored Interrupt Controller Does Not Support VHDL Simulation Models</td>
<td>33</td>
<td>✓</td>
<td>✓</td>
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<td>—</td>
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<td>Dec 2006</td>
<td>“UNC paths are not supported” Launching ModelSim</td>
<td>34</td>
<td>✓</td>
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<td>Uninitialized .bss Variables in Simulation</td>
<td>34</td>
<td>✓</td>
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<td>Cannot Launch Nios II SBT for Eclipse on Red Hat Linux</td>
<td>34</td>
<td>✓</td>
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<td>Windows/Cygwin: Nios II Processor Generation Failure</td>
<td>35</td>
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<td>Nios II IDE Command-Line Tools Select Wrong Workspace on Linux</td>
<td>36</td>
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<td>Nios II IDE Cannot Find stdio.h in Outline View</td>
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<td>Build Failure with Nios II Advanced Exceptions, MMU, and MPU</td>
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<td>Build Command Not Functional for BSPs Created With the Nios II SBT</td>
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<td>Linker Errors with Dual-Port Memories</td>
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<td>User-Managed BSP Settings Not Supported in Nios II IDE</td>
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<td>Compilation Error with Separate Exception Stack Option</td>
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<td>Incorrect Stack and Heap Free Space Report</td>
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<td>Nios II IDE Reports Problems Without Displaying Error in Console</td>
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<td>Nov 2009</td>
<td>Method for Accessing MMU and MPU Registers in the Debugger</td>
<td>41</td>
<td>✓</td>
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<td>IDE Cannot Display Imported Profiling Data</td>
<td>41</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
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<tr>
<td>Jul 2008</td>
<td>Error Message when Downloading .elf File</td>
<td>42</td>
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<td>Variable Casting Unsupported in ISS</td>
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<td>✓</td>
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<td></td>
<td>Trace Debug Does Not Support Instruction-Related Exceptions</td>
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<td>✓</td>
<td>✓</td>
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<td>Oct 2007</td>
<td>Trace Debug Does Not Support the JMPI Instruction</td>
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### Table 3. Nios II EDS Errata (1) (Part 4 of 5)

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<tr>
<td>May 2007</td>
<td>Cannot Locate Source Code in Driver Files Shared by Multiple Projects</td>
<td>43</td>
<td>✓ ✓ ✓ ✓ ✓ ✓</td>
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<td></td>
<td>Console Window Is Not Updated After ISS Error</td>
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<td>✓ ✓ ✓ ✓ ✓ ✓</td>
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<td></td>
<td>ISS Fails on Designs Containing Triple Speed Ethernet MAC or SG-DMA Components</td>
<td>43</td>
<td>✓ ✓ ✓ ✓ ✓ ✓</td>
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<td>Memory Window Sets Control Register Values Incorrectly</td>
<td>43</td>
<td>✓ ✓ ✓ ✓ ✓ ✓</td>
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<tr>
<td>Dec 2006</td>
<td>Programs That Interact With a Terminal Console on Windows Do Not Work</td>
<td>44</td>
<td>✓ ✓ ✓ ✓ ✓ ✓</td>
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<tr>
<td></td>
<td>“Run as ModelSim” in the Nios II IDE Fails</td>
<td>44</td>
<td>✓ ✓ ✓ ✓ ✓ ✓</td>
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<td>The Restart Command on the Run Menu Does Not Work</td>
<td>44</td>
<td>✓ ✓ ✓ ✓ ✓ ✓</td>
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<td>Jun 2006</td>
<td>Watchpoints Do Not Work on Certain Variables</td>
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### Navigating Projects

<table>
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<tr>
<td>Dec 2006</td>
<td>Nios II IDE Freezes While Displaying the Splash Screen</td>
<td>45</td>
<td>✓ ✓ ✓ ✓ ✓ ✓</td>
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<tr>
<td></td>
<td>Internal Error When Double-Clicking on a Large Objdump File</td>
<td>45</td>
<td>✓ ✓ ✓ ✓ ✓ ✓</td>
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<td>Jun 2006</td>
<td>C/C++ Scanner Does Not Support Certain C/C++ Constructs</td>
<td>46</td>
<td>✓ ✓ ✓ ✓ ✓ ✓</td>
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### Nios II SBT

<table>
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<th>Issue</th>
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<th>Affected Version</th>
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<tr>
<td>May 2010</td>
<td>BSP Editor Does Not Show Command-Line Help</td>
<td>46</td>
<td>✓ ✓ ✓ ✓ — —</td>
</tr>
<tr>
<td>Feb 2010</td>
<td>BSP Not Updated for Memory Size Changes in SOPC Builder</td>
<td>47</td>
<td>✓ ✓ ✓ ✓ — —</td>
</tr>
<tr>
<td>Nov 2009</td>
<td>Build Errors on Interrupt API Calls</td>
<td>47</td>
<td>✓ ✓ ✓ ✓ — —</td>
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<tr>
<td></td>
<td>BSP Editor Incorrectly Allows Mixed Drivers</td>
<td>48</td>
<td>Fixed Fixed Fixed ✓ ✓</td>
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<td>nios2-bsp --help Displays Incorrect List of BSP Types</td>
<td>48</td>
<td>Fixed Fixed Fixed ✓ ✓</td>
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<td></td>
<td>nios2-bsp-create-settings Fails to List BSP Types</td>
<td>49</td>
<td>— — — — — Fixed</td>
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<td>Makefile Error with Absolute Source Path</td>
<td>49</td>
<td>— — — — — Fixed</td>
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<td></td>
<td>Incorrect Linker Errors</td>
<td>50</td>
<td>— — — — — Fixed</td>
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<td></td>
<td>Java Heap Space Error When Creating BSP</td>
<td>50</td>
<td>— — — — — Fixed</td>
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<td>Oct 2007</td>
<td>SBT Fails if Nios II EDS is Installed in a Path Containing Spaces</td>
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<td>User-Managed BSP Settings Not Supported in Nios II IDE</td>
<td>39</td>
<td>✓ ✓ ✓ ✓ ✓ ✓</td>
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<td>Upper-Case File Extensions Not Supported</td>
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### Peripherals

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<td>May 2010</td>
<td>Vectored Interrupt Controller Does Not Support VHDL Simulation Models</td>
<td>33</td>
<td>✓ ✓ ✓ ✓ — —</td>
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<tr>
<td>Nov 2009</td>
<td>SBT Fails With UART Driver</td>
<td>51</td>
<td>— — — — — Fixed</td>
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<tr>
<td>Jul 2008</td>
<td>Unaligned Transfers of Small Payloads Fail on SG-DMA</td>
<td>52</td>
<td>✓ ✓ ✓ ✓ ✓ ✓</td>
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<tr>
<td>May 2007</td>
<td>DMA Controller Always Busy in Burst Mode</td>
<td>52</td>
<td>✓ ✓ ✓ ✓ ✓ ✓</td>
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</table>
### Nios II SBT for Eclipse Errata

This section describes in detail the issues related to the Nios II SBT for Eclipse.

#### Errors Creating or Importing Software Projects

If you place software projects in your Eclipse workspace directory, you might experience project-related errors. For example, you might see one of the following error messages:

- **Unable to create project**
  
  Reason: Unable to create project in workspace directory

- **Failed to import**
  
  Reason: Unable to import project

#### Workaround

Switch to a different workspace, or create a new workspace, separate from your project files. To switch workspaces or create a workspace, in the File menu, click **Switch Workspace**.

#### Solution Status

This issue will be fixed in a future release of the Nios II EDS.

---

### Table 3. Nios II EDS Errata (1) (Part 5 of 5)

<table>
<thead>
<tr>
<th>Added or Updated</th>
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<td>Nov 2009</td>
<td>NicheStack tcp_wake() Function Does Not Awaken All select() Sleepers</td>
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<td>EIC-Based Nios II System Crashes With NicheStack</td>
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<td>Mar 2009</td>
<td>Gigabit Ethernet Performance Issues</td>
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<td>Dec 2006</td>
<td>stdio Does Not Work with MicroC/OS-II and Small C Library</td>
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<td>Jun 2006</td>
<td>cout From MicroC/OS-II Task Does Not Send Data to stdout</td>
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<td>malloc(), realloc() Failures With MicroC/OS-II</td>
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<td>SP2</td>
<td>SP1</td>
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<th>9.1</th>
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<td>Feb 2010</td>
<td>Unable to Configure FPGA from Flash with Parallel Flash Loader</td>
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<td>✔</td>
<td>✔</td>
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<td>Nov 2009</td>
<td>sof2flash Names Output Files Incorrectly</td>
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<td>—</td>
<td>—</td>
<td>—</td>
<td></td>
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<td>GNU Assembler Does Not Accept the --defsym Flag</td>
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<td>✔</td>
<td>✔</td>
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</table>

**Notes to Table 3:**

1. Refer to the Altera Knowledge Database for older errata and solutions.
2. For further information, refer to the *Nios II C2H Compiler User Guide*.
Nios II Options Do Not Appear in Eclipse

When Nios II SBT for Eclipse starts, the Nios II plugins might fail to load, resulting in the following symptoms:

- The Nios II perspective is not available
- No Nios II items appear when you click New in the File menu

Eclipse can behave this way if you have installed the Nios II EDS in the same directory as a previous installation.

When the Altera Complete Design Suite (ACDS) installer installs the Nios II EDS, you specify a root directory for the EDS file structure. For example, if the Nios II EDS 9.1 is installed on the Windows operating system, the root directory might be `c:\altera\91\nios2eds`. For simplicity, Altera documentation refers to this directory as `<Nios II EDS install path>`.

When the ACDS installer removes the Nios II EDS, it leaves behind some files in `<Nios II EDS install path>`. If you reinstall the Nios II EDS in the same directory, these leftover files might cause the Nios II SBT for Eclipse to work incorrectly.

**Workaround**

To correct this problem, execute the following steps:

1. Uninstall the Nios II EDS.
2. Delete `<Nios II EDS install path>`, including all subdirectories, before reinstalling the Nios II EDS.
   Alternatively, reinstall the Nios II EDS in a different directory.

**Solution Status**

This issue will be fixed in a future release of the Nios II EDS.

Build Errors on Software for Pre-Existing Design with SG-DMA

If you have a Nios II system generated with SOPC Builder v. 9.0 or earlier, and it contains the Scatter-Gather DMA (SG-DMA) component, you cannot build software for it with the Nios II SBT for Eclipse v. 9.1 or later. If you attempt to do so, the Nios II compiler reports errors.

**Workaround**

Regenerate the design with SOPC Builder v. 9.1 or later before attempting to compile the software in the Nios II SBT for Eclipse.

**Solution Status**

Not fixed.

Spurious System ID Mismatch Error

You might see the following error message when attempting to run or debug a Nios II software project, or to edit a launch configuration:

```
[Target Connection]:System ID mismatch - actual: "0xffffffff", expected: "<system id>"
```
<system id> is replaced by the system ID in your software project. When this error occurs, you cannot run or debug software.

This error normally means that the FPGA is unconfigured, or configured with a .sof file that does not correspond to your software project. However, the error occasionally appears even when the FPGA is configured with the correct .sof.

**Workaround**

After verifying that the FPGA is configured with the correct .sof, you can use one of the following methods to work around this issue:

- In the Run Configuration or Debug Configuration dialog box, on the Target Connection tab, click Refresh Connections repeatedly until the error message disappears.
- Delete the launch configuration and create a new launch configuration.

**Solution Status**

This issue will be fixed in a future release of the Nios II EDS.

---

**Errors Debugging as Local C/C++ Application**

If you try to debug a Nios II software project as the Local C/C++ Application launch configuration type, you see one of the following errors, depending on your platform:

- Windows: “Error creating session”
- Linux: “Execution is suspended because of error”

The Nios II debug perspective fails to open.

This is expected CDT behavior in the Eclipse platform. Local C/C++ Application is the launch configuration type for a standard CDT project. To invoke the Nios II plugins, you must use a Nios II launch configuration type.

**Workaround**

Always debug Nios II software projects as the Nios II Hardware launch configuration type.

---

**Error Messages on Console When Debugging**

When debugging, you might see the following messages in the Nios II SBT for Eclipse console:

- No symbol "auto" in current context.
- No symbol "new" in current context.

These are normal messages from the GNU Debugger (GDB) console, not error messages. This issue does not impact the debug process.

**Workaround**

No workaround needed.
Solution Status
None needed.

java.lang.NullPointerException Error When Running Application

When you attempt to run or debug an application, the Nios II SBT for Eclipse might show an internal error message similar to the following:

Launching New_configuration java.lang.NullPointerException

This error occurs in either of the following circumstances:

- You attempt to run your project without rebuilding after performing a make clean.
- You delete a run configuration belonging to your project while the project is running.

You must restart Nios II SBT for Eclipse to recover from this error.

Workaround
Build the project before attempting to run or debug it. Do not delete a run configuration while it is in use.

Solution Status
This issue will be fixed in a future release of the Nios II EDS.

Update Failure When Setting Empty Properties in the Nios II Properties Page

If you use the Nios II Properties page to update more than one board support package (BSP) setting to the empty string (""), the BSP is not updated.

Workaround
You can use either of the following workarounds to set multiple BSP settings to the empty string:

- Use the keyword none instead of "" to set settings to the empty string.
- Use the BSP Editor instead of the Nios II Properties page.

Solution Status
This issue will be fixed in a future release of the Nios II EDS.

Errors Converting Nios II IDE Multiprocessor Project

If you try use the nios2-convert-ide2sbt utility to convert an IDE multiprocessor project to the Nios II SBT, error messages appear, and the project fails to generate properly.

Workaround
Create new Nios II SBT application and BSP projects using project settings equivalent to the original Nios II IDE project settings.
Solution Status
This issue will be fixed in a future release of the Nios II EDS.

DHCP Time-Out on EP3C120
If you create a project based on the web server template targeting the Altera Embedded Systems Development Kit, Cyclone® III Edition (EP3C120) board, the system is unable to get an IP address at startup. DHCP times out and the system reverts to the default IP address.

Workaround
Create the project in the Nios II Command Shell and then import it to the Nios II SBT for Eclipse, by executing the following steps:

1. In the Nios II Command Shell, navigate to the following path:
   `<Nios II EDS install path>/examples/<language>/niosII_cycloneIII_3c120/tserd/software_examples/app/web_server_tse_3c120_rgmii`
   where `<language>` is either vhdl or verilog.

2. Type the following command to create a web server example for the EP3C120 kit:
   `create-this-app`

3. Import the command-line project to the Nios II SBT for Eclipse.
   
   For information about importing command-line projects, refer to “Importing a Command-Line Project” in the Getting Started with the Graphical User Interface chapter of the Nios II Software Developer’s Handbook.

Solution Status
This issue will be fixed in a future release of the Nios II EDS.

No Nios II Multiprocessor Collection Run Configuration
The multiprocessor collection run configuration is not available in the Run menu. Therefore, you cannot use a single run configuration to download .elf files to run on multiple Nios II processors in the Nios II SBT for Eclipse.

Workaround
Create a separate run configuration for each Nios II processor in the hardware design, and launch them one at a time.

Solution Status
Not fixed

Stop on Startup Option in Run Configuration Has No Effect
Turning on Stop on Startup at in the Debugger tab of a run configuration has no effect.
Workaround
You can manually set any breakpoint in the debug perspective.

Solution Status
This issue will be fixed in a future release of the Nios II EDS.

Project Imported From Command Line Fails to Build Correctly
If you try to import a command-line project to the Nios II SBT for Eclipse, and your project has source files located outside the project folder, the Nios II SBT for Eclipse fails to build your project because the makefile does not include source files located outside the project folder.

Workaround
You can work around this issue with either of the following methods:

- When importing the project, disable source management through the Nios II Application Properties or Nios II Library Properties dialog box.
  
    You must manually keep your makefile updated to reflect source files added to or removed from the project.

- Manually link the affected source files to the project. Right-click the project, click Properties, expand C/C++ General, select Paths and Symbols and then Source Location, and click Link Folder.

Solution Status
This issue will be fixed in future release of the Nios II EDS.

Debugger Breaks in crt0.s Instead of main()
If you attempt to launch a debug session by right clicking the project, without first creating a run configuration, the debugger breaks in the initialization code in crt0.s, rather than at the top of the main() function. You must explicitly create the run configuration before launching the debug session.

Workaround
You can work around this issue with either of the following methods:

- Create the run configuration with the Run Configuration dialog box. You can then debug with this run configuration by right-clicking the project, clicking Debug As, and selecting the debug configuration. The program correctly breaks at main().

- Manually set a breakpoint at main().

Solution Status
This issue will be fixed in a future release of the Nios II EDS.
C2H Compiler Errata

This section describes in detail the issues related to the Nios II C2H Compiler.

Error "java.lang.NoClassDefFoundError" During C2H Compilation

C2H compilation fails with the following error:

```
java.lang.NoClassDefFoundError: com/altera/c2h/C2Hgenerate
Exception in thread "main" make: *** [c2h_hdl-t] Error 1
```

This error occurs when you invoke C2H from the Nios II IDE on the Windows operating system. This problem does not affect Linux installations, nor C2H compilation from the Nios II command shell.

**Workaround**

To correct the problem, visit mySupport and request the Nios II EDS 8.1 Patch 0.01 (PC version).

**Solution Status**

Fixed in v. 9.0

C2H Cache Coherency Issue with Global Variables

Cache coherency issues might be encountered when the following conditions are all true:

- A C2H-accelerated function writes to a global or static variable
- The Nios II processor is configured with a data cache
- The referenced global variable is located in cacheable memory

In this situation, the data in the global variable can be corrupted.

This issue can only arise if the accelerated function writes to the global or static variable. If the function merely reads from the variable, cache coherency is maintained correctly.

**Workaround**

Add a data cache flush operation in the accelerator's wrapper function. The wrapper can be found in `alt_c2h_<function_name>.c`. In this file, find the following comment:

```c
/* Write 1 to address 0 starts the accelerator */
```

Directly after this comment, add the following function call:

```c
alt_dcache_flush_all();
```

This function call ensures that the data cache is flushed immediately before the accelerator is started.

Flushing the cache entails a performance penalty.

Alternatively, allocate your global and static variables in non-cacheable regions of memory. Placing these variables in non-cacheable regions eliminates any need to perform a data cache flush.
For information about allocating variables to non-cacheable regions, refer to the *Cache and Tightly-Coupled Memory* chapter of the *Nios II Software Developer’s Handbook*.

**Solution Status**
Fixed in v. 8.1

**Functions Declared Without a Return Type Are Not Supported**

The C2H compiler does not support functions without an explicitly declared return type.

**Workaround**
If you are using the implicit *int* return type, declare the return type explicitly. If your function has no return value, declare it as *void*.

**Solution Status**
Not fixed

**Pre-7.1 Systems Are Not Supported**

Starting in v. 7.1, the C2H Compiler does not support SOPC Builder systems created in SOPC Builder 7.0 or earlier (systems based on a *class.ptf* file).

**Workaround**
Before using the C2H Compiler, you must upgrade your SOPC Builder system. Open the system in SOPC Builder v. 7.1 or later. SOPC Builder prompts you to convert the system, creating a *_hw.tcl* file compatible with the C2H Compiler.

**Solution Status**
Not fixed

**--src-dir SBT Argument Does Not Work With C2H**

When using the C2H Compiler with Nios II SBT, an error occurs when using the *nios2-app-generate-makefile* argument *--src-dir* to specify a directory of source files. An example of the error is:

```
multiple definition of 'my_accelerated_function'
```

**Workaround**
Specify source files individually using the *--src-files* argument.

**Solution Status**
Not fixed

**Accelerator Generation Failure If Tools Are Installed in Path With Spaces**

If the path to your installation of the Altera Design Suite contains spaces, the C2H Compiler fails to generate the accelerators.
Workaround
Reinstall the tools to a path containing no spaces.

Solution Status
This issue will be fixed in a future release of the Nios II EDS.

The C2H Compiler Regenerates an Accelerator Unnecessarily
The C2H Compiler might regenerate an accelerator even when the accelerated function is unchanged. This problem can result from changes to files included by the C file containing the accelerated function. The C2H Compiler fails to check that the generated hardware description language (HDL) matches the previously generated HDL causing the system to be regenerated.

Workaround
To avoid this issue move the accelerator-specific information from the include file to a separate include file. This workaround prevents regeneration of the system when the HDL is unchanged.

Solution Status
Not fixed

Error: c2h_accelerator_base_addresses.h: No such file or directory
When a C2H accelerator is compiled for the first time, the following compile-time error can result if the Analyze all accelerators option is selected:
c2h_accelerator_base_addresses.h: No such file or directory.

Workaround
Click Build software and generate SOPC Builder system and build once before building with the Analyze all accelerators option.

Solution Status
This issue will be fixed in a future release of the Nios II EDS.

Java Heap Space Exception if Quartus II Compilation is Enabled
If your design contains a C2H accelerator, and you select Build software, generate SOPC Builder system, and run Quartus® II compilation in the C2H view, you might see the following error during Quartus II compilation:

Exception in thread "main" java.lang.OutOfMemoryError: Java heap space
make: *** [c2h_hdl-t] Error 1

Workaround
Select the Build software and generate SOPC builder system option in the C2H Compiler settings window, and then manually launch the Quartus II software to compile the design.
Solution Status
Not fixed

Pointer Dereferences to Volatile Types
The C2H Compiler treats pointer dereferences to a volatile type as if they alias all other pointer dereferences. Pointers that are restrict-qualified are treated the same way.

The two loops in Example 1 cannot be scheduled concurrently because the volatile qualification overrides the __restrict__ pragma.

Example 1. Non-Concurrent Loops

```c
volatile int * __restrict__ fifo_rd = FIFO_RD_BASE;
volatile int * __restrict__ fifo_wr = FIFO_WR_BASE;
for () {
    *fifo_wr = ....;
} 
for () {
    ... = *fifo_rd;
}
```

Workaround
Divide the function into multiple interrupt request (IRQ)-enabled accelerators that are launched concurrently from the processor, and use FIFO buffers to communicate between them.

Solution Status
Not fixed

C2H Compiler Does Not Accelerate Subfunctions Located in a Separate File
When accelerating a function in a file, the C2H Compiler cannot link subfunctions that are defined in a different file.

Workaround
Include all subfunctions called by the accelerated function within the same source code file.

Solution Status
Not fixed

Array Elements in Structures Do Not Copy Correctly
C2H accelerators do not correctly copy array elements that are elements of structures.

In Example 2, the a and b elements of the structure copy correctly, but the buf element does not. After this assignment, struct_a equals {9, 8, {3, 3, 3, 3}}.
Example 2. Array Elements of Structs

typedef struct my_struct {
    int a;
    int b;
    int buf[BUF_SIZE];
}MY_STRUCT;
MY_STRUCT struct_a = {1, 2, {3, 3, 3, 3}};
MY_STRUCT struct_b = {9, 8, {7, 7, 7, 7}};
struct_a = struct_b;

Workaround
Copy the array elements explicitly, as shown in Example 3.

Example 3. Copying Array Elements Explicitly

{  
    int i=0;
    do  
        {  
            struct_a.buf[i] = struct_b.buf[i];
            i++;
        } while (i<LENGTH_OF_BUF_ELEMENT)
}

Solution Status
This issue will be fixed in a future release of the Nios II EDS.

Clean Build Causes Build Failure

Performing a clean build on a Nios II IDE project that contains a hardware accelerator can cause the next build to fail in the IDE, because the clean build erroneously deletes a file required by the C2H Compiler.

Workaround
Do not perform a clean build on projects that use hardware accelerators. If you have already performed a clean build, recompile with option Build software, generate SOPC Builder system, and run Quartus II compilation to regenerate the necessary files.

Solution Status
This issue will be fixed in a future release of the Nios II EDS.

Changing Build Configurations Produces Unexpected Results

The C2H Compiler does not support multiple build configurations (for example Release or Debug) in the Nios II IDE. After creating one or more accelerators in a particular configuration, the C2H Compiler produces undefined results if you switch to a different build configurations and create more accelerators.
Workaround
For a specific SOPC Builder system and Nios II IDE project, specify C2H accelerators in only one build configuration. You can use multiple build configurations, as long as only one configuration specifies C2H Compiler settings.

Solution Status
Not fixed

Hardware Accelerators Remain After Deleting the Software Project
If a system contains C2H accelerators, deleting the software project that defines the accelerators does not remove the accelerators from the hardware system, and the accelerator logic remains in the SOPC Builder system.

Workaround
To remove an accelerator from a system, delete the accelerator from the C2H view in the Nios II IDE first, and then recompile the software project. The C2H Compiler then removes the accelerator from the SOPC Builder system. Once the compilation is complete then the software application can be deleted from the workspace.

Solution Status
Not fixed

Incorrect Results From Logical or Conditional Operation With Side-Effects
The C2H Compiler always evaluates both operands of logical (&&, ||) and conditional (?:) operators. This is different from expected American National Standards Institute (ANSI) C behavior, where operands are evaluated left-to-right, and unnecessary operands are skipped.

For example, in the expression (i-- && j--), if the value of i is zero, ANSI C does not evaluate the right-hand-side (RHS) expression, and j is not decremented. By contrast, the C2H Compiler evaluates both sides, decrementing j.

The following expressions are other examples that might be affected by this issue:

```c
if (i-- || j++)
    ...
```

```c
a = ((cond == 1)? i++ : j++);
```

Workaround
Use logical and conditional operations whose operators have no side effects. Operations with side effects include pre- and post- increment and decrement operations (++--, --), memory operations (*, [], ., ->), and function calls.

Solution Status
Not fixed
Launch SOPC Builder Button in C2H View

When the Nios II IDE workspace contains multiple projects with multiple system libraries, the incorrect SOPC Builder system might open when you click Launch SOPC Builder in the C2H view.

Workaround
Launch SOPC Builder from the Quartus II software. Alternatively, keep only one system library project open at a time while using the C2H Compiler.

Solution Status
Not fixed

Development Board Errata

This section describes in detail the Nios II EDS issues related to Nios development boards.

Intermittent Failures While Accessing CompactFlash Card

The Nios II Development Kit version 5.0 and later includes a CompactFlash controller peripheral suitable for interfacing to CompactFlash cards in True IDE mode on Nios development boards. For True IDE mode to operate, CompactFlash cards require that the ATASEL_N input be driven to ground during power-up.

The CompactFlash controller peripheral includes a configurable power register, used to cycle power to CompactFlash cards from Nios II software through a metal oxide semiconductor field-effect transistor (MOSFET) on the Nios development boards. However, in certain development boards, power to the CompactFlash card does not turn off completely during this power cycle operation. Because of this condition, the CompactFlash might not sample the ATASEL_N pin during the power-cycle operation after FPGA configuration when this pin is driven to ground. Instead, the CompactFlash card might sample the ATASEL_N pin when power is first applied to the development board, when I/O is not yet driven by the FPGA (before FPGA configuration).

Workaround
If you encounter errors with CompactFlash when using the Nios development boards, try one of the following solutions:

- Use a different CompactFlash card. Certain cards are more susceptible to the power-cycling issue than others.
- Modify the Nios development board. This is recommended if you are familiar and comfortable with board-level modifications. Disconnect pin 9 (ATASEL_N) on the CompactFlash socket on your Nios development board and tie this pin to ground.

The CompactFlash socket uses a staggered numbering on the pins (starting from pin 1: 1, 26, 2, 27, ...); refer to the CompactFlash Association specification for right-angle surface-mount connectors for exact specifications on this connector. This modification permanently enables True IDE mode operation.
Solution Status
Not fixed

Documentation Errata
This section describes in detail the Nios II EDS documentation issues.

Incorrect Information about Nested Exceptions
“Exception Processing” in the Programming Model chapter of the Nios II Processor Reference Handbook incorrectly states that multiple interrupts with different requested interrupt levels (RILs) must not be assigned to the same shadow register set.

Workaround
The correct information is as follows:
Multiple interrupts with different RILs can be assigned to a single shadow register set. However, with multiple register sets, you must not allow the RILs assigned to one shadow register set to overlap the RILs assigned to another register set.
Table 2–4 and Table 2–5 illustrate the validity of register set assignments when preemption within a register set is enabled.

Table 2–4. Example of Illegal RIL Assignment

<table>
<thead>
<tr>
<th>RIL</th>
<th>Register Set 1</th>
<th>Register Set 2</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>IRQ0</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>IRQ1</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td></td>
<td>IRQ2</td>
</tr>
<tr>
<td>4</td>
<td>IRQ3</td>
<td></td>
</tr>
<tr>
<td>5</td>
<td></td>
<td>IRQ4</td>
</tr>
<tr>
<td>6</td>
<td></td>
<td>IRQ5</td>
</tr>
<tr>
<td>7</td>
<td></td>
<td>IRQ6</td>
</tr>
</tbody>
</table>

Table 2–5. Example of Legal RIL Assignment

<table>
<thead>
<tr>
<th>RIL</th>
<th>Register Set 1</th>
<th>Register Set 2</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>IRQ0</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>IRQ1</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>IRQ3</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td></td>
<td>IRQ2</td>
</tr>
<tr>
<td>5</td>
<td></td>
<td>IRQ4</td>
</tr>
<tr>
<td>6</td>
<td></td>
<td>IRQ5</td>
</tr>
<tr>
<td>7</td>
<td></td>
<td>IRQ6</td>
</tr>
</tbody>
</table>

Solution Status
This issue will be fixed in a future release of the Nios II EDS documentation.
**Missing Documentation of Interrupt API Properties**

“Tcl Commands” in the Nios II Software Build Tools Reference chapter of the Nios II Software Developer’s Handbook does not include information about two `set_sw_property` command properties related to the application programming interfaces (APIs) for interrupts.

**Workaround**

The missing information belongs with the `set_sw_property` command, in “Tcl Commands” in the Nios II Software Build Tools Reference chapter of the Nios II Software Developer’s Handbook.

The missing information follows:

- **supported_interrupt_apis**—Specifies the interrupt API that the device driver supports. The Nios II SBT analyzes this property for each driver in the system to determine the appropriate API to be used in the system.

  Specify `legacy_interrupt_api` if the device driver supports the legacy API only or `enhanced_interrupt_api` if the device driver supports the enhanced API only. Specify both using a quoted list if the device driver supports both APIs.

  If you do not specify which API your device driver supports, the Nios II SBT assumes that only the legacy interrupt API is supported.

  This property is only available for device drivers.

  For more information about the legacy and enhanced APIs, refer to “Exception Handling” in the Nios II Software Build Tools chapter of the Nios II Software Developer’s Handbook.

- **isr_preemption_supported**—Specify `true` if your device driver’s interrupt service routine (ISR) can be preempted by a higher priority ISR. If you do not specify whether the driver supports preemption, the Nios II SBT assumes that your device driver does not support preemption. If your driver does not have an ISR, but the associated device has an interrupt port, you can set this property to `true`.

  This property is valid for operating systems and device drivers.

**Solution Status**

This issue will be fixed in a future release of the Nios II EDS documentation.

**Valid Range of hal.log_flags is –1 to 3**

The Nios II EDS documentation incorrectly states the valid range for the `hal.log_flags` BSP setting. Valid values of `hal.log_flags` range from –1 through 3.

**Solution Status**

This issue will be fixed in a future release of the Nios II EDS documentation.
NicheStack TCP/IP Stack - Nios II Edition Does Not Support TFTP

The Nios II documentation should include the following information:

Versions of the NicheStack TCP/IP Stack other than the Nios II Edition include optional Trivial File Transfer Protocol (TFTP) client and server applications. However, the TFTP client and server are not thread safe. They cannot be used in systems with the MicroC/OS-II RTOS.

TFTP is not available with the NicheStack TCP/IP Stack - Nios II Edition. The TFTP client and server features are disabled.

Solution Status
This issue will be fixed in a future release of Nios II EDS documentation.

Error Message After Renaming Project: “Resource is out of sync with the system”

The Getting Started with the Graphical User Interface chapter of the Nios II Software Developer’s Handbook does not explain the correct procedure for renaming a project.

Workaround

The missing information is as follows:

To rename a project in the Nios II SBT for Eclipse, execute the following steps:
1. Right click the BSP project and click Rename.
2. Type the new BSP name.
3. Right click the BSP project and click Refresh.

If you neglect to refresh the project, you might see the following error message when you attempt to build it:

Resource <original_bsp_name> is out of sync with the system

Solution Status
This issue will be fixed in a future release of Nios II EDS documentation.

Obsolete Wiki URLs in Help Systems and Handbook

The following documents contain obsolete URLs for the Nios Community Wiki:
- The Nios II Software Developer’s Handbook
- The Nios II Embedded Design Handbook
- The Nios II IDE help system

Workaround

You can visit the Nios Community Wiki at www.nioswiki.com.

Solution Status
Fixed in the documentation for v. 9.0.
Missing Source Control Information

The Quartus II Handbook refers to the Using the Nios II Software Build Tools chapter of the Nios II Software Developer’s Handbook for information about putting Nios II projects in source control. The referenced chapter only describes the process for Nios II SBT projects.

Workaround

The missing information follows.

Archiving Nios II IDE Software Projects

This section helps you identify the files you must include when archiving a Nios II IDE software project. With this information, you can archive a Nios II application project and its associated Nios II system library project.

You might want to archive your projects for one of the following reasons:

- To place them under source control
- To create backups
- To bundle the projects for transfer to another location

This section covers the following information:

- How to find and identify the files that you must include in an archived Nios II IDE software project.
- Which files must have write permission to allow the software projects to be built.

Table 4 and Table 5 list the files required by Nios II IDE software projects. This is the minimum set of files needed to completely rebuild a software project, including the executable and linking file (.elf).

Archive your Nios II IDE software projects together with the SOPC Builder system on which they are based. You cannot rebuild a Nios II IDE software project without its associated SOPC Builder system.

The files listed in Table 4 are located in the Nios II application project directory.

Table 4. Files Required for a Nios II Application Project

<table>
<thead>
<tr>
<th>File Description</th>
<th>File Name</th>
<th>Write Permission Required?</th>
</tr>
</thead>
<tbody>
<tr>
<td>All source files</td>
<td>for example: app.c, header.h, assembly.s, lookuptable.dat</td>
<td>No</td>
</tr>
<tr>
<td>Eclipse project file</td>
<td>.project</td>
<td>No</td>
</tr>
<tr>
<td>C/C++ Development Toolkit project file</td>
<td>.cdtproject</td>
<td>Yes</td>
</tr>
<tr>
<td>C/C++ Development Toolkit option file</td>
<td>.cdtbuild</td>
<td>No</td>
</tr>
<tr>
<td>Software configuration file</td>
<td>application.stf</td>
<td>No</td>
</tr>
</tbody>
</table>

The files listed in Table 5 are located in the Nios II system library project directory.
You must have write permission for certain files. The tools write to these files as part of the build process. If the files are not writable, the toolchain fails. Many source control tools mark local files read-only by default. In this case, you must override this behavior. You do not have to check the files out of source control unless you are modifying the Nios II software project.

Solution Status

In the documentation for v. 9.0, the missing information appears in the Nios II Integrated Development Environment chapter of the Nios II Software Developer’s Handbook. In the documentation for v. 9.1, the missing information appears in the Using the Nios II Integrated Development Environment appendix of the Nios II Software Developer’s Handbook.

Compiler Flags for Building Custom Newlib

“Common BSP Tasks” in the Using the Nios II Software Build Tools chapter of the Nios II Software Developer’s Handbook describes how to compile a custom version of the newlib library. However, it does not list the compiler flags that must be selected to generate the correct library.

Workaround

The missing information is as follows:

The Nios II EDS provides a number of precompiled newlib implementations. The provided libraries are precompiled with every viable combination of the GCC compiler flags shown in Table 6.

When you create a BSP with a precompiled newlib, the Nios II SBT selects the newlib matching your BSP’s compiler settings. When you create a custom newlib, you must ensure that the compiler flags listed in Table 6 match your BSP’s settings.

Table 6. GCC Compiler Options for Newlib

<table>
<thead>
<tr>
<th>Option Name</th>
<th>Purpose</th>
</tr>
</thead>
<tbody>
<tr>
<td>-pg</td>
<td>Link for profiling with gprof</td>
</tr>
<tr>
<td>-EB</td>
<td>Generate big-endian code</td>
</tr>
<tr>
<td>-mstack-check</td>
<td>Enable stack checking</td>
</tr>
<tr>
<td>-mno-hw-mul</td>
<td>Disable use of the mul family of instructions</td>
</tr>
<tr>
<td>-mhw-mulx</td>
<td>Enable use of the mulx family of instructions</td>
</tr>
<tr>
<td>-mcustom-fpu-cfg</td>
<td>Use a floating point custom instruction</td>
</tr>
</tbody>
</table>

Table 5. Files Required for a Nios II System Library Project

<table>
<thead>
<tr>
<th>File Description</th>
<th>File Name</th>
<th>Write Permission Required?</th>
</tr>
</thead>
<tbody>
<tr>
<td>Eclipse project file</td>
<td>.project</td>
<td>Yes</td>
</tr>
<tr>
<td>C/C++ Development Toolkit project file</td>
<td>.cdtproject</td>
<td>Yes</td>
</tr>
<tr>
<td>C/C++ Development Toolkit option file</td>
<td>.cdtbuild</td>
<td>No</td>
</tr>
<tr>
<td>System software configuration file</td>
<td>system.stf</td>
<td>Yes</td>
</tr>
</tbody>
</table>
The Nios II-specific compiler flag `-mcustom-fpu-cfg` requires the Nios II processor core to be implemented with the floating-point custom instruction as specified in the flag argument. The compiler flag can have one of the following two argument values:

- `-mcustom-fpu-cfg=60-1`—Use the Nios II floating-point custom instruction without divider
- `-mcustom-fpu-cfg=60-2`—Use the Nios II floating-point custom instruction with divider

For details about the Nios II-specific compiler flags `-mstack-check`, `-mno-hw-mul`, and `-mhw-mulx`, refer to “Altera Nios II Options” in Using the GNU Compiler Collection (GCC), installed with the Nios II EDS. For details about the `-pg` compiler flag, refer to “Compiling a Program for Profiling” in GNU Profiler, installed with the Nios II EDS. To find the installed documentation, on the Windows Start menu, click Programs > Altera > Nios II EDS <version> > Nios II <version> Documentation, and then click Literature.

**Solution Status**
This issue will be fixed in a future release of Nios II EDS documentation.

### Error Message on Invalid Exception or Reset Vector

Nios II compiler error messages, added in v 8.1, are not documented in the Nios II Software Developer’s Handbook.

**Workaround**
The missing information is as follows:

While building a Nios II software project, the compiler produces an error message if the SOPC Builder system defines an invalid exception or reset vector. Table 7 shows possible error messages and their causes.

In the SOPC Builder system, ensure that the exception and reset vectors conform to all the criteria listed in Table 7.

**Solution Status**
This issue will be fixed in a future release of Nios II EDS documentation.

**Table 7.** Exception and Reset Vector Error Messages  (Part 1 of 2)

<table>
<thead>
<tr>
<th>Message</th>
<th>Cause</th>
</tr>
</thead>
<tbody>
<tr>
<td>The section mapping &quot;.entry&quot; is not mapped to a memory region.</td>
<td>The .entry section is mapped to a nonexistent memory region.</td>
</tr>
<tr>
<td>The section mapping &quot;.exceptions&quot; is not mapped to a memory region.</td>
<td>The .exceptions section is mapped to a nonexistent memory region.</td>
</tr>
</tbody>
</table>
| The section mapping ".entry" maps to a memory region 
"<region name>" which is not the required length "32". | The .entry section is mapped to a memory region that is smaller than 32 bytes. |
| The section mapping ".entry" maps to a memory region 
"<region name>" which does not start at the SOPC Design specified address 
"<base address>". | The .entry section is mapped to a memory region that does not start at the reset vector base address. |
Table 7. Exception and Reset Vector Error Messages  (Part 2 of 2)

<table>
<thead>
<tr>
<th>Message</th>
<th>Cause</th>
</tr>
</thead>
<tbody>
<tr>
<td>The section mapping &quot;.exceptions&quot; maps to a memory region &quot;&lt;region name&gt;&quot; which does not start at the SOPC Design specified address &quot;,&lt;base address&gt;&quot;.</td>
<td>The .exceptions section is mapped to a memory region that does not start at the exception vector base address.</td>
</tr>
<tr>
<td>The SOPC design Nios II reset and exception vector addresses are mapped to the same memory. For HAL the exception address must be at least 32 bytes larger than the reset address.</td>
<td>The .entry section and .exceptions section map to the same memory device, and the base of the .exceptions section is less than 32 bytes above the base of the .entry section.</td>
</tr>
<tr>
<td>The section mapping &quot;.exceptions&quot; mapped device &quot;,&lt;memory 1&gt;&quot; is not the same as the SOPC Design specified device &quot;,&lt;memory 2&gt;&quot;.</td>
<td>The .exceptions section is mapped to a device that is different from the exception vector device specified in the SOPC Builder system.</td>
</tr>
<tr>
<td>The section mapping &quot;.entry&quot; mapped device &quot;,&lt;memory 1&gt;&quot; is not the same as the SOPC Design specified device &quot;,&lt;memory 2&gt;&quot;.</td>
<td>The .entry section is mapped to a device that is different from the reset vector device specified in the SOPC Builder system.</td>
</tr>
</tbody>
</table>

Nios II IDE Online Help Expand Buttons Do Not Work

The expand text arrow and the Show All option in the online help do not work in Internet Explorer.

Workaround

In Internet Explorer, carry out the following actions:

1. Click the Refresh/Show Current Topic icon (yellow arrows icon at the top of the search pane). The table of contents appears with the selected topic highlighted.
2. Click the highlighted topic to refresh the browser frame. Expanded text works. Alternatively, use Firefox 2.0.0.6.

Solution Status

Not fixed

Hardware Abstraction Layer Errata

This section describes in detail the Nios II Hardware Abstraction Layer issues.

Missing Structure Member Errors in alt_log_printf.c with Small JTAG UART Driver

In a BSP, if you enable the small JTAG UART driver (altera_avalon_jtag_uart_driver.enable_small_driver), and enable Altera logging (hal.log_port) using the JTAG UART as the log port, when you build the project, the compiler reports missing structure members in alt_log_printf.c. The project fails to build.
Workaround

To avoid this issue, execute the following steps:

1. In function `alt_log_jtag_uart_print_control_reg()` in `alt_log_Printf.c`, insert the italicized code as shown in Example 6.

Example 6. ALT_LOG_PRINTF() Correction

```c
ALT_LOG_PRINTF(
   "%s SW CirBuf = %d, HW FIFO wspace=%d AC=%d WI=%d RI=%d WE=%d RE=%d\r\n",
   header,
   /* Beginning of inserted code */
   #ifndef ALTERA_AVALON_JTAG_UART_SMALL
   (dev->tx_out-dev->tx_in),
   #else
   0,
   #endif
   /* End of inserted code */
   space,ac,wi,ri,we,re);
```

2. Rebuild the project.

Solution Status

This issue will be fixed in a future release of the Nios II EDS.

“unused variable” Warning When Building alt_main.c

If you set the `hal.enable_exit` BSP setting to `false`, when building the project you see the following warning in `alt_main.c`:

```c
warning: unused variable 'result'
```

Design Impact

This warning is harmless.

Workaround

Set `hal.enable_exit` to `true`.

Alternatively, you can ignore this harmless warning.

Solution Status

This issue will be fixed in a future release of the Nios II EDS.

Build Warnings in Performance Counter Driver Files

If your Nios II system contains a performance counter, you might see the following warning when you build a BSP:

```c
warning: return makes pointer from integer without a cast
```

This warning appears in `alt_get_performance_counter_base()` in the software file `altera_avalon_performance_counter.c`.
This warning appears when the your BSP uses the Altera performance counter driver, and the C preprocessor symbol `PERFORMANCE_COUNTER_0_BASE` is defined.

This warning is harmless.

**Workaround**

In the `alt_get_performance_counter_base()` function in `altera_avalon_performance_counter.c`, add typecast `(void *)` to `PERFORMANCE_COUNTER_0_BASE`.

**Solution Status**

This issue will be fixed in a future release of the Nios II EDS.

---

### Hardware Example Design Errata

This section describes in detail the Nios II EDS hardware example design issues.

#### Incorrect PLL Phase Shift in Stratix II and Cyclone II Designs

In several example designs targeting the Nios II Cyclone II 2C35 and Nios II Stratix® II 2S60 RoHS development boards, the synchronous static random access memory (SSRAM) and legacy DDR SDRAM clocks are implemented with an incorrect phase-locked loop (PLL) phase shift.

This issue affects the following example designs:

- 2s60 RoHS Triple Speed Ethernet SG-DMA
- 2s60 RoHS Standard
- 2s60 RoHS Full Featured
- 2c35 Triple Speed Ethernet SG-DMA
- 2c35 Standard
- 2c35 Full Featured

This issue also affects any custom 2c35 or 2s60 RoHS design using SSRAM or legacy DDR SDRAM.

The incorrect phase shift might cause timing violations when reading from or writing to SSRAM or legacy DDR SDRAM.

**Workaround**

To correct the PLL settings, perform the following steps:

1. Open the standard design in SOPC Builder.
2. Double-click the component instance named `pll` to launch the PLL MegaWizard™ interface.
3. Click **Launch Altera's ALTPLL MegaWizard** to launch the MegaWizard interface.
4. Select the **Output Clocks** page and then the clock page used to generate the SSRAM clock. Depending on the particular design, this might be `clk c1`, `clk c2`, or another clock.
5. Ensure that Clock phase shift is ~3.38 ns.

6. Click the clock page used to generate the write clock for the legacy DDR SDRAM, if any. Depending on the particular design, this might be clk c1, clk c2, or another clock.

7. Ensure that Clock phase shift is 270 degrees.

8. Click Finish to save changes and exit the PLL MegaWizard interface.

9. Click Finish to save settings to the PLL instance in SOPC Builder.

10. Regenerate the system in SOPC Builder and recompile in the Quartus II software. Refer to the readme.txt file in the design example folder for information on compiling designs containing the DDR controller.

For further information refer to ssram_interface_readme.html, located in the \<Quartus II installation directory\>/sopc_builder/documents folder. This document discusses the SSRAM timing analysis methodology in detail. Additionally, AN 411: Understanding PLL Timing for Stratix II Devices discusses clock phase shift calculations and assignments for PLLs in Stratix II devices.

**Solution Status**
Not fixed

**Software Example Errata**
This section describes in detail the Nios II EDS software example issues.

**Hardware Tutorial Software Example Hangs on Some Boards**

The count binary application used in the tutorial hangs when run on some Altera development boards. The software runs through the binary count once and stops responding.

This issue affects boards that do not have a display to handle the LCD output. The affected boards include the following:

- Embedded Systems Development Kit, Cyclone III Edition (EP3C120)
- Stratix IV GX FPGA Development Kit (EP4SGX230)

**Workaround**
Modify line 18 of the count_binary.h file, as follows:

```
#define LCD_PRINTF(lcd, args...) /* Do Nothing */
```

Rebuild and run the software again.

**Solution Status**
Not fixed

**Simple Socket Server Example Displays Incorrect IP Address**

The simple socket server networking software example displays an incorrect IP address for the incoming connection.
Workaround
There is currently no workaround for this problem. Ignore the IP address of any incoming connection that is displayed on the console.

Solution Status
Not fixed

Networking Examples
If you are running a networking software example, you might be asked for a nine-digit number. You are directed to find this number on a sticker on your Nios development board, identified by the prefix ASJ. Not all Nios development boards have this sticker.

Workaround
If your Nios development board does not have a sticker with the letters ASJ followed by a nine-digit number, enter a unique nine-digit number when prompted. To avoid network address conflicts, ensure that this number is unique to each Nios board connected to your network.

Solution Status
Not fixed

Flash Programmer Errata
This section describes in detail the Nios II EDS issues related to the flash programmer.

Unable to Configure FPGA from Flash with Parallel Flash Loader
If you program an FPGA image to flash memory on a board that utilizes the Parallel Flash Loader, the FPGA subsequently fails to load from flash memory. This issue arises because the sof2flash utility generates a flash file with an incompatible Programmer Object File (.pof) bitstream format.

Affected Configurations
This issue impacts the following target hardware:

■ The Stratix IV GX FPGA Development Kit
■ The Arria® II GX FPGA Development Kit
■ Any hardware using the Parallel Flash Loader
**Workaround**

1. Visit the MySupport website and request Nios II EDS patch 0.73. Alternatively, upgrade to the Nios II EDS v. 9.1 SP1 or later.

2. Invoke `sof2flash` with the following options:
   - `--pfi`
   - `--optionsbits=<option bit address>`

   With these options, `sof2flash` generates a flash file with a compatible `.pof` file bitstream format.

**Solution Status**

Fixed in v. 9.1 SP1

**Boot Failure or elf2flash Error Due to Incorrect Flash File Size**

The `sof2flash` utility fails to recognize certain devices. As a result, it generates Motorola S-record (.flash) files of the wrong size.

With erasable programmable configurable serial (EPCS) memory, the `elf2flash --after` command fails to recognize the .flash file size. The resulting error prevents you from programming flash memory.

With Common Flash Interface (CFI) memory, you can program the .flash file, but the Nios II system fails to boot.

This error occurs when programming flash memory for the following devices:

- The following Stratix III devices:
  - EP3SL50
  - EP3SL50F484
  - EP3SL50F780
  - EP3SL110
  - EP3SL110F1152
  - EP3SL110F780
  - EP3SE80
  - EP3SE80F1152
  - EP3SE80F780
- All Stratix IV devices
- All Arria II GX devices

**Workaround**

Visit the MySupport website and request Nios II EDS 9.0 SP2 patch 2.01 and Quartus II 9.0 SP2 patch 2.63.

Alternatively, download and install the Altera Complete Design Suite v. 9.1 or later.
Solution Status
Fixed in v. 9.1

Delay When Creating New Flash Programmer Configuration
You might experience a delay of several seconds when creating a new Flash Programmer configuration in the Nios II IDE.

Workaround
There is no workaround.

Solution Status
Not fixed

elf2flash File Size Limit
The elf2flash utility supports .elf files up to approximately 24 MBytes in size. The elf2flash utility might fail on files larger than 24 MBytes, with the error message java.lang.OutOfMemoryError.

Workaround
Lower the number of symbols in your .elf file by turning off debug symbols. Alternatively, specify less initialized data in the application.

Solution Status
Not fixed

Error Message “No such file or directory” When Programming Flash
When programming flash from a project stored in a path containing spaces, you might receive the spurious error message No such file or directory. The flash programmer does not correctly handle spaces in the directory path. However, this error is harmless, because flash programming completes successfully.

Workaround
None required.

Solution Status
Not fixed

Hardware Simulation Errata
This section describes in detail the Nios II EDS issues related to hardware simulation.

Vectored Interrupt Controller Does Not Support VHDL Simulation Models
SOPC Builder reports errors when you try to generate VHDL simulation files for the Vectored Interrupt Controller.
**Workaround**
There is no workaround.

**Solution Status**
This issue will be fixed in a future release of the Nios II EDS.

**Error “UNC paths are not supported” Launching ModelSim**
If you launch ModelSim® from a working directory that is mapped via a universal naming convention (UNC) path (a path that starts with // instead of drive letter), you receive the following error message in SOPC Builder: **UNC paths are not supported.**

**Defaulting to Windows directory.** This error occurs because ModelSim is calling a command shell, which does not support UNC paths.

**Workaround**
Map the UNC path to a drive letter and use the drive letter to reference the working directory in the launching shell.

**Solution Status**
Not fixed

**Uninitialized .bss Variables in Simulation**
If your program reads the value of an uninitialized .bss variable during HDL simulation, and the BSP (system library) is compiled with the **ModelSim only, no hardware support** property enabled in Nios II IDE, a warning appears about unfiltered data being 'x'. This warning appears because when this property is enabled, the code that clears the .bss memory region is omitted to speed up HDL simulation so this memory region is uninitialized. The .bss region contains global and static local variables that are not initialized by the application so they default to a value of zero. When the Nios II processor reads uninitialized variables, it displays a warning and converts any of the bits of the uninitialized data to zero which correctly mimics the effect of the missing .bss clearing code. The Hardware Abstraction Layer (HAL) code that executes before and after main() might use .bss variables, so these warnings might appear even if your application does not use the .bss section.

**Solution Status**
Not fixed

**Host Platform Errata**
This section describes in detail the Nios II EDS issues related to the host development platform.

**Cannot Launch Nios II SBT for Eclipse on Red Hat Linux**
You might be unable to launch Nios II SBT for Eclipse on the Red Hat Linux operating system. This is an issue with the version of XULRunner in Eclipse 3.4. The issue is resolved in XULRunner 1.9.1, which is available with Eclipse 3.5.
Workaround
Upgrade XULRunner to a newer version. To determine what version of XULRunner you have, type the following command at the command prompt:

```bash
xulrunner -v
```

To upgrade XULRunner using the `yum` software package manager, type the following command:

```bash
sudo yum update xulrunner
```

Solution Status
This issue will be fixed in a future release of the Nios II EDS.

Windows/Cygwin: Nios II Processor Generation Failure

You might see the following error message when you generate your Nios II system in SOPC Builder:

```bash
Error: Generator program for module 'cpu_fpoint' did NOT run successfully
```

This error might occur on a Windows-based system when all of the following conditions are true:

- You have installed a version of Cygwin other than the one distributed with the Quartus II software.
- You launch the Quartus II software and SOPC Builder from a Nios II Command Shell.
- You enable the floating point custom instruction (FPCI) in the Nios II processor core.

Workaround
Launch the Quartus II software and SOPC Builder from the Windows Start menu, and regenerate your system.

Solution Status
This issue will be fixed in a future release of the Nios II EDS.

Windows Vista: Limited Support in Nios II EDS

The Quartus II software introduces Windows Vista (32-bit and 64-bit) support in version 7.2. However, the Nios II Embedded Design Suite supports only the following platforms:

- Windows 2000
- Windows XP
- Windows XP (64)
- SUSE 9 (32-bit)
- SUSE 9 (64-bit)
- Red Hat Linux v3.0 (32-bit)
- Red Hat Linux v3.0 (64-bit)
- Red Hat Linux v4.0 (32-bit)
- Red Hat Linux v4.0 (64-bit)

**Workaround**

There is no workaround.

**Solution Status**

This issue will be fixed in a future release of the Nios II EDS.

---

**Nios II IDE Errata: Building Projects**

This section describes in detail the issues related to building projects in the Nios II IDE.

**Nios II IDE Command-Line Tools Select Wrong Workspace on Linux**

On Linux systems, the Nios II IDE command-line tools use the default Eclipse project workspace, regardless what workspace you are using in the IDE. The default workspace is at 

```
```

This issue affects the following tools:

- nios2-create-system-library
- nios2-create-application-project
- nios2-build-project
- nios2-import-project
- nios2-delete-project

**Workaround**

Explicitly supply the Nios II IDE command-line tools with a workspace location, by means of the `-data` command-line argument. The syntax of the `-data` argument is as follows:

```
-data <path to workspace>
```

The path to the workspace must be absolute, and must not contain whitespace. Aside from these restrictions, any valid file system path can be used for the workspace. Example 7 shows how to specify a workspace path.

**Example 7. Providing a Non-Default Workspace Location in Linux**

```
nios2-create-project -data $HOME/myworkspace <other arguments>
```

**Solution Status**

Not fixed
Nios II IDE Command-Line Tools Hang on Windows

On Windows systems, the Nios II IDE command-line tools are sometimes unable to locate the project workspace. When this happens, the tools might hang.

This issue potentially affects the following tools:

- nios2-create-system-library
- nios2-create-application-project
- nios2-build-project
- nios2-import-project
- nios2-delete-project

Workaround

Explicitly supply the Nios II IDE command-line tools with a workspace location, by means of the `--data` command-line argument. The syntax of the `--data` argument is as follows:

```bash
--data <path to workspace>
```

The path to the workspace must be absolute, and must not contain whitespace. Aside from these restrictions, any valid file system path can be used for the workspace. The workspace shown in Example 8 is the default workspace used by the Nios II IDE in version 9.0.

Example 8. Providing a Workspace Location

```bash
nios2-create-project --data c:/altera/90/nios2eds/bin/eclipse/nios2-ide-workspace-9.0 <other arguments>
```

Solution Status

Not fixed

Nios II IDE Cannot Find stdio.h in Outline View

If you create a new project using the Nios II IDE and try to open the `stdio.h` file from the Outline view before building the project, the IDE displays the error message No include files were found that matched that name.

Workaround

Build the project before attempting to open `stdio.h`.

Solution Status

Not fixed

Nios II IDE Hangs With UNC Project Path

In the New Project dialog box, if you turn on Specify Location and specify the path in UNC form, the IDE might hang.
**Workaround**

Map the UNC path to a remote drive which looks like a Windows drive to the Nios II IDE.

**Solution Status**

Not fixed

---

**Build Failure with Nios II Advanced Exceptions, MMU, and MPU**

Projects created in the Nios II IDE for an SOPC Builder system containing a Nios II processor configured with advanced exceptions, the memory management unit (MMU), or the memory protection unit (MPU) fail to build, and generate the following error messages:

```plaintext
ERROR - Classic build flow for Nios II system library does not support
the Nios II advanced exceptions.
Use the non-classic Nios II Board Support Package instead.
<timestamp> - (SEVERE) generate: java.lang.IllegalArgumentException:
  java.lang.IllegalArgumentException:
  com.altera.ingenious.GTFElement.GTFElementErrorException:
  <error>
element in GTF script
make[1]: *** [system_description/../obj/system.h-t] Error 1
make: *** [system_project] Error 2
```

**Workaround**

Create the software project with the Nios II SBT command-line development flow. Alternatively, use the Nios II SBT for Eclipse.

**Solution Status**

Not fixed

---

**Build Command Not Functional for BSPs Created With the Nios II SBT**

The build option in the Nios II IDE menu does not rebuild BSPs imported to the IDE.

**Workaround**

The Nios II SBT for Eclipse can both build and debug projects created on the command line. The Nios II SBT for Eclipse is the preferred tool for debugging Nios II SBT projects.

For information about the Nios II SBT for Eclipse, refer to the *Getting Started with the Graphical User Interface* chapter of the *Nios II Software Developer’s Handbook*.

In the Nios II IDE, you can build the BSP by building the associated application project.

**Solution Status**

Not fixed
Linker Errors with Dual-Port Memories

If your instruction master and data master ports are connected to the same dual-port memory and the ports have different addresses, your code fails to run or you experience a linker error. The Nios II IDE does not warn you of the addressing violation.

Workaround
Assign the same address to both ports of the dual-port memory.

Solution Status
Not fixed

User-Managed BSP Settings Not Supported in Nios II IDE

For projects created with the Nios II SBT and imported to the Nios II IDE, the IDE configuration settings have no effect.

For example, objdump, compiler, and linker settings made in the IDE are ignored. This behavior occurs because Nios II SBT projects are not IDE-managed projects. In addition, the make-related preferences do not pertain to imported Nios II SBT projects. The IDE ignores these options during the build process.

Workaround
Make these settings in the project's makefile.

Solution Status
Not fixed

URL Project Location Causes Project Creation Error

When you try to create a new project in an existing workspace, you might see an error dialog box saying:

Project cannot be created. Reason: Internal Error

This error might occur if the path to any project in the workspace is a URL location, for example file:/F:/Design. To view the path, right-click on the project and select Properties.

Workaround
Import your existing application and system library projects to a new workspace.

Solution Status
Not fixed
Compilation Error with Separate Exception Stack Option

Choosing the Use a separate exception stack option might cause the following compilation error when building a project:

"UNDEFINED VARIABLE %STACK_POINTER%"

This error occurs if the exception stack is larger than the memory available for it.

**Workaround**

On the system library properties page for the project, turn off the separate exception stack or reduce the **Maximum exception stack size** setting.

**Solution Status**

Not fixed

Incorrect Stack and Heap Free Space Report

The makefile reports an incorrect number of bytes free for the stack and heap, if the heap and stack are in different memory regions.

**Workaround**

No workaround available.

**Solution Status**

Not fixed

Nios II IDE Reports Problems Without Displaying Error in Console

When building a project, the Nios II IDE reports problems, but the build output in the console does not contain any errors.

The Nios II IDE incorrectly reports some linker warnings as errors, with a dialog box saying Errors exist in a required project. The Dhrystone software example exhibits this behavior, and recompiling the project again makes the issue go away.

**Workaround**

If the Console output does not contain errors, then the project built correctly. On subsequent builds, the linker step is skipped and the errors do not appear.

**Solution Status**

Not fixed
Nios II IDE Errata: Debugging Projects

This section describes in detail the issues related to debugging projects in the Nios II IDE.

Method for Accessing MMU and MPU Registers in the Debugger

You might observe an error if you try to read or write an MMU or MPU register via the Nios II Debugger.

**Workaround**

To read an MPU region, execute the following steps:

1. Set region INDEX in the MPUBASE register.
2. Exit and reenter debug mode, that is, single step.
3. Set the RD bit in the MPUACC register.
4. Exit and reenter debug mode, that is, single step.
5. Read back MPUBASE for pertinent information.
6. Read back MPUACC for pertinent information.

For a system with an MMU or MPU, this workaround allows you to read and write the current values of the registers. However, you cannot use it to control MPU regions or MMU TLB entries.

The debug core copies the Nios II processor’s register values to its internal memory when the processor enters debug mode. The debug core writes register values back to the processor only when the processor leaves debug mode. Therefore, if you attempt to set an MPU region with several consecutive values, only the last one, when leaving debug mode, is committed to the processor.

**Solution Status**

Not fixed

IDE Cannot Display Imported Profiling Data

If you create a software project in the Nios II SBT command line flow, generate profiling data to gmon.out in the Nios II command shell, and then import the project to the IDE, you cannot use the Profiling perspective to view the profiling data. When you attempt to view gmon.out, the IDE displays the following error message:

nios2-elf-gprof: ../: not in a.out format.

**Workaround**

Run nios2-elf-gprof from the Nios II command shell to generate a profiler report.

**Solution Status**

Not fixed
Error Message when Downloading .elf File

If the Build Automatically option is turned on in the IDE, you might get the following error when downloading an executable and linking formal file (.elf):

```
2 [main] sh 5736 fork: child 3892 - died waiting; \
for longjmp before initialization, errno 11
```

The Build Automatically option is off by default, as recommended by the Eclipse help system.

Workaround

Turn off the Build Automatically option and download the .elf file again.

Solution Status

Not fixed

Variable Casting Unsupported in ISS

Casting variables in the Variables view in the Debug perspective when using the Nios II ISS might cause an exception dialog box to open.

This exception might occur if you select Debug as ISS in the Nios II IDE and try to cast variables via the Variables view.

Workaround

There is no workaround.

Solution Status

Not fixed

Trace Debug Does Not Support Instruction-Related Exceptions

The instruction-related exception handler is not supported by the Nios II trace tools.

Workaround

There is no workaround.

Solution Status

Not fixed

Trace Debug Does Not Support the JMPI Instruction

The JMPI instruction is not supported by the Nios II trace tools.

Workaround

There is no workaround.

Solution Status

Not fixed
Cannot Locate Source Code in Driver Files Shared by Multiple Projects

If you hit a breakpoint in a driver file, and that driver file is shared with another project that is closed, the Nios II IDE might indicate that it cannot locate the source code.

**Workaround**
Open the closed system library project and resume debugging.

**Solution Status**
Not fixed

Console Window Is Not Updated After ISS Error

After performing a Run as ISS, if you receive an ISS error in the console window, the console is not updated subsequently.

**Workaround**
Close the console window after receiving an ISS error. A new console window opens when a new message is available.

**Solution Status**
Not fixed

ISS Fails on Designs Containing Triple Speed Ethernet MAC or SG-DMA Components

You receive an Internal Error when attempting to perform an ISS simulation of designs containing the Altera Triple Speed Ethernet media access control (MAC) or SG-DMA components because the Nios II ISS does not support these components.

**Workaround**
Remove the Triple Speed Ethernet MAC and SG-DMA components from your system and perform ISS simulation on the simplified system. You can also simulate the design in ModelSim or test it on hardware.

Before removing the Triple Speed Ethernet MAC and SG-DMA components, make a copy of the unmodified system to ensure that you can return to the original configuration.

**Solution Status**
Not fixed

Memory Window Sets Control Register Values Incorrectly

The memory window might incorrectly set values in memory-mapped control registers. For example, writing 0x1234 to a byte addressed register results in the value 0x3434 being stored in the register. The memory window shows this incorrect value.
**Workaround**

Use the GNU debugger (GDB) console window in the IDE, instead of the memory window, to write to the registers. For example, type the following command:

```
set (int) <register address>=0x1234
```

You must refresh the memory window for it to correctly display the target value.

**Solution Status**

Not fixed

---

**Programs That Interact With a Terminal Console on Windows Do Not Work**

Programs with this behavior work in v. 6.0 and earlier, but do not work in Nios II IDE v. 6.1 and later.

The Eclipse platform in v. 6.1 and later of the IDE (on Windows only) sends the string `\r\n` instead of just `\n` when running or debugging using the Terminal. This behavior can break existing software designs, and it is inconsistent with `nios2-terminal`, which still just sends `\n`.

**Workaround**

Change the software running on the Nios II processor to parse for `\r\n` as well as `\n`.

**Solution Status**

Not fixed

---

**“Run as ModelSim” in the Nios II IDE Fails**

The **Run as ModelSim** command might fail on launch configurations created in v. 7.0 or earlier of the IDE. This problem does not occur for new launch configurations.

**Workaround**

Select a location for the ModelSim tool from the launch configuration dialog box. You can use the **Browse** button next to the **ModelSim path** group, or type in a path to the ModelSim directory (for example `c:/altera/71/modelsim_ae/win32aloem`).

**Solution Status**

Not fixed

---

**The Restart Command on the Run Menu Does Not Work**

The **Restart** command on the Run menu does not work.

**Workaround**

Stop the program, then debug it again. If the debugger is hung in an endless loop, use the following **bash** alias to break the target, then stop it:

```
alias break="kill -2 \'ps -a | grep nios2-elf-gdb | cut -f6 -d' '"
```

Solution Status
Not fixed

Watchpoints Do Not Work on Certain Variables
Watchpoints do not work on a variable whose size is not 32 bits.

Workaround
Change the types of global and static local variables to int, long, or unsigned long before setting watchpoints on them.

Solution Status
Not fixed

Nios II IDE Errata: Navigating Projects
This section describes in detail the issues related to navigating projects in the Nios II IDE.

Nios II IDE Freezes While Displaying the Splash Screen
After clicking Switch Workspace on the File menu on a Windows machine, a Nios II IDE splash screen appears. Unfortunately, this splash screen obscures the dialog box that asks you to specify the new workspace. As a result, the IDE appears to freeze.

Workaround
Press Alt-Tab to switch applications. Two relevant application icons appear: an Eclipse icon associated with the splash screen and a Nios II IDE icon associated with the Workspace dialog box. Select the Nios II icon to bring the dialog box to the foreground.

Solution Status
Not fixed

Internal Error When Double-Clicking on a Large Objdump File
On Windows when opening a large objdump file in the Nios II IDE, you might get the following error message:

Unable to create this part due to an internal error.
Reason for the failure: Editor could not be initialized.
Workaround
Adjust the Windows launch arguments for the Nios II IDE editor. Perform the following steps:

1. On the Windows Start menu, browse to the Nios II EDS program icon, right-click it, then click Properties. The Windows Properties dialog box appears.
2. In the Target field, append "vmargs -Xmx1024m" to the end of the path to the Nios II IDE executable. For example:
   
   C:\altera\72\nios2eds\bin\eclipse\nios2-ide.exe -vmargs -Xmx1024m

Solution Status
Not fixed

C/C++ Scanner Does Not Support Certain C/C++ Constructs
The C/C++ scanner performs C/C++ Search, navigation, open declaration, and parts of content assist. Due to limitations of the C/C++ scanner, these features do not work with C code constructs not supported by the C++ language. An example is functions that take a function pointer as an argument.

Workaround
If the C/C++ Search fails, use the File Search facility.

Solution Status
Not fixed

Nios II SBT Errata
This section describes in detail the issues in the Nios II SBT.

BSP Editor Does Not Show Command-Line Help
The following commands in the Nios II Command Shell should display command-line help:

- nios2-bsp-editor --help
- nios2-bsp-editor --extended-help

Instead, these commands launch the BSP Editor.

Workaround
For help with the BSP Editor, refer to “Using the BSP Editor” in the Getting Started with the Graphical User Interface chapter of the Nios II Software Developer’s Handbook.

Solution Status
This issue will be fixed in a future release of the Nios II EDS.
BSP Not Updated for Memory Size Changes in SOPC Builder

If you change the size of a memory in SOPC Builder, the memory region size in any previously created BSP is no longer correct. Regenerating the BSP does not update the BSP’s memory region size.

**Workaround**

In the BSP Editor, on the **Linker Script** tab, you can correct the memory region size either of the following ways:

- Edit the memory region size manually.
- Click **Restore Defaults** to rerun the default Tcl script. The default Tcl script reads the updated memory region size from the SOPC information file (.sopcinfo) and updates the BSP.

For detailed information about keeping your BSP consistent with changes in the underlying SOPC Builder system, refer to “Revising Your BSP” in the *Nios II Software Build Tools* chapter of the *Nios II Software Developer’s Handbook*.

**Solution Status**

This issue will be fixed in a future release of the Nios II EDS.

Build Errors on Interrupt API Calls

You might observe build errors if your application project installs an ISR that calls the `alt_irq_register()` function.

The Nios II EDS v9.1 includes support for an optional External Interrupt Controller (EIC) interface on the Nios II processor. To support the EIC interface, the HAL includes an enhanced interrupt API.

When you create a BSP, the SBT determines which interrupt API(s) the device drivers in your system support. If all registered drivers in the BSP identify themselves as supporting the enhanced API, the SBT implements the enhanced API. All Altera device drivers in v9.1 and later support the enhanced API.

When the enhanced API is implemented, the legacy interrupt API, including `alt_irq_register()`, is not available. Therefore, if the application code contains a call to a legacy API function, a linker error occurs.

This issue might affect application projects that call `alt_irq_register()` or other legacy interrupt API functions directly, rather than depending on drivers in the BSP for interrupt support.

If your Nios II application project’s source code registers an ISR using `alt_irq_register()`, or includes an ISR, your application might fail to compile in the Nios II EDS v. 9.1, due to undefined references to `alt_irq_register()`, or to a change to the ISR function prototype.

**Workaround**

Modify your application code to use the new enhanced interrupt API. To use the enhanced API, you must modify several function calls and all ISR function prototypes.
For information about supporting the enhanced interrupt API, refer to the *Exception Handling* chapter of the Nios II Software Developer’s Handbook, or to AN 595: *Vectored Interrupt Controller Usage and Applications*.

You must upgrade to the enhanced API if you wish to use an EIC in your hardware design to accelerate interrupt response time. The enhanced API also works with the Nios II processor’s internal interrupt controller.

**Solution Status**
Not fixed

**BSP Editor Incorrectly Allows Mixed Drivers**

If multiple drivers are found for a single peripheral type, and you have multiple instances of the peripheral type in your SOPC Builder system, the Nios II BSP editor allows you to manually select a different driver for each instance of the peripheral or processor. For example, if you have two `altera_avalon_timer` devices, you can select two different timer drivers.

This behavior is not fully supported. You must choose the same driver for both instances of the peripheral type.

You can make driver assignments in one of the following ways:

- In the Nios II BSP editor, within the *Drivers* tab. The *Driver name* associated with each peripheral can be edited, if multiple driver types are found that support the peripheral. You must make the same driver type selection for each instance of a peripheral type in the list of peripherals as defined in the *Module Name* column.

- With the Nios II SBT in command-line mode (`nios2-bsp`, `nios2-bsp-create-settings`, `nios2-bsp-update-settings`): the `set_driver` command can be invoked (either from a Tcl script or with the `--cmd` option). The `set_driver` command accepts a peripheral module instance name and driver name as arguments. If you are using the command line development flow and wish to use this command, you must invoke it once for each instance of a peripheral type in your system, similar to the BSP editor flow described in the previous paragraph.

**Workaround**
There is no workaround.

**Solution Status**
Fixed in v. 9.1

**nios2-bsp --help Displays Incorrect List of BSP Types**

The Nios II SBT supports custom HAL-based operating systems, when they are added as Tcl-scripted software packages. However, the `nios2-bsp` tool help message indicates that there are only two BSP types, HAL and UCOSII. If other HAL-based operating system components are added they do not appear in the `nios2-bsp` help message.
Workaround
See “nios2-bsp-create-settings Fails to List BSP Types” to get a correct, dynamic list of available BSP types.

Solution Status
Fixed in v. 9.1

nios2-bsp-create-settings Fails to List BSP Types
The help message for nios2-bsp-create-settings incorrectly states that using the --type option with no value creates a list of available BSP types.

Workaround
To get a list of available BSP types, execute the following command:

`nios2-bsp-create-settings --type x --settings x --sopc=<existing sopcinfo path>`

The following command returns an error message that lists available BSP types:

SEVERE: Available BSP type values for the --type argument are: [<list of BSP types>]

Solution Status
Fixed in v. 8.1

Makefile Error with Absolute Source Path
You might receive the following error message from the Nios II SBT:

`Makefile:: *** target pattern contains no '%'`

This error message can appear if you are using Windows, you specify an absolute source path with the --src-dir option, and your application directory, specified with the --app-dir option, is the same as your source path.

Workaround
Use a relative path to the source directory, or organize your files so that your application directory is different from your source directory.

Solution Status
Fixed in v. 8.1
**Incorrect Linker Errors**

While building a HAL-based Nios II software project, the linker displays misleading error messages if the SOPC Builder system provides less than 32 bytes of memory at the reset vector. The linker errors are similar to the following:

```
/cygdrive/d/altera/80/nios2eds/bin/nios2-gnutools/H-i686-pc-cygwin/bin/../lib/gcc/ 
nios2-elf/3.4.6/../../../../nios2-elf/bin/ld:/cygdrive/d/test3/software/ 
hello_world_0_syslib/Debug/system_description/generated.x:186: warning: memory 
region UNDEFINED not declared
/cygdrive/d/altera/80/nios2eds/bin/nios2-gnutools/H-i686-pc-cygwin/bin/../lib/gcc/ 
nios2-elf/3.4.6/../../../../nios2-elf/bin/ld:/cygdrive/d/test3/software/ 
hello_world_0_syslib/Debug/system_description/generated.x:186: parse error
collect2: ld returned 1 exit status
make: *** [hello_world_0.elf] Error 1
```

**Workaround**

In the SOPC Builder system, if the reset and exception vectors are in the same memory device, ensure that the exception vector is at least 32 bytes above the reset vector.

**Solution Status**

Fixed in v. 8.1

**Java Heap Space Error When Creating BSP**

When creating a BSP, the Nios II SBT might hang with the following message:

```
SEVERE: OutOfMemoryError: Java Heap Space.
```

This problem occurs when your SOPC Builder design has connection cycles. A connection cycle consists of a group of components that master one another in a circular arrangement. For example, if `ddr_sdram_bridge` masters `lcd_sgdma`, and `lcd_sgdma` masters `ddr_sdram_bridge`, the two components form a simple connection cycle. In this situation, the SBT is unable to create a BSP.

**Workaround**

Avoid connection cycles in the SOPC Builder system.

**Solution Status**

Fixed in v. 9.0

**SBT Fails if Nios II EDS is Installed in a Path Containing Spaces**

The Nios II Command Line SBT fails if you install the Nios II EDS in a path containing spaces.

**Workaround**

Reinstall the Nios II EDS to a path that does not contain spaces.

**Solution Status**

Not fixed
**User-Managed BSP Settings Not Supported in Nios II IDE**

This issue is described on page 39.

**Upper-Case File Extensions Not Supported**

The Nios II SBT for applications and libraries (nios2-app-generate-makefile and nios2-lib-generate-makefile commands) does not support source files with certain upper-case extensions. If a file with an upper-case extension is included, the make command stops with no descriptive warning.

Only Nios II assembly language files built by the C preprocessor can have upper-case file extension (.S). All C language files must have the extension .c or .h. C++ language source files must have the extension .cpp, .cxx, .cc, or .h.

**Workaround**

Rename all C language files with the extension .c or .h. Rename all C++ language files with the extension .cpp, .cxx, .cc, or .h.

**Solution Status**

This issue will be fixed in a future release of the Nios II EDS.

---

**Peripheral Errata**

This section describes in detail the Nios II EDS issues related to peripheral components.

**Vectored Interrupt Controller Does Not Support VHDL Simulation Models**

This issue is described on page 33.

**SBT Fails With UART Driver**

If you use the SBT on a design containing the SOPC Builder UART peripheral, and if driver ioctl() support for the altera_avalon_uart component is enabled, then the BSP build process might fail. The system.h generated by the SBT contains an invalid type for the UART parameter.

**Workaround**

To correct system.h, carry out the following steps:

1. Manually edit the system.h file by changing the following line:

    ```
    #define UART1_PARITY "N"
    ```

   to:

    ```
    #define UART1_PARITY 'N'
    ```

   Change double quotes to single quotes.

2. Build the BSP.
**Solution Status**
Fixed in v. 8.1

**Unaligned Transfers of Small Payloads Fail on SG-DMA**

The Scatter Gather DMA SOPC Builder peripheral does not correctly handle unaligned transfers with small payloads. A payload length smaller than the data width causes erroneous data transfers.

**Workaround**
Avoid using DMA devices to transfer small payloads.
If absolutely necessary, for a 32-bit SG-DMA, a minimum length of 4 bytes guarantees that data is transferred correctly.

**Solution Status**
Not fixed

**DMA Controller Always Busy in Burst Mode**

The DMA controller component (`altera_avalon_dma`), when enabled for burst transactions, does not perform transfers at widths other than its full data width. The DMA controller is always busy.

**Workaround**
When bursting is enabled, the DMA controller must be programmed to perform transactions at its full data width.

**Solution Status**
Not fixed

**Non-System-Wide Reset Can Cause Improper Initialization of Mailbox Core**

The `altera_avalon_mailbox` peripheral might not be initialized properly when a soft (non-system-wide) reset occurs. In this condition, mailbox contents (read and write pointers) are not reinitialized and might show potentially stale data.

**Workaround**
Ensure that a system-wide reset event occurs by asserting the `reset_n` input to the SOPC Builder system containing the mailbox. This resets all peripherals and Nios II processors in the system.

**Solution Status**
This issue will be fixed in a future release of the Nios II EDS.
Target Software Errata

This section describes in detail the Nios II EDS issues related to target software packages.

NicheStack tcp_wake() Function Does Not Awaken All select() Sleepers

This issue impacts software based on the NicheStack TCP/IP Stack - Nios II Edition, with multiple sleeping tasks that depend on the same select() event to wake up. The tcp_sleep() and tcp_wake() functions do not work properly for tasks that call select().

Tasks can normally use tcp_sleep() and tcp_wake() to suspend and then awaken themselves upon specific events. The select() function passes an event pointer to tcp_sleep() that is identical for every task that might call select(). All other events that call tcp_sleep() pass it a unique event pointer that specifically identifies them as the task to be awakened at the next event.

The tcp_wake() function apparently assumes that the list of sleepers contains unique event pointers by which they can be identified. Therefore, when a select() event occurs, instead of awakening all tasks waiting for it, tcp_wake() awakens only the first task in the list. Therefore, when more than one task calls select() to await some outcome on a socket, only the highest-priority task in the sleepers list is awakened at select() events. Other sleeping tasks that depend on the same select() event do not wake up.

Workaround

There is no workaround.

Solution Status

This issue will be fixed in future release of the Nios II EDS.

EIC-Based Nios II System Crashes With NicheStack

When you attempt to run software based on the NicheStack TCP/IP Stack - Nios II Edition on a hardware design incorporating an external interrupt controller such as the VIC, the Nios II system crashes during initialization, with unpredictable results.

Workaround

There is no workaround.

Solution Status

This issue will be fixed in a future release of the Nios II EDS.

Gigabit Ethernet Performance Issues

The NicheStack TCP/IP Stack - Nios II Edition might hang while sending transmission control protocol (TCP) packets to the host, or experience dropped packets when receiving TCP packets from the host. These issues can arise with version 3.1 of the NicheStack TCP/IP Stack, using the triple-speed Ethernet component to transmit over a Gigabit Ethernet point-to-point connection.
**Workaround**


Alternatively, upgrade to the Nios II EDS v. 9.0 or later.

**Solution Status**

Fixed in v. 9.0

**stdio Does Not Work with MicroC/OS-II and Small C Library**

stdin, stdout, and stderr do not work in MicroC/OS-II applications built with the Small C library option.

**Workaround**

Disable the small C library option.

**Solution Status**

Not fixed

**cout From MicroC/OS-II Task Does Not Send Data to stdout**

If neither printf() or cout is used from main() before tasks are started, cout does not work from a task.

**Workaround**

Add the following C++ code to the beginning of main():

```cpp
std::ios_base::sync_with_stdio(false);
```

**Solution Status**

Not fixed

**malloc(), realloc() Failures With MicroC/OS-II**

When you use the MicroC/OS-II RTOS, calls to malloc() and realloc() might fail if successive calls to malloc() or realloc() within a MicroC/OS-II task occur after changing the task priority of the task in which a memory block is originally allocated.
Workaround

Use one of the following workarounds:

- Allocate and/or reallocate memory blocks outside of MicroC/OS-II tasks, before task switching starts. Preallocated memory blocks make it possible to change thread priorities at runtime.

- Allocate fixed areas of memory using arrays (rather than using `malloc()`) before task switching starts. Fixed memory arrays make it possible to change thread priorities at runtime.

- Allocate memory using `malloc()` or `realloc()` from a MicroC/OS-II task. You can change task priorities at runtime, but only for tasks that have not used `malloc()` or `realloc()`.

Solution Status

Not fixed

Toolchain Errata

This section describes in detail the Nios II EDS issues related to the Nios II and GNU compiler toolchains, including tools such as `gcc`, `gdb`, and `sof2flash`.

Unable to Configure FPGA from Flash with Parallel Flash Loader

This issue is described on page 31.

sof2flash Names Output Files Incorrectly

If `sof2flash` is called with the `--save` option and a relative path is specified for the output file (that is, including `"./"` or `"../"`), the raw binary file that is saved is incorrectly named `.rbf` instead of `<flash filename>.rbf`.

Workaround

Provide a full path to the output file(s), or change to the output directory before running the `sof2flash` command so the output file specification is `--output=<filename>.flash`.

Solution Status

Fixed in v. 8.1

GNU Assembler Does Not Accept the --defsym Flag

According the GNU documentation, you can set an assembler definition by using the `--defsym` flag, but it does not work in the following form: `--defsym MY_VAR=1`.

Workaround

There is no workaround.

Solution Status

Not fixed
How to Contact Altera

For the most up-to-date information about Altera products, refer to Table 8.

Table 8. Contact Information

<table>
<thead>
<tr>
<th>Contact (1)</th>
<th>Contact Method</th>
<th>Address</th>
</tr>
</thead>
<tbody>
<tr>
<td>Technical support</td>
<td>Website</td>
<td><a href="http://www.altera.com/support">www.altera.com/support</a></td>
</tr>
<tr>
<td>Technical training</td>
<td>Website</td>
<td><a href="http://www.altera.com/training">www.altera.com/training</a></td>
</tr>
<tr>
<td></td>
<td>Email</td>
<td><a href="mailto:custrain@altera.com">custrain@altera.com</a></td>
</tr>
<tr>
<td>Altera literature services</td>
<td>Email</td>
<td><a href="mailto:literature@altera.com">literature@altera.com</a></td>
</tr>
<tr>
<td>Non-technical support (General)</td>
<td>Email</td>
<td><a href="mailto:nacomp@altera.com">nacomp@altera.com</a></td>
</tr>
<tr>
<td>(Software Licensing)</td>
<td>Email</td>
<td><a href="mailto:authorization@altera.com">authorization@altera.com</a></td>
</tr>
</tbody>
</table>

Note:
(1) You can also contact your local Altera sales office or sales representative.

Typographic Conventions

Table 9 shows the typographic conventions that this document uses.

Table 9. Typographic Conventions (Part 1 of 2)

<table>
<thead>
<tr>
<th>Visual Cue</th>
<th>Meaning</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Bold Type with Initial Capital Letters</strong></td>
<td>Indicates command names, dialog box titles, dialog box options, and other graphical user interface (GUI) labels. For example, <strong>Save As</strong> dialog box. For GUI elements, capitalization matches the GUI.</td>
</tr>
<tr>
<td><strong>bold type</strong></td>
<td>Indicates directory names, project names, disk drive names, file names, file name extensions, dialog box options, software utility names, and other GUI labels. For example, \qdesigns directory, d: drive, and chiptrip.gdf file.</td>
</tr>
<tr>
<td><em>Italic Type with Initial Capital Letters</em></td>
<td>Indicates document titles. For example, <em>AN 519: Stratix IV Design Guidelines.</em></td>
</tr>
<tr>
<td><strong>Italic type</strong></td>
<td>Indicates variables. For example, $n + 1$. Variable names are enclosed in angle brackets (&lt; &gt;). For example, <code>&lt;file name&gt;</code> and <code>&lt;project name&gt;.pof</code> file.</td>
</tr>
<tr>
<td><strong>Initial Capital Letters</strong></td>
<td>Indicates keyboard keys and menu names. For example, Delete key and the Options menu.</td>
</tr>
<tr>
<td><strong>“Subheading Title”</strong></td>
<td>Quotation marks indicate references to sections within a document and titles of Quartus II Help topics. For example, “Typographic Conventions.”</td>
</tr>
<tr>
<td><strong>Courier type</strong></td>
<td>Indicates signal, port, register, bit, block, and primitive names. For example, <code>data1</code>, <code>tdi</code>, and <code>input</code>. Active-low signals are denoted by suffix <code>n</code>. For example, <code>resetn</code>. Indicates command line commands and anything that must be typed exactly as it appears. For example, <code>c:\qdesigns\tutorial\chiptrip.gdf</code>. Also indicates sections of an actual file, such as a Report File, references to parts of files (for example, the AHDL keyword SUBDESIGN), and logic function names (for example, <code>TRI</code>).</td>
</tr>
<tr>
<td><strong>1., 2., 3., and a., b., c., and so on.</strong></td>
<td>Numbered steps indicate a list of items when the sequence of the items is important, such as the steps listed in a procedure.</td>
</tr>
<tr>
<td><strong>■ ■</strong></td>
<td>Bullets indicate a list of items when the sequence of the items is not important.</td>
</tr>
</tbody>
</table>
Table 9. Typographic Conventions (Part 2 of 2)

<table>
<thead>
<tr>
<th>Visual Cue</th>
<th>Meaning</th>
</tr>
</thead>
<tbody>
<tr>
<td><img src="hand_icon.png" alt="Hand Icon" /></td>
<td>The hand points to information that requires special attention.</td>
</tr>
<tr>
<td><img src="caution_icon.png" alt="Caution Icon" /></td>
<td>A caution calls attention to a condition or possible situation that can damage or destroy the product or your work.</td>
</tr>
<tr>
<td><img src="warning_icon.png" alt="Warning Icon" /></td>
<td>A warning calls attention to a condition or possible situation that can cause you injury.</td>
</tr>
<tr>
<td><img src="angled_arrow_icon.png" alt="Angled Arrow" /></td>
<td>The angled arrow instructs you to press Enter.</td>
</tr>
<tr>
<td><img src="feet_icon.png" alt="Feet Icon" /></td>
<td>The feet direct you to more information about a particular topic.</td>
</tr>
</tbody>
</table>