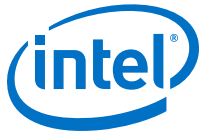




# Unique Chip ID Intel FPGA IP Core Release Notes



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## Unique Chip ID Intel FPGA IP Core Release Notes

If a release note is not available for a specific IP core version, the IP core has no changes in that version. Information on the latest update releases is in the *Intel Quartus Prime Design Suite Update Release Notes*.

### Related Information

[Intel Quartus Prime Design Suite Update Release Notes](#)

## Unique Chip ID Intel FPGA IP Cores v18.0

**Table 1. v18.0 May 2018**

Description	Impact
Added readid port for Unique Chip ID Intel Stratix 10 FPGA IP core.	-
Renamed the following IP core as per rebranding: <ul style="list-style-type: none"> <li>Intel FPGA S10 Chip ID IP core to Chip ID Intel Stratix 10 FPGA IP core.</li> <li>Intel FPGA Unique Chip ID for Arria 10 IP core to Unique Chip ID Intel Arria 10 FPGA IP core.</li> <li>Intel FPGA Cyclone 10 GX Unique Chip ID IP core to Unique Chip ID Intel Cyclone 10 GX FPGA IP core.</li> </ul>	-

### Related Information

[Unique Chip ID Intel FPGA IP Cores User Guide](#)

## Intel FPGA Unique Chip ID IP Core v17.1

**Table 2. v17.1 December 2017**

Description	Impact
Added support for Intel® Stratix® 10 and Intel Cyclone® 10 GX devices	-

### Related Information

[Intel FPGA Chip ID IP Cores User Guide](#)

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