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1. External Memory Interfaces Intel® Cyclone® 10 GX FPGA IP Core Release Notes

If a release note is not available for a specific IP core version, the IP core has no changes in that version. Information on the latest update releases is in the Intel Quartus Prime Design Suite Update Release Notes.

1.1. External Memory Interfaces Intel® Cyclone® 10 GX FPGA IP 18.1

Table 1. v18.1 September 2018

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<tr>
<th>Description</th>
<th>Impact</th>
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<tbody>
<tr>
<td>Verified in the Intel® Quartus® Prime software v18.1.</td>
<td>Provides external memory interface IP for DDR3 and LPDDR3 protocols for Intel Cyclone® 10 GX devices.</td>
</tr>
</tbody>
</table>

Related Information
- External Memory Interfaces Intel Cyclone 10 GX FPGA IP User Guide
- External Memory Interfaces Intel Cyclone 10 GX FPGA IP Design Example User Guide
- Intel Quartus Prime Design Suite Release Notes

1.2. External Memory Interfaces Intel Cyclone 10 GX FPGA IP 18.0

Table 2. v18.0 May 2018

<table>
<thead>
<tr>
<th>Description</th>
<th>Impact</th>
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</thead>
<tbody>
<tr>
<td>Verified in the Intel Quartus Prime software v18.0.</td>
<td>Provides external memory interface IP for DDR3 and LPDDR3 protocols for Intel Cyclone 10 GX devices.</td>
</tr>
</tbody>
</table>

Related Information
- External Memory Interfaces Intel Cyclone 10 GX FPGA IP User Guide
- External Memory Interfaces Intel Cyclone 10 GX FPGA IP Design Example User Guide
- Intel Quartus Prime Design Suite Release Notes

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*Other names and brands may be claimed as the property of others.
1.3. Intel Cyclone 10 GX External Memory Interface IP 17.1

Table 3. v17.1 November 2017

<table>
<thead>
<tr>
<th>Description</th>
<th>Impact</th>
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<tr>
<td>Initial release.</td>
<td>Provides external memory interface IP for DDR3 and LPDDR3 protocols for Intel Cyclone 10 GX devices.</td>
</tr>
</tbody>
</table>

Related Information

- Intel Cyclone 10 GX External Memory Interfaces IP User Guide
- Intel Cyclone 10 GX External Memory Interfaces IP Design Example User Guide
- Intel Quartus Prime Design Suite Release Notes