Mailbox Client with Avalon® Streaming Interface Intel® FPGA IP Release Notes
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1. Mailbox Client with Avalon® Streaming Interface Intel® FPGA IP Release Notes

IP versions are the same as the Intel® Quartus® Prime Design Suite software versions up to v19.1. From Intel Quartus Prime Design Suite software version 19.2 or later, IP cores have a new IP versioning scheme.

The IP version (X.Y.Z) number may change from one Intel Quartus Prime software version to another. A change in:

- X indicates a major revision of the IP. If you update your Intel Quartus Prime software, you must regenerate the IP.
- Y indicates the IP includes new features. Regenerate your IP to include these new features.
- Z indicates the IP includes minor changes. Regenerate your IP to include these changes.

Related Information

- Introduction to Altera IP Cores
- Mailbox Client with Avalon® Streaming Interface Intel FPGA IP User Guide
- Errata for other IP cores in the Knowledge Base
- Intel Quartus Prime Design Suite Version 18.1 Update Release Notes

1.1. Mailbox Client with Avalon® Streaming Interface Intel FPGA IP v1.0.1

Table 1. v1.0.1 2020.10.05

<table>
<thead>
<tr>
<th>Intel Quartus Prime Version</th>
<th>Description</th>
<th>Impact</th>
</tr>
</thead>
<tbody>
<tr>
<td>20.3</td>
<td>Changed the name of this IP from Mailbox Avalon ST Client Intel FPGA IP to Mailbox Client with Avalon® Streaming Interface Intel FPGA IP.</td>
<td>This IP supports Intel Agilex™ devices. Use the new name to find this IP in the Intel Quartus Prime software version or on the web.</td>
</tr>
</tbody>
</table>

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*Other names and brands may be claimed as the property of others.
1.2. Mailbox Avalon ST Client Intel FPGA IP v1.0.0

Table 2. v1.0.0 2019.09.30

<table>
<thead>
<tr>
<th>Intel Quartus Prime Version</th>
<th>Description</th>
<th>Impact</th>
</tr>
</thead>
<tbody>
<tr>
<td>19.3</td>
<td>Initial release.</td>
<td>The Mailbox Avalon ST Client Intel FPGA IP provides a communication channel between your custom logic and the secure device manager (SDM) for Intel Agilex devices. You can use the Mailbox Avalon ST Client IP to send command packets and receive response packets from SDM peripheral modules.</td>
</tr>
</tbody>
</table>
## 2. Mailbox Client with Avalon Streaming Interface Intel FPGA IP User Guide Archives

If an IP core version is not listed, the user guide for the previous IP core version applies.

<table>
<thead>
<tr>
<th>Intel Quartus Prime Version</th>
<th>IP Core Version</th>
<th>User Guide</th>
</tr>
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<tbody>
<tr>
<td>20.1</td>
<td>1.0.0</td>
<td>Mailbox Avalon ST Client Intel FPGA IP User Guide</td>
</tr>
<tr>
<td>19.3</td>
<td>1.0.0</td>
<td>Mailbox Avalon ST Client Intel FPGA IP User Guide</td>
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