I.9 BCH/BCH FEC IP Core

General Features
• Compliant with G.975.1 Annex 9
• 8.5dB net coding gain at 10-15 output BER
• 10G rates and variable rates in between
• Generic CPU interface for control and monitoring

Statistics:
• corrected bits
• corrected ones
• corrected zeros
• corrected codes
• uncorrectable codes

Key Features
• Enables replacement of I.9 ASSPs
• Support for ASIC and FPGA implementation

Application Diagrams

Example Application: 10G

1-4G Clients
1 GE, OTU-1, OC48, FC1/24

FPGA

OTU-2 with (E)FEC

1-4G Clients
Related Products
For additional information regarding Altera OTN IP products visit www.altera.com.

Want to Dig Deeper?
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