I.7 BCH/BCH FEC IP Cores

**General Features**
- Compliant with G.975.1 Annex 7
- 8.02dB net coding gain at 10-15 output BER
- 10G, 40G, and variable rates in between
- Generic CPU interface for control and monitoring

**Statistics:**
- corrected bits
- corrected ones
- corrected zeros
- corrected codes
- uncorrectable codes

**Key Features**
- Enables replacement of I.7 ASSPs
- Support for ASIC and FPGA implementation
- Integrates seamlessly with other Altera IP cores

**Example Application: 10G**
1-4G Clients
1 GE, OTU-1, OC48, FC1/24

OTU-2 with (E)FEC

FPGA
Related Products
For additional information regarding Altera FPGA products and NTC OTN IP Products visit www.altera.com.

Want to Dig Deeper?
For more information about our I.7 BCH/BCH FEC IP Cores, please contact your Altera® sales representative or FAE, or visit www.altera.com.