



PROCESS CHANGE NOTIFICATION EPF10K50V DEVICE PROCESS TRANSITION

Altera's EPF10K50V devices will transition from a 0.30-micron triple metal layer to a 0.30-micron quad metal layer process using the same equipment and process flow. These devices will be pin-, function-, timing-, and programming file-compatible with existing 0.30-micron versions of the EPF10K50V devices.

Altera will begin the transition to the 0.30-micron quad metal layer process for all EPF10K50V ordering codes on January 31, 2000. After this date, customers may receive devices from either the quad or triple layer metal processes.

Devices produced on the 0.30-micron quad metal layer process can be distinguished by the second (β), fourth and fifth ($\alpha\alpha$) characters of the Altera lot number, which is marked on the bottom of the device, or by the characters preceding the Altera date code, which is marked on the top side of the device.

Lot Number	Topside Date Code
L β Z $\alpha\alpha$ #####	X β Z $\alpha\alpha$ YYWW

Device*	β	$\alpha\alpha$	Lot Number Example	Date Code Example
EPF10K50V	F	51	L <u>Fz51</u> #####	X <u>Fz51</u> YYWW

For additional information regarding the changes described in this document, contact your local Altera sales representative.

* Qualification data will be available September 14, 1999. Please contact Altera's Customer Quality Engineering Manager at (408) 544-7563 for more details.