



CUSTOMER ADVISORY

ADV2118

Updates to Package Outline Drawing of Intel® Field Programmable Gate Array (FPGA) Devices

Description:

Intel® is notifying customers of availability and updates to the Package Outline Drawing (POD) of “G” (RoHS 6 compliant) parts. See table below for the links to the download page for each family. The “G” parts POD will be identified by “(RoHS6 G Suffix)”.

Product Family	POD Link
Stratix IV	https://www.intel.com/content/www/us/en/programmable/support/literature/lit-index/lit-pkg/package.html?family=Stratix_IV
Stratix V	https://www.intel.com/content/www/us/en/programmable/support/literature/lit-index/lit-pkg/package.html?family=Stratix_V
Arria II GZ	https://www.intel.com/content/www/us/en/programmable/support/literature/lit-index/lit-pkg/package.html?family=Arria_II
Arria V GZ	https://www.intel.com/content/www/us/en/programmable/support/literature/lit-index/lit-pkg/package.html?family=Arria_V

Recommended Action:

Please update your Package Outline Drawings (POD) by looking up the part number and downloading “RoHS6 G Suffix” POD version from this search tool:

<https://www.intel.com/content/www/us/en/programmable/support/literature/lit-index/lit-pkg/package-search.html?type=search>

Products Affected:

Intel Stratix IV, Stratix V, Arria II GZ and Arria V GZ product families.

Note: No updates to Stratix III "G" parts POD.

The list of affected part numbers (OPNs) can be downloaded in Excel form:

<https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/pcn/adv2118-opn-list.xlsx>

Reason for Change:

Adjustment of Package Outline Drawing due to calculation methodology differences of multi-site assembly.

Contact:

For more information, please contact your local Sales representative, or submit a question or request at the My Intel support page, log-in via:

<https://www.intel.com/content/www/us/en/my-intel/fpga-sign-in.html>

Customer Notifications Subscription:

Customers that subscribe to the Intel FPGA customer notification mailing list will receive the notifications automatically via email.

If you would like to receive Intel FPGA customer notifications by email, you can register to the Intel FPGA program and set up your subscription at:

<https://www.intel.com/content/www/us/en/programmable/my-intel/mal-emailsub/technical-updates.html>

To submit a question or request at the My Intel support page, log-in via:

<https://www.intel.com/content/www/us/en/my-intel/fpga-sign-in.html>

Revision History:

Date	Rev	Description
04/30/2021	1.0.0	Initial Release

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