CUSTOMER ADVISORY
ADV2116

Intel® Arria® 10 Device, Intel® Stratix® 10 Device with L-tile, H-tile or E-tile Device and Intel Stratix® 10 E-tile Device Transceiver Updates

This is not a new ADV issuance. This is a revision of the previous ADV2116 release. See Revision History (last page) for details.

Description:

Intel® is notifying customers of important updates related to the transceivers of Intel® Arria® 10, Intel® Stratix® 10 L/H-tile, and Intel® Stratix® 10 E-tile devices.

It was determined that transceivers are not preserved in performance when the device is powered but remains unprogrammed for an extended period of time.

Recommended actions to preserve performance of the transceivers of a powered FPGA:

a) Program the FPGA as soon as practically possible and ensure that unused channels to be used later are protected using the QSF assignment PRESERVE_UNUSED_XCVR_CHANNEL.

<u>or</u>

b) If the FPGA is unprogrammed, ensure that it is only for a cumulative period less than 1 year at a junction temperature less than 60°C.

This update is also described in the following Knowledge Base (KDB) article:
https://www.intel.com/content/altera-www/global/en_us/index/support/support-resources/knowledge-base/hsio/2021/transceivers-are-not-preserved-when-the-device-remains-unprogram.html

For more information or support, please contact your local Sales representative, or submit a request at the My Intel support page. Log-in via:
https://www.intel.com/content/www/us/en/my-intel/fpga-sign-in.html

Products Affected:

Intel Arria 10,
Intel Stratix 10 L/H-tile
Intel Stratix 10 E-tile

The list of affected part numbers (OPNs) can be downloaded in Excel form:
https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/pcn/adv2116
-opn-list.xlsx

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Revision History

Date	Rev	Description
04/09/2021	1.0.0	Initial Release
04/16/2021	1.1.0	Updated second paragraph to add "is powered but" to clarify the condition.