



CUSTOMER ADVISORY

ADV2116

Intel® Arria® 10 Device, Intel® Stratix® 10 Device with L-tile, H-tile or E-tile Device and Intel Stratix® 10 E-tile Device Transceiver Updates

This is not a new ADV issuance. This is a revision of the previous ADV2116 release. See Revision History (last page) for details.

Description:

Intel® is notifying customers of important updates related to the transceivers of Intel® Arria® 10, Intel® Stratix® 10 L/H-tile, and Intel® Stratix® 10 E-tile devices.

It was determined that transceivers are not preserved in performance when the device is powered but remains unprogrammed for an extended period of time.

Recommended actions to preserve performance of the transceivers of a powered FPGA:

- a) Program the FPGA as soon as practically possible and ensure that unused channels to be used later are protected using the QSF assignment
PRESERVE_UNUSED_XCVR_CHANNEL.
- or
- b) If the FPGA is unprogrammed, ensure that it is only for a cumulative period less than 1 year at a junction temperature less than 60°C.

This update is also described in the following Knowledge Base (KDB) article:

https://www.intel.com/content/altera-www/global/en_us/index/support/support-resources/knowledge-base/hsio/2021/transceivers-are-not-preserved-when-the-device-remains-unprogram.html

For more information or support, please contact your local Sales representative, or submit a request at the My Intel support page. Log-in via:

<https://www.intel.com/content/www/us/en/my-intel/fpga-sign-in.html>

Products Affected:

Intel Arria 10,
Intel Stratix 10 L/H-tile
Intel Stratix 10 E-tile

The list of affected part numbers (OPNs) can be downloaded in Excel form:

<https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/pcn/adv2116-opn-list.xlsx>

Contact:

For more information, please contact your local Sales representative, or submit a question or request at the My Intel support page, log-in via:

<https://www.intel.com/content/www/us/en/my-intel/fpga-sign-in.html>

Customer Notifications Subscription:

Customers that subscribe to the Intel FPGA customer notification mailing list will receive the notifications automatically via email.

If you would like to receive Intel FPGA customer notifications by email, you can register to the Intel FPGA program and set up your subscription at:

<https://www.intel.com/content/www/us/en/programmable/my-intel/mal-emailsub/technical-updates.html>

Revision History

| Date | Rev | Description |
|------------|-------|--|
| 04/09/2021 | 1.0.0 | Initial Release |
| 04/16/2021 | 1.1.0 | Updated second paragraph to add "is powered but" to clarify the condition. |

©2021 Intel Corporation. All rights reserved. Intel, the Intel logo, Altera, Arria, Cyclone, Enpirion, Max, Nios, Quartus and Stratix words and logos are trademarks of Intel Corporation or its subsidiaries in the U.S. and/or other countries. Other marks and brands may be claimed as the property of others. Intel reserves the right to make changes to any products and services at any time without notice. Intel assumes no responsibility or liability arising out of the application or use of any information, product, or service described herein except as expressly agreed to in writing by Intel. Intel customers are advised to obtain the latest version of device specifications before relying on any published information and before placing orders for products or services.