



CUSTOMER ADVISORY

ADV2106

Cyclone® V Device and Arria® V Device Pin Connection Guidelines Update

Description:

Intel® is notifying customers of an update to the following documents:

- Cyclone® V Device Family Pin Connection Guidelines
- Arria® V GT, GX, ST, and SX Device Family Pin Connection Guidelines

Here are the updates listed in the Cyclone V Device Family Pin Connection Guidelines –

- Updated Connection Guidelines for SDMMC_D3
- Updated Figures 2, 7, and 8

Here is the update listed in the Arria V GT, GX, ST, and SX Device Family Pin Connection Guidelines –

- Updated Connection Guidelines for SDMMC_D3

The updated Cyclone V Device Family Pin Connection Guidelines can be found here:

<https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/dp/cyclone-v/pcg-01014.pdf>

The updated Arria V GT, GX, ST, and SX Device Family Pin Connection Guidelines can be found here:

<https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/dp/arriv-v/pcg-01013.pdf>

Recommended Actions:

Customers are requested to take note of the changes and determine the impact on their designs.

For questions or support, please contact your local Field Applications Engineer (FAE), or submit a question or request at the My Intel support page, log-in via:

<https://www.intel.com/content/www/us/en/my-intel/fpga-sign-in.html>

Products Affected:

All Cyclone V devices and Arria V GT, GX, ST, and SX devices.

The list of affected part numbers (OPNs) can be downloaded in Excel form:

<https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/pcn/adv2106-opn-list.xlsx>

Reason for Change:

Addition and clarification of certain device specifications based on latest available data and characterization. There is no change to Cyclone V device and Arria V device silicon and materials.

Change Implementation:

Table 1

Milestone	Availability
Availability of Cyclone V Device Family Pin Connection Guidelines Update	Now
Availability of Arria V GT, GX, ST, and SX Device Family Pin Connection Guidelines Update	Now

Contact:

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Customer Notifications Subscription:

Customers that subscribe to the Intel FPGA customer notification mailing list will receive the notifications automatically via email.

If you would like to receive Intel FPGA customer notifications by email, you can register to the Intel FPGA program and set up your subscription at:

<https://www.intel.com/content/www/us/en/programmable/my-intel/malemailsub/technical-updates.html>

Revision History

Date	Rev	Description
03/19/2021	1.0.0	Initial Release

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