



# CUSTOMER ADVISORY

## ADV2013

### Intel® Arria® 10 Device Documentation Update

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#### Description:

Intel® is notifying customers of important documentation updates for Intel Arria® 10 devices.

The following Intel Arria 10 device documents have been updated recently-

- Intel Arria 10 device datasheet
- Intel Arria 10 GX/GT Device Errata and Design Recommendations

The Intel Arria 10 device datasheet can be found here:

[https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/hb/arria-10/a10\\_datasheet.pdf](https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/hb/arria-10/a10_datasheet.pdf)

The Intel Arria 10 GX/GT Device Errata and Design Recommendations document can be found here:

<https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/es/es-1057.pdf>

Please review the revision history in the respective documents for the complete history of updates.

#### Recommended Actions:

Customers are requested to take note of the changes and determine the impact on their designs.

## Products Affected:

All Intel Arria 10 devices.

The list of affected part numbers (OPNs) can be downloaded in Excel form:

<https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/pcn/adv2013-opn-list.xlsx>

## Reason for Change:

Addition and clarification of certain device specifications based on latest available data and characterization. There is no change to the Intel Arria 10 product silicon and materials.

## Change Implementation:

Table 1

Milestone	Availability
Availability of Intel Arria 10 device datasheet update	Now
Availability of Intel Arria 10 GX/GT Device Errata and Design Recommendations	Now

## Contact:

For more information, please contact your local Field Applications Engineer (FAE), or submit a question or request at the My Intel support page, log-in via:

<https://www.intel.com/content/www/us/en/my-intel/fpga-sign-in.html>

## Customer Notifications Subscription:

Customers that subscribe to the Intel FPGA customer notification mailing list will receive the notifications automatically via email.

If you would like to receive Intel FPGA customer notifications by email, you can register to the Intel FPGA program and set up your subscription at:

<https://www.intel.com/content/www/us/en/programmable/my-intel/malemailsub/technical-updates.html>

## Revision History

Date	Rev	Description
04/03/2020	1.0.0	Initial Release

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