



CUSTOMER ADVISORY

ADV2010

Intel Acceleration Stack (IAS) for Xeon® CPUs with FPGAs update 1.2.1 for Intel® Programmable Acceleration Card (PAC) with Intel Arria®10 GX FPGA

Description:

Intel® is notifying customers regarding the release of a new update version 1.2.1 for the Intel Acceleration Stack (IAS) for Xeon® CPUs with FPGAs for Intel Programmable Acceleration Card with Intel Arria®10 GX FPGA. Update IAS 1.2.1 provides security updates, Intel Quartus® software version 19.2 support, and bug fixes. There are no physical changes to hardware with this update.

Download the latest IAS update version 1.2.1 here-

https://www.intel.com/content/www/us/en/programmable/products/boards_and_kits/dev-kits/altera/acceleration-card-arria-10-gx/getting-started.html

Table 1: Change Details

	Change From --> IAS 1.2	Change To --> IAS 1.2.1
Server Operating System Compatibility	Red Hat Enterprise Linux (RHEL) 7.4 Ubuntu 16.04 CentOS 7.4	RHEL 7.6 Ubuntu 18.04 No more CentOS support
FPGA Interface Manager (FIM) IP	FIM supported by Intel Quartus software v17.1.1	FIM supported by Intel Quartus software v19.2 Secure FIM (Authentication) Bug Fixes
Intel Quartus software and High Level Design (HLD)	Intel Quartus software and HLD version upgrade to v17.1.1	Intel Quartus software and HLD version upgrade to v19.2 Secure Acceleration Function Unit (AFU) Authentication

Board Management Controller (BMC) Firmware	BMC version 26889	BMC version 26895 Bug fixes Secure BMC Firmware Authentication (from Intel supplied by Bittware) Removed support for BittWare Monitor Tools
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Recommendations:

Intel recommends our FPGA PAC partners and customers to support IAS 1.2.1.

- Updating the PAC from IAS 1.2 to IAS 1.2.1 is irreversible.
- If your card currently has IAS 1.1, you will need to update your card to IAS 1.2 before updating to IAS 1.2.1
- There is no current planned date to ship PACs with IAS 1.2.1 preinstalled. Intel will continue to ship IAS 1.2 on PACs
- If you are a server provider:
 - Review the release notes, new security and signing features, and upgrade notes.
 - Please inform customers that IAS 1.2.1 is available and recommend their workload to run with IAS 1.2.1.
 - Please check for continued compatibility with IAS 1.2.
- If you are a system integrator / workload developer:
 - Workloads supporting IAS 1.2.1 will not work with IAS 1.2.
 - Check that your server provider will support IAS 1.2.1 on your server
 - Review the release notes, new security and signing features, and user guides.
- If you are a customer that has Intel PAC with Arria 10 GX running in your server please check with your Systems Integrator, OEM provider, and workload provider prior to installing IAS 1.2.1.

For more information on this change, please refer to the following link-

https://www.intel.com/content/www/us/en/programmable/products/boards_and_kits/dev-kits/altera/acceleration-card-arria-10-gx/documentation.html

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Products Affected:

Table 2:

Part Number	MMID	Pre-existing IAS Version
BD-ACD-10AX1152H	983136	1.2
BD-ACD-10AX1152B	999A65	1.2
BD-ACD-10AX1152D	983173	1.1/1.2
DK-ACB-10AX1152AES	980017	1.1/1.2
BD-ACD-10AX1152F	983135	1.1
BD-ACD-10AX1152A	980018	1.1

Reason for Change:

The reason for this change is twofold:

- To add authentication and integrity checks when updating the board remotely, including:
 - FPGA FIM updates (stored in Flash)
 - FPGA AFU (loaded to partial reconfiguration region of FPGA)
 - BMC Firmware updates (BMC Flash)
- To update/enhance features from prior IAS 1.2 release
 - Bug fixes from IAS 1.2
 - Customer-requested server OS compatibility updates
 - Compatibility with latest Intel Quartus software version 19.2 release

There is no change to the device silicon, board, and materials.

Change Implementation:

Milestone	Availability
Release of Intel Acceleration Stack (IAS) for Xeon CPUs with FPGAs update 1.2.1 for Intel Programmable Acceleration Card with Intel Arria10 GX FPGA	Now

Contact:

For more information, please contact your Field Applications Engineer (FAE), or submit a question or request at the My Intel support page, log-in via:

<https://www.intel.com/content/www/us/en/my-intel/fpga-sign-in.html>

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<https://www.intel.com/content/www/us/en/programmable/my-intel/malemailsub/technical-updates.html>

Revision History:

Date	Rev	Description
03/20/2020	1.0.0	Initial Release

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