



CUSTOMER ADVISORY

ADV1919

Intel® Stratix® 10 Device L-Tile and H-Tile Transceiver Calibration Code Firmware Update

Description:

Intel® Programmable Solutions Group is notifying customers of an important update to the Intel Stratix® 10 device L-Tile and H-Tile transceiver calibration code firmware. Please refer to the following Knowledge Base for more details:
https://www.intel.com/content/altera-www/global/en_us/index/support/support-resources/knowledge-base/hsio/2019/why-do-pma-registers-of-any-other-channels-might-get-corrupted-w.html

Customers are requested to review the recommended actions listed below if it is determined that their Intel Stratix 10 device is affected.

Recommended Actions:

If the Intel Stratix 10 device you are using is included in the affected part numbers list below, refer to Table 1 to determine the appropriate actions for the update:

Table 1:

Customer design version	Recommended actions
Intel Quartus® Prime software version 19.1 or version 19.3 Note: <i>For Intel Quartus Prime software version 19.2, see row 3 below</i>	To configure your affected Intel Stratix 10 device: Option 1: Download the Intel Quartus Prime Programmer version 19.3 Patch 0.15. Option 2: Update to the Intel Quartus Prime Programmer version 19.4 (or later).

	<p>Important information:</p> <ul style="list-style-type: none"> • Design recompilation is not required. • To convert .sof to other types of programming files such as pof, .rbf, .rpd, .jic, use the Quartus Prime Programming File Generator upon downloading Intel Quartus Prime Programmer patch version 19.3 or Intel Quartus Prime Programmer version 19.4 (or later).
Intel Quartus Prime software version 19.4 (or later)	Download Intel Quartus Prime Programmer version 19.4 (or later).
Other Intel Quartus Prime software version (including version 19.2)	Contact your local Field Applications Engineer (FAE) or submit a service request in the My Intel support page.

If a firmware update is not possible, or for questions or support, please contact your local Field Applications Engineer (FAE) or submit a service request in the [My Intel](#) support page.

Products Affected:

Intel Stratix 10 SX, GX, MX, TX L-Tile and H-Tile devices.

Please download the list of affected part numbers (OPNs) from the link below:

<https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/pcn/adv1919-opn-list.xlsx>

Reason for Change:

Due to a problem identified in the Intel® Stratix® 10 device L -Tile and H-Tile transceiver calibration code firmware, certain transceiver design use cases are affected. Refer to the following Knowledge Base for more details:

https://www.intel.com/content/altera-www/global/en_us/index/support/support-resources/knowledge-base/hsio/2019/why-do-pma-registers-of-any-other-channels-might-get-corrupted-w.html.

Change Implementation:

Table 2

Milestone	Available for Download on
Intel Quartus Prime Programmer version 19.3 Patch 0.15	December 6 th 2019
Intel Quartus Prime Programmer version 19.4	December 20 th 2019

Contact:

For more information, please contact your local Field Applications Engineer (FAE) or submit a Service Request at the [My Intel](#) support page.

Customer Notifications Subscription:

Customers that subscribe to Intel PSG's customer notification mailing list can receive the Customer Advisory automatically via email.

If you would like to receive customer notifications by email, please subscribe to our customer notification mailing list at:

<https://www.intel.com/content/www/us/en/programmable/my-intel/mal-emailsub/technical-updates.html>

Revision History

Date	Rev	Description
12/18/2019	1.0.0	Initial Release

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