

This document lists the Arria® V device family features that will be supported in the future in comparison with the currently supported features in the Quartus® II software version 13.1.

## Device Support

Table 1 lists the software support for each device variant.

**Table 1. Device Support for Arria V Devices**

Device Variant	Software Support
Arria V GX	Selected Arria V devices are supported in the Quartus II software version 13.1.
Arria V GT	
Arria V GZ	
Arria V SX	
Arria V ST	

## Features Support

Table 2 lists the transceiver clocking feature.

**Table 2. Transceiver Clocking Support for Arria V Devices**

Feature	Current Support	Future Support
Multichannel bonding	<ul style="list-style-type: none"> <li>Side-wide xN bonding for configurations using standard physical coding sublayer (PCS). Serial data rate support is up to 3.125 Gbps. (for Arria V GX, GT, SX, and ST devices)</li> <li>Side-wide xN bonding or multi-bank transceiver PLL feedback compensation bonding for configurations using standard PCS, PCI Express® (PCIe®) Gen1, Gen2, and Gen3 x2, x4, and x8 PCS (for xN bonding only), and 10G PCS. Serial data rate support is up to 12.5 Gbps. (for Arria V GZ devices)</li> </ul>	Side-wide multi-bank transceiver PLL feedback compensation bonding for configurations using standard PCS. Serial data rate support is up to 6.5536 Gbps. (for Arria V GX, GT, SX, and ST devices)

## Document Revision History

Table 3 lists the revision history for this document.

**Table 3. Document Revision History**

Date	Version	Changes
January 2014	1.5	Removed table: SoC HMC Support for Arria V Devices.
May 2013	1.4	<ul style="list-style-type: none"> <li>■ Updated Table 1 and Table 3.</li> <li>■ Removed the following tables:               <ul style="list-style-type: none"> <li>■ Configuration Support for Arria V Devices</li> <li>■ FPGA Fabric Support for Arria V Devices</li> <li>■ External Memory Support for Arria V Devices</li> </ul> </li> </ul>
November 2012	1.3	<ul style="list-style-type: none"> <li>■ Added Table 5.</li> <li>■ Updated Table 1, Table 2, Table 3, Table 4, and Table 6.</li> <li>■ Removed the following tables:               <ul style="list-style-type: none"> <li>■ Transceiver Protocol Configurations for Arria V Devices</li> <li>■ Transceiver Dynamic Reconfiguration Support for Arria V Devices</li> </ul> </li> </ul>
June 2012	1.2	Updated Table 1, Table 2, Table 3, Table 4, Table 5, Table 6, and Table 7.
February 2012	1.1	Updated Table 3, Table 5, Table 6, and Table 7.
November 2011	1.0	Initial release.