

Dedicated Pin	160-Pin PQFP (1), (2)	192-Pin PGA (2)	208-Pin RQFP/PQFP (3)
INPUT/GCLK1	139	P9	184
INPUT/GCLRn	141	R9	182
INPUT/OE1	140	T9	183
INPUT/OE2/GCLK2	142	U9	181
TDI (4)	146	U10	176
TMS (4)	23	H15	127
TCK (4)	98	H3	30
TDO (4)	135	U8	189
GND	3, 18, 32, 47, 57, 64, 66, 81, 96, 111, 126, 138, 143, 148	C7, C13, D4, D8, D10, G14, H4, K14, L4, P8, P10, P15, R4, R11	14, 32, 50, 72, 75, 82, 94, 116, 134, 152, 174, 180, 185, 200
VCCINT (5.0 V only)	56, 65, 137, 144	D7, D11, P7, P11	74, 83, 179, 186
VCCIO (3.3 V or 5.0 V)	10, 25, 40, 55, 74, 89, 103, 118, 133, 155	C5, C11, D14, G4, H14, K4, L14, P3, R5, R14	5, 23, 41, 63, 85, 107, 125, 143, 165, 191
No Connect (N.C.)	–	–	1, 2, 51, 52, 53, 54, 103, 104, 105, 106, 155, 156, 157, 158, 207, 208.
Total User I/O Pins (5)	132	164	164

LAB	MC	160-Pin PQFP (1), (2)	192-Pin PGA (2)	208-Pin RQFP/PQFP
A	1	2	U17	153
A	2	–	–	–
A	3	1	R16	154
A	4	–	–	–
A	5	160	P14	159
A	6	–	U16	160
A	7	–	–	–
A	8	159	R15	161
A	9	158	U15	162
A	10	–	–	–
A	11	157	T15	163
A	12	–	–	–
A	13	156	U14	164
A	14	–	U13	166
A	15	–	–	–
A	16	154	T14	167
B	17	12	N17	141
B	18	–	–	–
B	19	11	M16	142
B	20	–	–	–
B	21	9	M15	144
B	22	–	P17	145
B	23	–	–	–
B	24	8	N16	146
B	25	7	R17	147
B	26	–	–	–
B	27	6	P16	148
B	28	–	–	–
B	29	5	T17	149
B	30	–	N15	150
B	31	–	–	–
B	32	4	T16	151
C	33	39	B17	108
C	34	–	–	–
C	35	38	C15	109
C	36	–	–	–
C	37	37	C17	110
C	38	–	C16	111
C	39	–	–	–
C	40	36	D17	112
C	41	35	D15	113
C	42	–	–	–
C	43	34	E17	114
C	44	–	–	–
C	45	33	D16	115
C	46	–	E15	117
C	47	–	–	–
C	48	31	F16	118

LAB	MC	160-Pin PQFP (1), (2)	192-Pin PGA (2)	208-Pin RQFP/PQFP
D	49	49	A14	92
D	50	–	–	–
D	51	48	B12	93
D	52	–	–	–
D	53	46	B13	95
D	54	–	A15	96
D	55	–	–	–
D	56	45	B14	97
D	57	44	A16	98
D	58	–	–	–
D	59	43	C14	99
D	60	–	–	–
D	61	42	B16	100
D	62	–	B15	101
D	63	–	–	–
D	64	41	A17	102
E	65	153	U12	168
E	66	–	–	–
E	67	152	R13	169
E	68	–	–	–
E	69	151	U11	170
E	70	–	T13	171
E	71	–	–	–
E	72	150	T11	172
E	73	149	T12	173
E	74	–	–	–
E	75	147	R12	175
E	76	–	–	–
E	77	146 (4)	U10 (4)	176 (4)
E	78	–	R10	177
E	79	–	–	–
E	80	145	T10	178
F	81	21	J16	130
F	82	–	–	–
F	83	20	J15	131
F	84	–	–	–
F	85	19	K17	132
F	86	–	J14	133
F	87	–	–	–
F	88	17	K16	135
F	89	16	K15	136
F	90	–	–	–
F	91	15	L17	137
F	92	–	–	–
F	93	14	L16	138
F	94	–	M17	139
F	95	–	–	–
F	96	13	L15	140

LAB	MC	160-Pin PQFP (1), (2)	192-Pin PGA (2)	208-Pin RQFP/PQFP
G	97	30	E16	119
G	98	–	–	–
G	99	29	F17	120
G	100	–	–	–
G	101	28	F15	121
G	102	–	G16	122
G	103	–	–	–
G	104	27	G15	123
G	105	26	G17	124
G	106	–	–	–
G	107	24	H17	126
G	108	–	–	–
G	109	23 (4)	H15 (4)	127 (4)
G	110	–	J17	128
G	111	–	–	–
G	112	22	H16	129
H	113	60	C9	79
H	114	–	–	–
H	115	59	D9	80
H	116	–	–	–
H	117	58	C10	81
H	118	–	A10	84
H	119	–	–	–
H	120	54	A11	86
H	121	53	B10	87
H	122	–	–	–
H	123	52	A12	88
H	124	–	–	–
H	125	51	B11	89
H	126	–	A13	90
H	127	–	–	–
H	128	50	C12	91
I	129	128	U6	197
I	130	–	–	–
I	131	129	T5	196
I	132	–	–	–
I	133	130	U7	195
I	134	–	T6	194
I	135	–	–	–
I	136	131	T7	193
I	137	132	R6	192
I	138	–	–	–
I	139	134	R7	190
I	140	–	–	–
I	141	135 (4)	U8 (4)	189 (4)
I	142	–	R8	188
I	143	–	–	–
I	144	136	T8	187

LAB	MC	160-Pin PQFP (1), (2)	192-Pin PGA (2)	208-Pin RQFP/PQFP
J	145	100	J2	27
J	146	–	–	–
J	147	101	J3	26
J	148	–	–	–
J	149	102	K1	25
J	150	–	J4	24
J	151	–	–	–
J	152	104	K2	22
J	153	105	K3	21
J	154	–	–	–
J	155	106	L1	20
J	156	–	–	–
J	157	107	L2	19
J	158	–	M1	18
J	159	–	–	–
J	160	108	L3	17
K	161	91	F3	38
K	162	–	–	–
K	163	92	F1	37
K	164	–	–	–
K	165	93	E2	36
K	166	–	G2	35
K	167	–	–	–
K	168	94	G3	34
K	169	95	G1	33
K	170	–	–	–
K	171	97	H1	31
K	172	–	–	–
K	173	98 (4)	H3 (4)	30 (4)
K	174	–	J1	29
K	175	–	–	–
K	176	99	H2	28
L	177	61	B9	78
L	178	–	–	–
L	179	62	C8	77
L	180	–	–	–
L	181	63	A9	76
L	182	–	A8	73
L	183	–	–	–
L	184	67	A7	71
L	185	68	B8	70
L	186	–	–	–
L	187	69	A6	69
L	188	–	–	–
L	189	70	B7	68
L	190	–	A5	67
L	191	–	–	–
L	192	71	C6	66

LAB	MC	160-Pin PQFP (1), (2)	192-Pin PGA (2)	208-Pin RQFP/PQFP
M	193	119	U1	4
M	194	–	–	–
M	195	120	R2	3
M	196	–	–	–
M	197	121	R3	206
M	198	–	U2	205
M	199	–	–	–
M	200	122	P4	204
M	201	123	U3	203
M	202	–	–	–
M	203	124	T3	202
M	204	–	–	–
M	205	125	U4	201
M	206	–	U5	199
M	207	–	–	–
M	208	127	T4	198
N	209	109	N1	16
N	210	–	–	–
N	211	110	M2	15
N	212	–	–	–
N	213	112	M3	13
N	214	–	P1	12
N	215	–	–	–
N	216	113	N2	11
N	217	114	R1	10
N	218	–	–	–
N	219	115	P2	9
N	220	–	–	–
N	221	116	T1	8
N	222	–	N3	7
N	223	–	–	–
N	224	117	T2	6
O	225	82	B1	49
O	226	–	–	–
O	227	83	C3	48
O	228	–	–	–
O	229	84	C1	47
O	230	–	D3	46
O	231	–	–	–
O	232	85	D1	45
O	233	86	C2	44
O	234	–	–	–
O	235	87	E1	43
O	236	–	–	–
O	237	88	E3	42
O	238	–	D2	40
O	239	–	–	–
O	240	90	F2	39

LAB	MC	160-Pin PQFP (1), (2)	192-Pin PGA (2)	208-Pin RQFP/PQFP
P	241	72	A4	65
P	242	–	–	–
P	243	73	B6	64
P	244	–	–	–
P	245	75	B5	62
P	246	–	A3	61
P	247	–	–	–
P	248	76	B4	60
P	249	77	A2	59
P	250	–	–	–
P	251	78	C4	58
P	252	–	–	–
P	253	79	B2	57
P	254	–	B3	56
P	255	–	–	–
P	256	80	A1	55

Notes:

- (1) A complete thermal analysis should be performed before committing a design to this device package. See the *Operating Requirements for Altera Devices Data Sheet* for more information.
- (2) EPM7256S devices is not available in the 160-pin PQFP package.
- (3) EPM7256E devices are not available in the 208-pin RQFP/PQFP packages.
- (4) This JTAG pin applies to MAX 7000S devices only and this pin may function as either a JTAG port or a user I/O pin. If the device is configured to use the JTAG ports for ISP, this pin is not available as a user I/O pin.
- (5) The user I/O pin count includes dedicated input pins and all I/O pins.

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