



up 51%

Higher OpenFOAM Performance

3rd Gen Intel® Xeon® Scalable Processor versus Prior Gen¹ up 34%

Competitive Advantage

2nd Gen Intel® Xeon® Platinum 9200 series versus AMD EPYC Milan Processor²

### Addressing Computational Fluid-dynamics Challenges

Design engineers use computational fluid dynamics (CFD) software to simulate and analyze how products will perform as gas and liquid flow around them. These workloads involve complex, unstructured meshes with tens of millions of cells, and they demand high memory bandwidth for solvers to perform efficiently.

Intel's outstanding hardware, software, and ecosystem help manufacturers design better products faster and within budget. With increases in memory bandwidth, system memory, and instructions per clock (IPC), Intel® Xeon® Scalable processors deliver outstanding OpenFOAM performance compared to previous-generation processors. For other applications, Intel® Xeon® Scalable processors can offer increased vectorization with AVX-512 and choice of core frequency with Intel® Speed Select Technology.

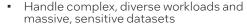


#### Key 3rd Generation Intel® Xeon® Scalable Processor Features

- 8 DDR4 memory channels, 3,200 MT/s
- Up to 20% higher instructions per clock versus prior generation<sup>3</sup>
- Built-in Al inference and training acceleration with Intel® Deep Learning Boost
- Built-in acceleration with Intel® AVX-512

### **Intel HPC Leadership**

### Intel's unmatched portfolio and broad ecosystem help users:



- Achieve greater throughput with increased system utilization and availability
- Drive insight and discovery via accelerated computing



## Outstanding Performance

With eight DDR4 memory channels, up to 40 cores per socket, increased cache sizes, and up to **20%** increase in instructions per clock compared to the previous generation of processors,<sup>3</sup> 3rd Gen Intel® Xeon® Scalable processors deliver exceptional performance for a range of applications. The latest generation is configured to support up to 6 TB of system memory per processor with support for Intel® Optane™ persistent memory (PMem) 200 series.

Built-in Intel® Speed Select Technology, plus a special SKU engineered for liquid cooled systems, provides

### **Built-in Acceleration**



Only Intel® Xeon® processors support Intel® AVX-512 (512-bit register) instructions for **2x** the instructions completed per cycle versus Intel® AVX2 (256-bit register). While not directly related to OpenFOAM performance, this is a key differentiator on other key workloads.

Performance varies by use, configuration, and other factors. Learn more at <a href="http://www.intel.com/PerformanceIndex">http://www.intel.com/PerformanceIndex</a>
Performance results are based on testing as of dates shown in configurations and may not reflect all publicly available updates. See backup for configuration details. No product or component can be absolutely secure.

Your costs and results may vary.

Intel technologies may require enabled hardware, software, or service activation

Intel® Advanced Vector Extensions (Intel® AVX) provides higher throughput to certain processor operations. Due to varying processor power characteristics, utilizing AVX instructions may cause some parts to operate at less than the rated frequency and b) some parts with Intel® Turbo Boost Technology 2.0 to not achieve any or maximum turbo frequencies. Performance varies depending on hardware, software, and system configuration and you can learn more at <a href="http://www.intel.com/go/turbo">http://www.intel.com/go/turbo</a>

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# **Unmatched Ecosystem**

unparalleled flexibility.



Intel has engaged for decades with a broad ecosystem of software engineers and organizations, ensuring that both open source and commercial applications perform better on Intel® architecture. As a result, users can reduce project timelines and reach insights faster—and developers can simplify their optimization efforts.

### Choosing the Perfect Intel® Processor for OpenFOAM

OpenFOAM is distributed as open-source code by the OpenFOAM Foundation under the terms of the GNU General Public License. OpenFOAM is developed by multiple contributors led by Henry Weller, creator of OpenFOAM and Director of CFD Direct and The OpenFOAM Foundation. The current version can be downloaded at <a href="https://openfoam.org/download/">https://openfoam.org/download/</a>. Downloads are available for Ubuntu Linux, other Linux distributions, and Windows or macOS versions of the software. Users can download and compile OpenFOAM from source code as well. In addition to the Foundation, there are several other companies writing for OpenFOAM.

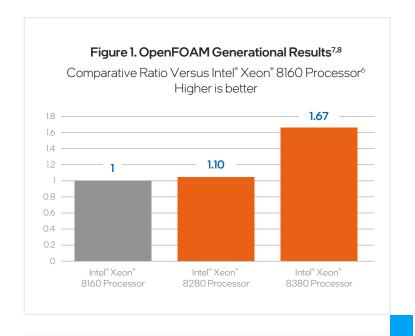
OpenFOAM is highly memory bound, so memory bandwidth is a key consideration. Choosing a moderate core count and frequency SKU is ideal for product engineers running multiple types of workloads and applications on the same system as OpenFOAM. To reduce system footprint, some users choose lower corecount CPUs with higher frequencies. When performance is paramount, many users choose the higher core counts available with Intel® Xeon® Platinum 8300 processors or Intel® Xeon® Platinum AP processors, which also offer unapparelled memory bandwidth (12 channels, versus 8 channels from the closest competitor).



For more information about Intel® Xeon® Scalable processors for HPC, visit Intel.com/content/www/us/en/high-performance-computing/processors.html.

For details on Intel® software tools and libraries, visit intel.com/content/www/us/en/software/software-overview.html.

For more information about OpenFOAM, visit <a href="https://openfoam.org">https://openfoam.org</a>.



### Conclusion

Intel® architecture offers an outstanding combination of increased memory bandwidth and instructions per clock, in addition to software optimizations, resulting in optimized performance for OpenFOAM users. 3<sup>rd</sup> Gen Intel® Xeon® Scalable processors offer increased core counts over previous-generation processors, in addition to support for Intel® Optane™ PMem 200 series.

OpenFOAM is a very memory-bandwidth-sensitive code, and 3rd Gen Intel® Xeon® processors offer 33% more memory channels and 9% faster with DDR4 3,200 MT/S.⁴ In addition, OpenFOAM is tuned by using Intel® tools to run on Intel® architecture and employ Intel® oneAPI math kernel library (MKL)—the fastest and most widely used math library for Intel-based systems—to simplify development and help ensure that new instruction-set⁵ architectures (ISAs) just work.

- $\textbf{1.} \hspace{0.2cm} \textbf{For workloads and configurations, see [108] at $\underline{\textbf{https://edc.intel.com/content/www/us/en/products/performance/benchmarks/3rd}$ Results may vary. }$
- 3. Up to 20% IPC improvement: 3rd Gen Intel" Xeon" Scalable Processor: 1-node, 2x 28-core 3rd Gen Intel" Xeon" Scalable Processor; Intel® Reference Platform, 512GB (16 slots / 32GB / 3200) total DDR4 memory, HT on, ucode=x270, RHEL 8.0, Kernel Version 4.18.0-80.el8.x86\_64, test by Intel on 3/30/2021. 2nd Gen Intel® Xeon" Scalable Processor: 1-node, 2x 28-core 2nd Gen Intel® Xeon" Scalable Processor, Intel® Reference Platform, 384GB (12 slots / 32GB / 2933) total DDR4 memory, HT on, ucode=x2f0 (RHEL 8.0, Kernel Version 4.18.0-80.el8.x86\_64, test by Intel on 3/30/2021. SPECrate2017\_int\_base (est). Tests at equal core frequency, equal uncore frequency, equal compiler.
- $\textbf{4.}\ \ 3 \text{rd} \\ \text{Gen Intel} \\ \text{"Xeon" Scalable Processors with Up to 8 channels DDR43200 MT/s vs} \\ \text{$6$ channels DDR42900MT/s on 2nd Gen Intel} \\ \text{"Xeon" Scalable Processors with Up to 8 channels DDR43200 MT/s vs} \\ \text{$6$ channels DDR42900MT/s on 2nd Gen Intel} \\ \text{$7$ channels DDR4290MT/s on 2nd Gen Intel} \\ \text{$7$ channels DDR4290M$
- 5. #1 Math Library (fastest and most-used math library for Intel® processor-based systems): See <a href="https://www.intel.com/content/www/us/en/developer/tools/oneapi/oneawkl.html">https://www.intel.com/content/www/us/en/developer/tools/oneapi/oneawkl.html</a>
- 7. 2nd Gen Intel® Xeon® 8280 Processor (ww27'20)/OpenFOAM: App Version: v7; Data collected: 7/20/2020; Build notes: Tools: Intel® FORTRAN Compiler 2019u5, Intel® C Compiler 2019u5, Intel® MPI 2019u7; threads/core: 1; Turbo: ON; Build knobs: -O3 -ip -xCORE-AVX512 2nd Gen Intel® Xeon® 8280 Processor; Config Date: 7/13/2020; Platform:; CPU Details:; # CPU Sockets: 2; # CPU Cores: 28; CPU Base Frequency: -2.7 GHz; CPU Max Frequency: -4.0 GHz; CPU Base TDP: 205 W; RAN\*: 192GB, 12x16GB 2933MHz DDR4, Hynix, HMA82GR7CJR8N WM; Hard Drive: 800GB ATA INTEL® SSDSC2BA80 (, sos; Cluster File System: OPA based Lustre; BiOS: SE5C620.868.0.2.0.1.0.011.032620200659; BIOS Settings: HT=on TURBO=ON, SNC=disabled: fluorerocode: 0x500002c; Intel® Management: 04.01.04.339; BMC: 242; Security Vulnerabilities And Mitigations: itilb\_multihit; Processor vulnerable, Illf: Not affected, mds; : Not affected, meltdown: Not affected, spec\_store\_bypass; intel® to special via seconp, spectre\_VI: Mitigation: Load fences, usercopy swapgs, barriers and \_user pointer sanitization, spectre\_VI: Mitigation: Clear CPU buffers; SMT, vulnerable; Operating System: Base installation Oracle Linux Server 7.6, updated, to CentOS Linux 7.7.1908, (compatible with RHEL; Kernel: 3.10.0-1127.13.1.el7.crt1x86\_64; OPED stack: OPA 10.10.2.0.44, Lustre 2.10.8, default; Ethernet Switch: Intel® OPA Edge Switch 100 Series 48 Port; Host Bus Adapter: 100 Gbps; Intel® OPA 1 port, PCIe x16.
- 8. Intel® Xeon® Platinum 8380 Processor; Config Date: 12/5/2021; Platform: Coyote Pass; CPU Details: 2 CPUs per node; Stepping: 6; 40c @ 2.3Ghz; # CPU Sockets: 2; # CPU Cores: 40; CPU Base Frequency: 2.3 GHz; CPU Max Frequency: 3.4 GHz; CPU Base TDP: 270 W; RAM: 256GB 16\*16GB 3200MT/s DDR4, Hynix HMA82GR7CJR8N-XH; Hard Drive: SSDSC2KG96 960GB; Cluster File System: HDR based Lustre; BIOS: SESC6200.86B.0020 P23.210326309; BIOS 56ttings: HT=on TURBO=ON; Microcode: 0xd000270; Intel® Management Engine: 04.04.04.058; BMC: 278; Security Vulnerabilities And Mitigations: filb\_multihit. Not affected: diff. Not affected; microcode in the floading of the floating of the flo
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