

If you are upgrading your Arria 10 PCIe Gen3 IP from Quartus® Prime 16.0 or earlier to Quartus Prime 16.0.1 or newer, follow the instructions below to update the Preset requested by the PCIe HIP.

1) Edit the qsys file for your Arria 10 PCIe hard IP instantiation with a text editor and change the Gen3 preset parameter from

```
<parameter name="gen3_coeff_1_hwtcl" value="9" />
```

to

```
<parameter name="gen3_coeff_1_hwtcl" value="8" />
```

After making this change, open the qsys file in Quartus Prime and regenerate the PCIe hard IP core with the updated parameter.

2) Compile your design with Quartus Prime.