



Intel® Server Board S2600CW Product Family

Monthly Specification Update

April 2017

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Revision History

Date	Modifications
October, 2016	MSU program re-introduced Standard format introduced Added new Product Codes and updated FW stack EPSD case number introduced
November, 2016	No updates
December, 2016	No updates
January, 2017	System FW posted on support site updated Motherboard TA's updated Configuration Guide updated
February, 2017	Technical Product Specification updated
March, 2017	Technical Product Specification updated to v2.4 Added product errata number 2
April, 2017	S2600CWTR and S2600CWTSR PBA and Firmware revision numbers updated Service Guide updated to v1.3

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1 Preface

This document is intended to communicate product errata, published specification changes, published specification clarifications, and published document changes for the following Intel server products:

- Intel® Server Board S2600CW product family

It is intended for system integrators and software developers of applications, operating systems, or tools.

Nomenclature

1. **Specification Changes** are modifications to the current published specifications for Intel server boards. These changes will be incorporated in the next release of the specifications.
2. **Specification Clarifications** describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.
3. **Documentation Changes** include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.
4. **Errata** are design defects or errors. Errata may cause the server board or system behavior to deviate from published specifications. Hardware and software designed to be used with any given processor stepping must assume that all errata documented for that processor stepping are present on all devices.

2 Product Scope

The following specific boards, BIOS, and components are covered by this update:

Product Code	Baseboard PBA Revision	BIOS Revision	ME Revision	BMC Revision	FRU/SDR Revision
S2600CW2R	H12881-271	01.01.0016	03.01.03.021	01.43.9685	1.13
S2600CW2SR	H12883-271	01.01.0016	03.01.03.021	01.43.9685	1.13
S2600CWTR	H12882-272	01.01.0016	03.01.03.021	01.43.9685	1.13
S2600CWTSR	H12884-272	01.01.0016	03.01.03.021	01.43.9685	1.13

3 Summary Tables of Changes

The following tables provide an overview of known errata and known document changes that apply to the specified Intel server products. The tables use the following notations:

- Doc:** Intel intends to update the appropriate documentation in a future revision.
- Fix:** Intel intends to fix this erratum in the future.
- Fixed:** This erratum has been previously fixed.
- No Fix:** There are no plans to fix this erratum.
- Shaded:** This erratum is either new or has been modified from the previous specification update.

Table 1. Errata Summary

No.	Plans	Description of Errata
1.	Fix	MCE messages regarding QPI in Linux OS
2.	No Fix	The SEL log cannot identify "CATERR due to CPU 3-strike timeout" 0x04h value in ED2 events.

Table 2. Documentation and Firmware Changes

No.	Document Name	Description of Documentation Change
1.	Technical Product Specification v2.3	Updated to v2.3. Please refer to the document for details.
2.	System Update Package for EFI and System Update Package for OFU and WinPE released	BIOS 01.01.0019 ME 03.01.03.032 BMC 01.47.10181 FRU/SDR 1.13
3.	Intel® Server Board S2600CW Configuration Guide	Updated to v2.3. Please refer to the document for details.
4.	Intel® Server Board S2600CW Technical Product Specification	Updated to v2.4. Please refer to the document for details.
5.	Service Guide for Intel® Server Board S2600CW Family	Updated to v1.3. Please refer to section 5. Documentation Changes for details.

The following sections provide in-depth descriptions of each erratum/documentation change indicated in the tables above. The errata and documentation change numbers referenced in the following sections correspond to the numbers in the tables above.

4 Product Errata

1. ID Number EPSD100254244

MCE messages regarding QPI in Linux OS

Problem	When using Linux OS on Intel Server Products for Xeon E5 2600 CPUs with BIOS 01.01.0016 or later customer might receive a Hardware Event as a Machine Check Error Message regarding Intel QPI link layer detected control error from R3QPI
Implication	MCE logged on the mcelog
Status	This issue was caused due to BIOS incorrectly enabling an Intel Xeon E5 2600 V4 CPU feature when customer was using Intel Xeon E5 2600 V3 CPUs. The feature has been disabled when the platform detects customer is using Intel Xeon 2600 V3 CPUs.
Solution	Update Platform BIOS to 01.01.0019 release

2. ID Number N/A

Removed “ED2 – 4: CATERR due to CPU 3-strike timeout” from CATERR Sensor section

Problem	Hardware-enhanced diagnostics and fault isolation is not supported by current generation BMC
Implication	The SEL log cannot identify “CATERR due to CPU 3-strike timeout” 0x04h value in ED2 events
Status	Root cause identified. ME firmware doesn't support this feature.
Solution	Errata publishing on Technical Product Specification v2.4 Removed “ED2 – 4: CATERR due to CPU 3-strike timeout” from CATERR Sensor section

5 Documentation Changes

1. Intel® Server Board S2600CW Product Family Technical Product Specification updated to v2.3
2. System Update Packages for EFI and OFU/WinPE released. The Update packages include the following FW version: BIOS 01.01.0019, ME 03.01.03.032, BMC 01.47.10181 and FRUSDR 1.13
3. Intel® Server Board S2600CW Product Family Technical Product Specification updated to v2.4
4. System Integration and Service Guide v1.3 has a section (3.4) added for AXRSBBU9 Battery Backup Unit installation.