Intel® Server Board S2600CW Product Family

Monthly Specification Update

March 2017

Intel® Server Products and Solutions
<table>
<thead>
<tr>
<th>Date</th>
<th>Modifications</th>
</tr>
</thead>
</table>
| October, 2016   | MSU program re-introduced  
                              Standard format introduced  
                              Added new Product Codes and updated FW stack  
                              EPSD case number introduced |
| November, 2016  | No updates                                                                   |
| December, 2016  | No updates                                                                   |
| January, 2017   | System FW posted on support site updated  
                              Motherboard TA’s updated  
                              Configuration Guide updated |
| February, 2017  | Technical Product Specification updated                                       |
| March, 2017     | Technical Product Specification updated to v2.4  
                              Added product errata number 2 |
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The products and services described may contain defects or errors known as errata which may cause deviations from published specifications. Current characterized errata are available on request.

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1 Preface

This document is intended to communicate product errata, published specification changes, published specification clarifications, and published document changes for the following Intel server products:

- Intel® Server Board S2600CW product family

It is intended for system integrators and software developers of applications, operating systems, or tools.

Nomenclature

1. **Specification Changes** are modifications to the current published specifications for Intel server boards. These changes will be incorporated in the next release of the specifications.

2. **Specification Clarifications** describe a specification in greater detail or further highlight a specification’s impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.

3. **Documentation Changes** include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

4. **Errata** are design defects or errors. Errata may cause the server board or system behavior to deviate from published specifications. Hardware and software designed to be used with any given processor stepping must assume that all errata documented for that processor stepping are present on all devices.
## 2 Product Scope

The following specific boards, BIOS, and components are covered by this update:

<table>
<thead>
<tr>
<th>Product Code</th>
<th>Baseboard PBA Revision</th>
<th>BIOS Revision</th>
<th>ME Revision</th>
<th>BMC Revision</th>
<th>FRU/SDR Revision</th>
</tr>
</thead>
<tbody>
<tr>
<td>S2600CW2R</td>
<td>H12881-271</td>
<td>01.01.0016</td>
<td>03.01.03.021</td>
<td>01.43.9685</td>
<td>1.13</td>
</tr>
<tr>
<td>S2600CW2SR</td>
<td>H12883-271</td>
<td>01.01.0016</td>
<td>03.01.03.021</td>
<td>01.43.9685</td>
<td>1.13</td>
</tr>
<tr>
<td>S2600CWTR</td>
<td>H12882-271</td>
<td>01.01.0014</td>
<td>03.01.03.005</td>
<td>01.33.8932</td>
<td>1.11</td>
</tr>
<tr>
<td>S2600CWTSR</td>
<td>H12884-270</td>
<td>01.01.0014</td>
<td>03.01.03.005</td>
<td>01.33.8932</td>
<td>1.11</td>
</tr>
</tbody>
</table>
3 Summary Tables of Changes

The following tables provide an overview of known errata and known document changes that apply to the specified Intel server products. The tables use the following notations:

- **Doc:** Intel intends to update the appropriate documentation in a future revision.
- **Fix:** Intel intends to fix this erratum in the future.
- **Fixed:** This erratum has been previously fixed.
- **No Fix:** There are no plans to fix this erratum.
- **Shaded:** This erratum is either new or has been modified from the previous specification update.

### Table 1. Errata Summary

<table>
<thead>
<tr>
<th>No.</th>
<th>Plans</th>
<th>Description of Errata</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>Fix</td>
<td>MCE messages regarding QPI in Linux OS</td>
</tr>
<tr>
<td>2.</td>
<td>No Fix</td>
<td>The SEL log cannot identify “CATERR due to CPU 3-strike timeout” 0x04h value in ED2 events.</td>
</tr>
</tbody>
</table>

### Table 2. Documentation and Firmware Changes

<table>
<thead>
<tr>
<th>No.</th>
<th>Document Name</th>
<th>Description of Documentation Change</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>Technical Product Specification v2.3</td>
<td>Updated to v2.3. Please refer to the document for details.</td>
</tr>
</tbody>
</table>
| 2.  | System Update Package for EFI and System Update Package for OFU and WinPE released | BIOS 01.01.0019  
ME 03.01.03.032  
BMC 01.47.10181  
FRU/SDR 1.13 |
| 3.  | Intel® Server Board S2600CW Configuration Guide | Updated to v2.3. Please refer to the document for details |
| 4.  | Intel® Server Board S2600CW Technical Product Specification | Updated to v2.4. Please refer to the document for details |

The following sections provide in-depth descriptions of each erratum/documentation change indicated in the tables above. The errata and documentation change numbers referenced in the following sections correspond to the numbers in the tables above.
4 Product Errata

1. ID Number  EPSP100254244

MCE messages regarding QPI in Linux OS

Problem When using Linux OS on Intel Server Products for Xeon E5 2600 CPUs with BIOS 01.01.0016 or later customer might receive a Hardware Event as a Machine Check Error Message regarding Intel QPI link layer detected control error from R3QPI

Implication MCE logged on the mcelog

Status This issue was caused due to BIOS incorrectly enabling an Intel Xeon E5 2600 V4 CPU feature when customer was using Intel Xeon E5 2600 V3 CPUs. The feature has been disabled when the platform detects customer is using Intel Xeon 2600 V3 CPUs.

Solution Update Platform BIOS to 01.01.0019 release

2. ID Number  N/A

Removed “ED2 – 4: CATERR due to CPU 3-strike timeout” from CATERR Sensor section

Problem Hardware-enhanced diagnostics and fault isolation is not supported by current generation BMC

Implication The SEL log cannot identify “CATERR due to CPU 3-strike timeout” 0x04h value in ED2 events

Status Root cause identified. ME firmware doesn’t support this feature.

Solution Errata publishing on Technical Product Specification v2.4 Removed “ED2 – 4: CATERR due to CPU 3-strike timeout” from CATERR Sensor section
5  Documentation Changes

1. Intel® Server Board S2600CW Product Family Technical Product Specification updated to v2.3
2. System Update Packages for EFI and OFU/WinPE released. The Update packages include the following FW version:
   BIOS 01.01.0019, ME 03.01.03.032, BMC 01.47.10181 and FRUSDR 1.13
3. Intel® Server Board S2600CW Product Family Technical Product Specification updated to v2.4