



Motherboard Logo Program (MLP)

Intel® Desktop Board

DG33BU

MLP Report

4/9/2008

Purpose:

This report describes the DG33BU Motherboard Logo Program testing run conducted by Intel Corporation.

THIS TEST REPORT IS PROVIDED "AS IS" WITH NO WARRANTIES WHATSOEVER, INCLUDING ANY WARRANTY OF MERCHANTABILITY, NONINFRINGEMENT FITNESS FOR ANY PARTICULAR PURPOSE, OR ANY WARRANTY OTHERWISE ARISING OUT OF ANY PROPOSAL, SPECIFICATION OR SAMPLE.

Information in this document is provided in connection with Intel® products. No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by the sale of Intel products. Except as provided in Intel's Terms and Conditions of Sale for such products, Intel assumes no liability whatsoever, and Intel disclaims any express or implied warranty, relating to sale and/or use of Intel products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right. Intel products are not intended for use in medical, life saving, or life sustaining applications.

Intel retains the right to make changes to its test specifications at any time, without notice.

The hardware vendor remains solely responsible for the design, sale and functionality of its product, including any liability arising from product infringement or product warranty.

Intel and Pentium are registered trademarks of Intel Corporation in the United States and other countries.

* Other names and brands may be claimed as the property of others.

Copyright © 2007 Intel Corporation. All rights reserved.

Contents

Introduction	4
Terms and Definitions.....	4
Desktop Board Configuration	5
Desktop Board DG33BU Final Configuration Report: Completion of MLP	5
Board Information.....	5
Product Code.....	5
Processor	5
Power Management.....	5
Operating System Tested.....	5
Onboard Integrated Devices and Driver	6
Driver Test Manager Used (DTM).....	7
Errata and Contingencies.....	8
Test Notes.....	9

Introduction

Terms and Definitions

Term	Definitions
WHQL	Windows* Hardware Qualification Lab
DTM	Driver Test Manager
MLP	Motherboard Logo Program. For further information see: http://www.microsoft.com/whdc/hwtest/default.msp
AP Machine	Audio Precision Machine
Winqual	Windows Qualification
MSFT Tested Product List	Tested Products List. You can view the Windows Marketplace for tested products list at: http://winqual.microsoft.com/HCL/ProductList.aspx?m=v&cid=105&q=s

Desktop Board Configuration

Desktop Board DG33BU Final Configuration Report: Completion of MLP

Data in this section reflects system configuration at time of MLP submission.

Board Information

Product Code ¹	BIOS String/Model	Technologies NOT Logo'd (yet)
DB33BU	DPP3510J.86A.0407.2008.0218.0923	N/A - all technologies logo'd
Processor		
Speed	2.66GHz	
Family	Intel® Core™2 Quad	
Bus Speed	1066 MHz	
Motherboard		
Board AA #	D79951	
Board FAB #	407	
* This report applies to the production FAB revision; Please consult your Intel Corporation representative to clarify the motherboard revision you intend to perform logo testing if not the same.		
System Memory		
Speed	Dual Channel, DDR2, 800	
Memory Type	DIMM	
Connector Type	DDR2, 240 Pin	
Power Management		
BIOS Default	S3	
Operating System Tested		
	Check Tested	Comments
Windows XP Pro	<input checked="" type="checkbox"/>	XP Pro with Service Pack 3 RC2
Windows XP Pro 64-bit	<input checked="" type="checkbox"/>	XP Pro with Service Pack 2
Windows Vista	<input checked="" type="checkbox"/>	Vista Ultimate with Service Pack 1
Windows Vista 64-bit	<input checked="" type="checkbox"/>	Vista Ultimate with Service Pack 1
Windows Vista Basic	<input type="checkbox"/>	Vista Basic with Service Pack 1

¹ These are the product names to enter in the "Submission ID of previously logo'd qualified PC system or server" field during your "System Using a Previously Logo'd Motherboard" submission to Microsoft.

Windows Vista Basic 64-bit	<input type="checkbox"/>	Vista Basic with Service Pack 1
----------------------------	--------------------------	---------------------------------

Onboard Integrated Devices and Driver for XP Pro 32 and 64bit

Technology	OS	Version
Chipset Update Utility Intel® Chipset Software Utility	Windows XP Pro	8.4.0.1016
	Windows XP Pro 64-bit	8.4.0.1016
Graphics Intel® Graphics Media Accelerator	Windows XP Pro	14.32.3.4906
	Windows XP Pro 64-bit	14.32.3.64.4906
Audio Realtek	Windows XP Pro	5574
	Windows XP Pro 64-bit	5574
LAN Intel® PRO Network Connections	Windows XP Pro	12.1
	Windows XP Pro 64-bit	12.1
HECI Intel® Management Engine Interface	Windows XP Pro	3.0.30.1086
	Windows XP Pro 64-bit	3.0.30.1086

Onboard Integrated Devices and Driver for Vista 32 and 64bit

Technology	OS	Version
Chipset Update Utility Intel® Chipset Software Utility	Windows Vista	8.4.0.1016
	Windows Vista 64-bit	8.4.0.1016
Graphics Intel® Graphics Media Accelerator	Windows Vista	15.7.3.1409
	Windows Vista 64-bit	15.7.3.64.1409
Audio Realtek	Windows Vista	5548
	Windows Vista 64-bit	5548
LAN Intel® PRO Network Connections	Windows Vista	12.1
	Windows Vista 64-bit	12.1
HECI Intel® Management Engine Interface	Windows Vista	3.0.30.1086
	Windows Vista 64-bit	3.0.30.1086

Driver Test Manager Used (DTM)

Microsoft website: <http://www.microsoft.com/whdc/DevTools/WDK/DTM.mspix>

Please check regularly for test kit updates from Microsoft. Please ensure latest filters updated prior to WHQL run.

Operating Systems	Notes	WHQL Testkit
Windows XP Pro Windows XP Pro 64-bit	DTM for Windows XP SP3	DTM for Windows XP SP3
Windows Vista Windows Vista 64-bit	DTM for Windows Vista SP1	DTM for Windows Vista SP1

Errata and Contingencies

Operating System	Failing Test	Expiry date	ID Number	Type	Error Description
Windows Vista Windows Vista 64-bit	PCI Hardware Compliance Test For Systems Running Windows Vista (PCIHCT)	3/5/2008	990	Erratum	The Bit 5 (Surprise Down Error Severity) in the Uncorrectable Error Severity register (offset Ch) in the Advanced Error Reporting Capability table must be read-only and always return 1 if the Bit 5 (Surprise Down Error Mask) in the Uncorrectable Error Mask Register in the Advanced Error Reporting Capability table is not implemented
Windows Vista Windows Vista 64-bit	PCI Hardware Compliance Test For Systems Running Windows Vista (PCIHCT)	12/31/2008	385	Erratum	The following PCI Compliance test failure is acceptable: Bit 15 (Bridge Configuration Retry Enable) in the Device Control register (offset 8h) in the PCI Express Capability table must be read-only and always return 0 as it is reserved for devices other than PCI Express to PCI/PCI-X Bridges. Assertion 13A41D3E-2576-41DC-A67C-525DA3637CEA This failure is acceptable because this is a PCIe 1.1 feature and the WLP requires compliance with only PCIe 1.0a.
Windows Vista Windows Vista 64-bit	PCI Hardware Compliance Test For Systems Running Windows Vista (PCIHCT)	12/31/2008	709	Erratum	Certain devices are known to have incompatibilities with the test causing a system hang during testing. This erratum is for those devices The PCIHCT tests Bit 15 of the Device Control Register in the PCI Express Capability table. The testing of Bit 15, specifically the writing of a 1 to the bit causes a function level reset making the device unavailable for a maximum of 100ms. All writes to the PCI config space of the device are ignored during the reset. All reads to the PCI config space of the device return all ones (1). This confuses the PCIHCT test and it incorrectly fails the assertions for Bits 4, 11, 12, 13 and 14. Assertion 73C19B53-84B8-4BA4-97EA-3FF6DEF57273: FAILED. Bit 4 (Enable Relaxed Ordering) in the Device Control register (offset 8h) in the PCI Express Capability table must be read-writable if it's implemented. Assertion C0846E74-2CFE-4CC2-A492-6AAF16F206CD: FAILED. Bit 11 (Enable No Snoop) in the Device Control register (offset 8h) in the PCI Express Capability table must be read-writable if it's implemented. Assertion A56B0EE0-0C2C-47A4-90C4-5F8EEB30D427: FAILED. Bit range 14:12 (Max_Read_Request_Size) in the Device Control register (offset 8h) in the PCI Express Capability table must be read-writable if implemented. Assertion 13A41D3E-2576-41DC-A67C-525DA3637CEA: FAILED. Bit 15 (Bridge Configuration Retry Enable) in the Device Control register (offset 8h) in the PCI Express Capability table must be read-only and always return 0 as it is reserved for devices other than PCI Express to PCI/PCI-X Bridges.
Windows Vista Windows Vista 64-bit Windows XP Pro	UAA Test - Vista (System)	7/31/2008	534	Erratum	UAA Test requires the Traffic Priority bit to be read/write - however there are two specs that apply, and they conflict. One says the bit must be read/write, the other says it must be read-only. Contact has been made with the author of both specs (Intel) but until this point is clarified we cannot fail submissions containing this test failure.

Test Notes

Operating System	Test	Description
XP and Vista	BIOS download	Internal: http://bios.intel.com/downloads/ External: http://www.intel.com/ click on Support and Download
XP and Vista	BIOS setup	Please make sure the BIOS setting are as below, otherwise use default settings. System Date and Time: Current date and time Peripheral Configuration: Enable all onboard component Drive Configuration: Set to IDE Chipset Configuration: Enable HPET ACPI Suspend State: Set to <S3 State> Boot Device Priority: set <Hard Disk Driver> to first
XP and Vista Filter update	DTM WHQL test	http://winqual.microsoft.com/member/SubmissionWizard/LegalExemptions/filterupdates.cab
Special H/W that use to PASS the test	None	None
Vista 64-Bit	LAN driver installation	LAN Driver has to be installed manually. Extract the LAN driver folder and updated the driver directly from device manager.