

Previously Logo'd Motherboard Program (PMLP)

Intel® Desktop Board

QS77GK-IS

PLMP Report

10/15/2012

Purpose:

This report describes the QS77GK-IS Previously Logo'd Motherboard Program testing run conducted by Intel Corporation.

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Contents

Introduction	4
Terms and Definitions.....	4
Desktop Board Configuration	5
Desktop Board QS77GK-IS Final Configuration Report: Completion of PLMP	5
Board Information.....	5
Product Code.....	5
Processor	5
Motherboard	5
System Memory.....	5
Power Management.....	5
Operating System Tested.....	5
Onboard Integrated Devices and Driver for Windows 7 32-bit and 64-bit.....	6
Windows Logo Kits Used (WLK)	6
Errata and Contingencies	6
Test Notes	7

Introduction

Terms and Definitions

Term	Definitions
WHQL	Windows* Hardware Qualification Lab
WLK	Windows Logo Kits
MLP	Motherboard Logo Program. For further information see: http://msdn.microsoft.com/zh-Tw/library/windows/hardware/gg463010
AP Machine	Audio Precision Machine
Winqual	Windows Qualification
MSFT Tested Product List	Tested Products List. You can view the Windows Marketplace for tested products list at: http://winqual.microsoft.com/HCL/ProductList.aspx?m=v&cid=105&g=s

Desktop Board Configuration

Desktop Board QS77GK-IS Final Configuration Report: Completion of PLMP

Data in this section reflects system configuration at time of PLMP submission.

Board Information

Product Code ¹	BIOS String/Model	Technologies NOT Logo'd (yet)
QS77GK-IS	GKPPT10H.86A.0020.2012.0919.2135	N/A - all technologies logo'd
Processor		
Speed	1.80GHz	
Family	Intel Core I3	
Bus Speed	100 MHz	
Motherboard		
Board AA #	G69901	
Board FAB #	200	
* This report applies to the production FAB revision; Please consult your Intel Corporation representative to clarify the motherboard revision you intend to perform logo testing if not the same.		
System Memory		
Speed	Dual Channel, DDR3, 1333MHz	
Memory Type	DIMM Nanya DDR3 1333/8GB	
Connector Type	DDR3, 240 Pin	
Power Management		
BIOS Default	S3	
Operating System Tested		
	Check Tested	Comments
Windows 7 & 64-bit	<input checked="" type="checkbox"/>	Windows 7 Ultimate Service Pack 1

¹ These are the product names to enter in the "Submission ID of previously logo'd qualified PC system or server" field during your "System Using a Previously Logo'd Motherboard" submission to Microsoft.

Onboard Integrated Devices and Driver for Windows 7& 64-bit

Technology	OS	Version	Package version
Chipset Update Utility Intel® Chipset Software Utility	Windows 7	V9.3.0.1021	V9.3.0.1021
	Windows 7 64-bit		
Graphics Intel® HD Graphics	Windows 7	V8.15.10.2795	V 15.26.14.2795
	Windows 7 64-bit		
Audio	NA	NA	NA
LAN Intel	Windows 7	V11.16.96.0	V17.2
	Windows 7 64-bit		
MEI Intel® Management Engine Interface	Windows 7	V8.1.0.1248	V8.1.0.1248
	Windows 7 64-bit		

Windows Hardware Certification Kit Used (WHCK)

Microsoft website: <http://www.microsoft.com/whdc/DevTools/WDK/DTM.msp>

Please check regularly for test kit updates from Microsoft. Please ensure latest filters updated prior to WHQL run.

Operating Systems	Notes	WHQL Testkit
Windows 7 & 64-bit	WLK1.6 for Windows 7 SP1	WLK1.6 for Windows 7 SP1

Errata and Contingencies

Operating System	Failing Test	Expiry Date	ID Number	Type	Error Description
Windows 7 Windows 7 64-bit	PCI Hardware Compliance Test For Systems	12/1/2015	401	Erratum	The following PCI Compliance test failure is acceptable: Bit 15 (Bridge Configuration Retry Enable) in the Device Control register (offset 8h) in the PCI Express Capability table must be read-only and always return 0 as it is reserved for devices other than PCI Express to PCI/PCI-X Bridges. Assertion 13A41D3E-2576-41DC-A67C-525DA3637CEA This failure is acceptable because this is a PCIe 1.1 feature and the WLP requires compliance with only PCIe 1.0a.
Windows 7 Windows 7 64-bit	PCI Hardware Compliance Test For Systems	12/1/2015	1241	Erratum	This happens because the PCI Compliance test assumes that if the Data Link Layer Link Active Reporting Capable bit in the Link Capabilities register for a given PCIe port is set then that indicates that the Data Link Layer Link Active bit will also be set. This is an incorrect assumption because the Data Link Layer Link Active bit can be reset when there is no device below the port. This assertion needs to be removed from the PCIHCT. The current architecture of the PCIHCT prevents it from knowing whether devices exist below a bridge/port.

					This is an acceptable failure: Assertion 0E36472A-0EDB-4685-A67D-7AF1FFBFA81B Data Link Layer Link Active Reporting Capable bit is set but Data Link Layer Link Active is not.
Windows 7 Windows 7 64-bit	UAA Test	12/1/2015	1300	Erratum	HD Audio pin configuration document calls out setting Port Connectivity to No Connection as the way to turn a pin off in a particular system. UAA Test incorrectly tests such pins. This is an acceptable failure.
Windows 7 Windows 7 64-bit	UAA Test	12/1/2015	1666	Erratum	graphics-hdmi-test-intel-two-of-three
Windows 7 Windows 7 64-bit	UAA Test	12/1/2015	513	Erratum	UAA Test requires the Traffic Priority bit to be read/write - however there are two specs that apply, and they conflict. One says the bit must be read/write, the other says it must be read-only. Contact has been made with the author of both specs (Intel) but until this point is clarified we cannot fail submissions containing this test failure.
Windows 7 Windows 7 64-bit	Signed Driver Check	12/1/2015	1248	Erratum	If a driver is found to be signed by Microsoft that is tested with the chklogo test, the message, ERROR: No Logo Level Attribute on file., is an invalid failure if followed by, This file has a 0 level Logo and followed by, Cannot match the device ID of the driver the device was test within an invidual assertion. Drivers that may have been logod but are not associated with a valid hardware ID are attributed incorrectly as unclassified drivers and incorrectly failed by this test

Test Notes

Operating System	Test	Description
Windows 7 and Vista	BIOS download	Internal: http://bios.intel.com/downloads/ External: http://www.intel.com/ click on Support and Download
Windows 7 and Vista	BIOS setup	Please make sure the BIOS setting are as below, otherwise use default settings. System Date and Time: Current date and time Peripheral Configuration: Enable all onboard component Drive Configuration: Set to AHCI Chipset Configuration: Enable HPET ACPI Suspend State: Set to <S3 State> Boot Device Priority: set <Hard Disk Driver> to first
Windows 7 and Vista filter update	WLK WHQL test	https://sysdev.microsoft.com/member/SubmissionWizard/LegalExemptions/updatefilters.cab
Special H/W that use to PASS the test	None	None