



Intel® Desktop Board D910GLDW Specification Update

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The Intel® Desktop Board D910GLDW may contain design defects or errors known as errata, which may cause the product to deviate from published specifications. Current characterized errata are documented in this Specification Update.

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The Intel® desktop board D910GLDW may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

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REVISION HISTORY

Date of Revision	Version	Description
November 2004	-001	This document is the first Specification Update for the Intel® Desktop Board D910GLDW.
January 2005	-002	Added Erratum 1.
February 2005	-003	Added Documentation Change 1.
March 2005	-004	Added Erratum 2.

PREFACE

This document is an update to the specifications contained in the *Intel® Desktop Board D910GLDW Technical Product Specification* (Order number C88966). It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain Specification Changes, Errata, Specification Clarifications, and Documentation Changes.

Refer to the *Intel® Pentium® 4 Processor Specification Update* (Order number 249199) for specification updates concerning the Intel Pentium 4 processor and that may apply to the desktop board D910GLDW. Unless otherwise noted in this document, it should be assumed that any processor errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® 915G and 915P Chipset: Specification Update* (Order Number 301468) for specification updates concerning the 82910GL MCH Controller and that may apply to the desktop board D910GLDW. Unless otherwise noted in this document, it should be assumed that any MCH errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® IO Controller Hub 6 (ICH6) Family Specification Update* (Order Number 301474) for specification updates concerning the 82801ER I/O Controller Hub and that may apply to the desktop board D910GLDW. Unless otherwise noted in this document, it should be assumed that any ICH 6 errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Nomenclature

Specification Changes are modifications to the current published specifications. These changes will be incorporated in the next release of the specifications.

Errata are design defects or errors. Characterized errata may cause the desktop board D910GLDW's behavior to deviate from published specifications. Hardware and software designed to be used with any given Altered Assembly (AA) and BIOS revision level must assume that all errata documented for that AA and BIOS revision level are present on all desktop boards.

Specification Clarifications describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.

Documentation Changes include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

Specification Update for the Intel® Desktop Board D910GLDW

GENERAL INFORMATION

Basic Desktop Board D910GLDW Identification Information

AA Revision	BIOS Revision	Notes
C82194-102	WB91X10J.86A.0388	1-5
C82194-103	WB91X10J.86A.0690	1-5
C82202-102	WB91X10J.86A.0388	1-5
C82202-103	WB91X10J.86A.0690	1-5
C82203-102	WB91X10J.86A.0388	1-5
C82203-103	WB91X10J.86A.0690	1-5

NOTES:

- The AA number is found on a small label on the component side of the board.
- The 82910GL Chipset kit used on this AA revision consists of two components as follows:

Device	Stepping	S-Spec Numbers
82910GL GMCH	B1	SL74W
82801FB ICH6	B2	SL7Y5

- Refer to the *Intel® Pentium® 4 Processor Specification Update* (Order Number 249199) for errata related to the Pentium 4 processor and that may apply to the desktop board D910GLDW.
- Refer to the *Intel® 915G and 915P Chipset: Specification Update* (Order Number 301468) for errata related to the 82910GL MCH that may apply to the desktop board D910GLDW.
- Refer to the *Intel® IO Controller Hub 6 (ICH6) Family Specification Update* (Order Number 301474) for errata related to the 82801FB I/O Controller Hub that may apply to the desktop board D910GLDW.

Summary Table of Changes

The following table indicates the Specification Changes, Errata, Specification Clarifications, or Documentation Changes that apply to the Intel® Desktop Board D910GLDW. Intel intends to fix some of the errata in a future revision of the desktop board, and to account for the other outstanding issues through documentation or specification changes as noted. This table uses the following notations:

CODES USED IN SUMMARY TABLE

Doc:	Document change or update that will be implemented.
Plan Fix:	This erratum may be fixed in a future revision of the desktop board, driver, or BIOS.
Fixed:	This erratum has been previously fixed.
No Fix:	There are no plans to fix this erratum.
Shaded:	This erratum is either new or modified from the previous version of the document.

NO.	PLANS	ERRATA
1	Plan Fix	The IEEE-1394a interface may lose link connection when transferring files from some IEEE-1394a DVD/CD-ROM devices
2	Plan Fix	Wake On LAN may not function after loss and restore of AC power
NO.	PLANS	DOCUMENTATION CHANGES
1	Doc	Incorrect Operating Voltage Listed for Supported Memory in Section 1.5, System Memory

ERRATA

1. *The IEEE-1394a Interface May Lose Link Connection When Transferring Files From Some IEEE-1394a DVD/CD-ROM Devices*

PROBLEM: PCI frequency noise between GND and the IEEE-1394a analog ground (AGND), in excess of 1V peak to peak, has been measured when transferring files from an IEEE-1394a DVD/CD-ROM device and an IDE Hard Disk Drive. The presence of this noise causes an IEEE-1394a link drop off with certain IEEE-1394a DVD/CD-ROM devices.

IMPLICATION: Users may experience the IEEE-1394a link drop when transferring files from some IEEE-1394a DVD/CD-ROM devices. This failure has been observed with a very limited number of IEEE-1394a DVD/CD-ROM devices. The failure is masked when the system is configured with a minimum of two PCI cards. This failure is considered a low risk.

WORKAROUND: None.

STATUS: This erratum may be fixed in a future hardware revision.

2. *Wake On LAN May Not Function After Loss and Restore of AC Power*

PROBLEM: If AC power is removed, Wake On LAN may not function until the LAN circuit is re-initialized. The sequence of events is as follows:

- AC power removed (AC power cord disconnected, for example)
- AC power restored. +5V Standby voltage is re-applied to the board
- Wake On LAN may not be functional
- Switch power on
- Switch power off to put system back into Standby mode (S5 state)
- Wake On LAN functionality restored

IMPLICATION: Wake on LAN will not function if AC power is interrupted.

WORKAROUND: In the BIOS Setup program, set After Power Failure to "Power On" in the Power Menu.

STATUS: This erratum may be fixed in a future BIOS revision.

DOCUMENTATION CHANGES

The Documentation Changes listed in this section apply to the *Intel® Desktop Board D910GLDW Technical Product Specification* (Order Number C88966). All Documentation Changes will be incorporated into a future version of that specification.

1. *Incorrect Operating Voltage Listed for Supported Memory in Section 1.5, System Memory*

Section 1.5, page 15, incorrectly lists the operating voltage for supported memory as 2.5 V. The correct operating voltage is 2.6 V. The first item in the bulleted list in Section 1.5 will be replaced with the following:

- 2.6 V (only) DDR SDRAM DIMMs with gold-plated contacts