Intel® SoC Watch User Guide for Windows* OS

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Version History

These are the main releases of Intel® SoC Watch:

Date	Revision	Description
June, 2019	2.11	Improves handling of unrecognized CPUs, reporting S-state when hibernation occurs, and other bug fixes.
September, 2019	2019.12	Added support for Intel platform code named Ice Lake. Modified hw-cpu-pstate reporting.
October, 2019	2019.13	Fixed issue in hw-cpu-pstate for Intel platform code named Ice Lake.
November, 2019	2020.1	Added support for Intel platform code named Comet Lake.
February, 2020	2020.2	Added collection of tool usage analytics. Added new features pch-slps0, pch-slps0-dbg. Improved error messages and help output. Enhanced driver security.

1 About Intel® SoC Watch

Intel® SoC Watch is a command line tool for monitoring and debugging system behaviors related to power consumption on Intel® architecture-based platforms. It reports active and low power states for the system/CPU/GPU/devices, processor frequencies and throttling reasons, wakeups, and other metrics that provide insight into the system's energy efficiency. The tool includes utility functions that include delaying the start of collection and launching an application prior to starting collection.

Data is collected from both hardware and OS sources. When using the default mode of collection, the tool collects data at normally occurring OS context-switch points so that the tool itself is not perturbing the system sleep states. Tool overhead when collecting during idle scenarios can be < 1%, however active workloads with a high-rate of context switching will increase the overhead. A minimum collection interval is used to control the rate of collection.

Intel SoC Watch writes a summary report file (.csv) at the end of collection on the system under analysis (target system), allowing immediate access to results. Additional result files can be specified including: an import file (.pwr) for Intel® VTune™ Profiler that can be used for visualization of correlated timelines for all the collected metrics with powerful zoom and filtering functions, and a time trace file (.csv) that can be viewed as a timelines in tools like Microsoft* Excel*.

Related Information

See the Intel® SoC Watch Release Notes for information on new features as well as known issues.

For online help, including information about importing results into Intel® VTune™ Profiler, see the Energy Analysis User Guide (https://software.intel.com/en-us/energy-analysis-user-guide).

2 Installation

See the *Intel® SoC Watch Release Notes* for supported platforms and installation instructions.

Getting Started with Intel® SoC Watch

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1. Launch a command prompt with administrative privileges. Intel SoC Watch requires administrative access to load a device driver.
2. Run the `socwatch` command with one or more required options to collect energy data.
3. Review the reports generated by Intel SoC Watch to analyze energy consumption.
4. [Optional] Import the results into Intel VTune Amplifier to review a visual representation of the data collected.

Listed below are a few basic commands that will have you looking at Intel SoC Watch reports quickly. Use the Options Quick Reference section to learn about all the commands, their options, and abbreviations.

To collect and report CPU C-state and P-state residency data and system sleep state residency for the platform, replace `<duration>` with the number of seconds you want to collect data and use the command below. Intel SoC Watch will write a summary report to the file `SOCWatchOutput.csv`.

```
socwatch -t <duration> -f cpu -f sstate
```

If you want to name the output files something other than the default `SOCWatchOutput`, replace `<string>` in the command below with the name you want. After collection, look for the `<string>.csv` file to find the summary report.

> **Tip**
> Include a folder name in the string to group the files under a folder. The folder will be created if it doesn't exist.

```
socwatch -t <duration> -f cpu -f sstate -o <string>
```

To collect a broad set of system metrics for a high level look at platform power behavior, replace `<duration>` with the number of seconds you want to collect data in the following command. Intel SoC Watch will write a summary report to `SOCWatchOutput.csv`.

```
socwatch -t <duration> -f sys
```

To get detailed reports available for any metric being collected (e.g., idle and wakeup analysis and timelines), include the `--max-detail` (`-m`) option during collection and specify additional report types using the `--result` option. In the example below, all metrics in group name `sys` are collected for 60 seconds, including over time data and any detailed data for metrics that offer it. A trace file is generated (`-r int`) in addition to the summary report. All the results are in files named `run1` under the folder `sysDetail`.

```
socwatch -t 60 -f sys -m -r int -o sysDetail/run1
```

Note that trace (timeline) reports for long collections can take many minutes to generate. In this case, you may wish to generate only summary reports at the end of collection and later request the trace file be generated as shown below using the `--input` (`-i`) option. In this example, the sys group of metrics is collected for 60 minutes with maximum detail, but only summary report is generated initially. The second command causes the results to be re-processed and timeline reports generated. All of the new reports have the same base name and location unless a `-o` is included to give the re-processed results a new name.

Tip
Use the `-o` option in conjunction with `-i` to change the base name.

```
socwatch -t 3600 -f sys -m -o myResult
socwatch -i myResult -r int
```

To generate an export file containing all supported metrics that can be opened for viewing in Intel VTune Amplifier, include the `-r vtune` option alongside any other collection parameters. The `--max-detail` option is required to generate a complete set of trace data for all metrics. The following command will collect platform data for 30 seconds and produce a file called `run1.pwr` that can be imported to Intel VTune Amplifier.

```
socwatch -t 30 -f sys -r vtune -m -o run1
```

Entering Connected Standby (Modern Standby)

If running on a system that supports Connected Standby and has WDTF installed, use the `--auto-connected-standby` (`-z`) option to automatically put the system in Connected Standby and then exit standby when the collection time completes. You can manually exit standby early and collection will stop at that time and generate the reports. You can also enter Connected Standby manually after starting a collection.

NOTE
In order to use the `--auto-connected-standby` (`-z`) option, the Windows* OS Driver Test Framework (WDTF) must be installed. See the *Installation Notes* section in the Release Notes for instructions.

Your platform must be enabled for *S0 Low Power Idle* state for this option to successfully put the system into Modern Standby after the WDTF is installed. The following command should list a sleep state such as *Standby (S0 Low Power Idle) Network Connected*, as available on this system if Modern Standby is possible: `powercfg /a`

Example: The following command collects CPU C-state residency for 60 seconds after putting the system into Connected Standby. It will delay collection and the transition to standby by the `--startdelay` (`-s`) time of 20 seconds. (There is no option to delay the start of collection after entering standby.)

```
socwatch -s 20 -t 60 -f cpu-cstate -z
```

4 Options Quick Reference

Invoke Intel SoC Watch with administrator privilege, using the following syntax:

```
socwatch <general options><post-processing options><collection options>
```

- Order of options does not matter unless specifically noted.
- Help is displayed if no option is specified.
- All features are not available on all systems, so the help text is dynamic, meaning it displays only the collection options that are supported by the system on which it is run. The metrics available differ because of changes in the system's hardware architecture support. This User's Guide contains a list of all metrics across all systems.
- You can specify feature names that are not available or not enabled on a particular system. When the tool starts, it will display console messages regarding features that cannot be collected, but collection will proceed if at least one feature is valid on that system.

Intel SoC Watch terminates data collection for one of three reasons (whichever occurs first):

1. the `--time` option was specified and the timer elapsed,
2. the `--program` option was used and the specified program exited,
3. a Ctrl-C interrupt was entered in the command window.

The location and name of the results files is displayed at the end of a collection. The summary report will be there with that name and a `.csv` extension. Raw data files and additional files based on post-processing options specified on the command line are located there as well, all with the same base name (default name is `SoCWatchOutput`).

NOTE
Result files are replaced if the same name is used for multiple collections.

General Options

The following options display information about the tool or system on which it is run.

Abbreviation	Option Name	Description
	`--enable-alts`	Enable alternative key for terminating collection [ALT-S].
	`--export-help`	Write help output to JSON formatted file.
`-h`	`--help`	Display tool usage information and exit. The help shown is specific to the system on which it is run. Only metrics supported by the system architecture will be listed.
`-l`	`--log <filename>`	Redirect all console output, including errors, to specified file.
	`--print-fms`	Display CPU ID as Family.Model.Stepping and exit.
	`--skip-usage-collection`	Do not ask or collect tool usage analytics (ignore prior consent).
	`--update-usage-consent [yes|no]`	Set or change your consent to collection of tool usage analytics.

Abbreviation	Option Name	Description
-v	--version	Display tool version information and exit.

Post-processing Options

The following options affect how results are reported and where they are stored.

Abbreviation	Option Name	Description
-i	--input *<filename>*	Specify the path and base filename (without extension) of an existing collection to generate additional reports. Use with the -r option to specify which types of reports.
-o	--output *<filename>*	Specify the base name for the output files from this collection. If this option is not specified, the files are written to the current working directory with base name SoCWatchOutput. Specifying console as the filename will cause the summary results to also write to stdout. If a name already exists, the previous results will be replaced.
-r	--result *<result_type>*	Specify the type of result to generate. This option can be repeated to get multiple types of reports. Following are the result types that can be specified: • sum Write summary reports to .csv file. [default] • int Write over-time data to _trace.csv file. • vtune Generate .pwr file for import to Intel VTune Amplifier. • auto Write summary results as a single line to file Automation_Summary.csv in current directory. Appends results, does not overwrite. If column headers for the new result changed, new headers will be inserted. Use to generate sets of data in a single file for comparison.

Collection Options

These options affect what is collected and how it is collected.

Abbreviation	Option Name	Description
-f	--feature *<name>*	Specify which metric to collect, choose from the group names or individual names listed in the tables below. This option can be repeated to collect multiple metrics in a single run. Most features can be collected simultaneously, exceptions noted in the table of feature names.
-m	--max-detail	Collect all data available for each feature specified. This will cause snapshot metrics to be sampled. Use of this option can increase tool overhead, so best used only

Abbreviation	Option Name	Description
		when timeline of the data is needed or when collecting across system entry to hibernation. Without this option, the tool collects data at the minimum required by the data source for best accuracy. Data may be traced, sampled, or snapshot. • Traced data is obtained at state transition points resulting in accurate summary and timeline results. • Sampled data is read at OS context switch points (or at timed intervals if polling option is used). This is less accurate as changes that take place between samples will not be measured. Metrics that come from hardware status/ state data must be sampled. • Snapshot data can be read at the beginning and end of the collection and the difference gives an accurate result with lowest overhead, but no timeline. Only metrics that come from hardware accumulators can be snapshot. The algorithm used to determine the collection method for each data type is as follows: If `-m` is specified: if the data can be traced, trace it; else sample it. If `-m` is not specified: if the data can be snapshot, snapshot it; else if the data can be traced, trace it; else sample it.
`-n`	`--interval <milliseconds>`	Specify the time in milliseconds that should pass before reading next hardware data sample (default 100 ms). For default collection mode, this is the minimum time between sampling at context switch points. When `--polling` option is used, this is actual time between samples. The minimum polling interval is 1ms. However, using low polling intervals will result in higher overhead and may fail to measure some metrics (e.g. bandwidths) with intervals shorter than the default.
	`--polling`	Make data collection occur at regular intervals rather than at context switch points. Use the `--interval` option to set the interval period (default: 100ms). Use of this option significantly increases perturbation of sleep states because it

Abbreviation	Option Name	Description
		employs a timer which will interrupt sleep states, increase wakeup counts, and change timer resolution.
-p	--program *<application> <parameters>*	Specify the name of an executable to be started automatically prior to collection. The name can be followed by zero or more arguments that will be passed to the program. **NOTE** This option must occur at the end of the command line, everything following the executable name will be given to it as arguments. **NOTE** The executable can only be launched as a desktop application (not as Windows Modern UI application).
	--program-delay *<seconds>*	Specify number of seconds to wait before starting the program specified by -p. Has no effect if -p not used.
-s	--startdelay *<seconds>*	Specify number of seconds to wait before starting collection of data. If used with -p and --program-delay, this delay is applied after the program starts. If used with --auto-connected-standby, this delays entry to standby.
-t	--time *<seconds>*	Specify collection duration in seconds. Collection will stop when this time has elapsed unless Ctrl-C is entered or an executable specified with --program option exits prior to the specified duration.
-z	--auto-connected-standby	Automatically enter Connected Standby for the duration of the collection. Will automatically exit Connected Standby when the -t specified time expires. If system is woken from Connected Standby prior to the end of the duration, the collection will stop as well. If --start-delay is specified, it occurs prior to entering Connect Standby. **NOTE** Requires Windows* Driver Test Framework, WDTF to be installed on the system under test.

Feature Names (Individual)

The available feature names for the `--feature` option and their collection methods are listed below. You can specify multiple feature names individually or using group names described in the Feature Group Names section.

Note that every feature listed is not available on every platform supported by Intel SoC Watch. The `--help` option is dynamic, only showing features available for the platform on which it is run. Use it to determine which features are supported. You can specify unsupported features on the command line and the tool will simply display a message for those that cannot be collected, but continue with collection if there is at least one that is supported.

Collection methods are indicative of a metric's level of accuracy and overhead. Traced collection provides high accuracy along with precise transition points between states. Sampled collection is least accurate since transitions can occur which are never noted. Sampled data needs to be read at intervals throughout the collection period which increases tool overhead. Increasing the sampling rate (reading at closer intervals) will improve accuracy but increase overhead. Snapshot collection means the data comes from an accumulator so it can be collected only at the start and end of the collection period and give perfect accuracy. This gives accuracy and the lowest overhead. If the `--max-detail` (`-m`) option is given, the Snapshot metrics will instead be read at the same intervals as the Sampled metrics throughout the collection, so that you can generate a trace file to see how it changed overtime.

Name	Collection Methods	Description
`acpi-dstate`	Trace	Device D-state residencies, from OS event trace. If a device has multiple components, the component F-state residency report is included. The list of devices included in the report are filtered to only include selected ACPI devices. To get the complete device list, disable the filter using option `--option no-device-filter`.
`acpi-sstate`	Trace	ACPI Sx state residency (S1-S4), from OS event trace.
`all-approx-bw`	Sampled	Estimated memory bandwidth for multiple SoC agents (concurrently), from hardware signal accumulators. Precise bandwidth can be collected for one agent at a time using other bandwidth features. Only one bandwidth or DRAM self-refresh metric can be specified at a time. Hardware counter overflow can occur if -m is not used for signal-based metrics. SoC transactions are counted and multiplied by 64 to form the estimates, which will be equal to or higher than the actual bandwidths. The estimate is high if partial or 32-byte transfers actually occurred. On 22nm Intel Atom® Processor-based SoC for Tablets and 2-in-1s (formerly code named Bay Trail), this feature provides the following estimated bandwidths: • Module 0 and Module 1: the bandwidth from CPU Module 0 and Module 1 to the DDR • GFX: the bandwidth from the graphics component to the DDR

Name	Collection Methods	Description
		• Display: the bandwidth from the display controller to the DDR • ISP: the bandwidth from the camera image processor to the DDR • VED: the bandwidth from the video encode and decode components to the DDR • IO: the bandwidth between the north and south clusters On systems code named Cherry Trail, and Intel Atom Processor Z35XX (formerly code named Moorefield), this feature provides the following estimated bandwidths: • Module 0 and Module 1: the bandwidth from CPU Module 0 and Module 1 to the DDR • GFX: the bandwidth from the graphics component to the DDR • Display: the bandwidth from the display controller to the DDR • ISP: the bandwidth from the camera image processor to the DDR • IO: the bandwidth between the north and south clusters
`core-temp`	Sampled	IA core temperature statistics, from hardware status data.
`cpu-ddr-mod0-bw` `cpu-ddr-mod1-bw`	Sampled	Precise CPU to DDR module 0 or 1 bandwidth, from hardware signal accumulators. Only one bandwidth or DRAM self-refresh metric can be specified at a time. Hardware counter overflow can occur if -m is not used for signal-based metrics. Supported on Intel Atom Processor-based SoCs for systems code named Cherry Trail and Broxton-M.
`cpu-gpu-concurrency`	Snapshot	Concurrent active time of CPU and GPU, from hardware accumulators.
`ddr-bw`	Sampled	Total DDR memory bandwidth, from hardware accumulators or from signal accumulators. On platforms where signals are used, only one bandwidth or DRAM self-refresh metric can be specified at a time. The hardware accumulator data is always collected over time due to frequent overflow, so snapshot is not available. Hardware counter overflow can occur if -m is not used for signal-based metrics.
`disp-ddr-bw`	Sampled	Precise display controller to DDR memory bandwidth, from hardware signal accumulators. Only one bandwidth or DRAM self-refresh metric can be specified at a time. Hardware counter overflow can occur if `-m` is not used for signal-based metrics.

Name	Collection Methods	Description
		Supported on Intel Atom Processor-based SoCs for systems code named Cherry Trail and Broxton-M.
dram-pwr	Sampled	Total DRAM power consumption from hardware accumulators. This data can be collected on Intel Atom Processor-based SoCs for systems code named Apollo Lake and Denverton, and on Intel systems code named Skylake-Xeon.
dram-srr	Sampled	DRAM residency in self-refresh mode, from hardware signals. Only one bandwidth or DRAM self-refresh metric can be specified at a time. Hardware counter overflow can occur if -m is not used for signal-based metrics. Do not specify a polling interval greater than 14 seconds when measuring DRAM self-refresh. Supported on Intel Atom Processor-based SoCs for systems code named Cherry Trail and Broxton-M.
gfx-ddr-bw	Sampled	Graphics component to DDR bandwidth, from hardware signal accumulators. Only one bandwidth or DRAM self-refresh metric can be specified at a time. **NOTE** Some bandwidth measurements include ReadPartial or WritePartial traffic. The payload (how many bytes were transferred) of a partial transaction can range from 1 to 64 bytes. Therefore, the exact bandwidth cannot be accurately measured. In these cases, the Intel SoC Watch bandwidth results will provide a Total Bandwidth Range. The Total Bandwidth Range describes the minimum and maximum bandwidth that was measured. The actual bandwidth falls within the Total Bandwidth Range. Hardware counter overflow can occur if -m is not used for signal-based metrics. Supported on Intel Atom Processor-based SoCs for systems code named Cherry Trail and Broxton-M.
igfx-throt-rsn	Sampled	Reasons for throttling the integrated GPU frequency, from hardware status data.
hw-cpu-cstate	Snapshot	CPU C-state (sleep) residencies, from hardware accumulators.
hw-cpu-hwp	Sampled	HWP capabilities, from hardware status data.
hw-cpu-pstate	Sampled	CPU P-state operating frequency residencies, from hardware status data.
hw-igfx-cstate	Snapshot	Integrated graphics processor C-state residency (RC6), from hardware accumulators. Always sampled due to short overflow time period.

Name	Collection Methods	Description
hw-igfx-pstate	Sampled	Integrated graphics processor P-state operating frequency residencies, from hardware status data.
ia-throt-rsn	Sampled	Reasons for throttling the CPU frequency, from hardware status data.
io-bw	Sampled	IO bandwidth between the North Cluster (NC) and South Cluster (SC), from hardware signal accumulators. Only one bandwidth or DRAM self-refresh metric can be specified at a time. Hardware counter overflow can occur if -m is not used for signal-based metrics. Supported on Intel Atom Processor-based SoCs for systems code named Cherry Trail and Broxton-M.
isp-ddr-bw	Sampled	ISP (camera image processor) to DDR bandwidth, from hardware signal accumulators. Only one bandwidth or DRAM self-refresh metric can be specified at a time. Hardware counter overflow can occur if -m is not used for signal-based metrics. Supported on Intel Atom Processor-based SoCs for systems code named Cherry Trail and Broxton-M.
os-cpu-cstate	Trace	CPU ACPI C-state (sleep) residencies, core concurrency, and analysis of wakeups, from OS event trace. Use `-m` to get additional analysis reports written to file _WakeupAnalysis.csv.
os-cpu-pstate	Trace	CPU P-state (frequency) residencies, from OS event trace.
os-gfx-cstate	Trace	Graphics processor C-state residencies for integrated or discrete graphics, from OS event trace.
pch-slps0	Snapshot	PCH SLP_S0 residency, from hardware accumulator.
pch-slps0-dbg	Sampled	Blocking reasons for SLP_S0, from hardware status data.
pkg-pwr	Snapshot	Calculate the entire SoC/Package power consumption, from hardware accumulator.
ring-throt-rsn	Sampled	Reasons for throttling the ring clock frequency, from hardware status data.
sc-dstate	Sampled	South Complex (SC) component D0ix approximated state residencies. This data can only be collected on Intel Atom Processor-based SoCs for systems code named Cherry Trail .

Name	Collection Methods	Description
soc-temp	Sampled	SoC temperature data, from hardware status data. This data can only be collected on Intel Atom® Processor-based SoCs for systems code named Cherry Trail.
timer-resolution	Trace	Timer resolution change events from OS trace events.

Feature Group Names

The following features are groupings of the previously described features. These group names can be used to simplify command lines to collect multiple features concurrently. For example, `-f cpu` can replace the `-f cpu-cstate -f cpu-pstate` in a command line.

If a group includes a feature that is not enabled on the target platform, that feature will be ignored and collection continue, as long as there is one feature that can be collected.

All features are not supported on all platforms, a group will only include the supported features. Use the `--help` option on the target platform to see the list of group names and specific features included each group.

Name	Description
cpu	cpu-hw + cpu-os
cpu-hw	Most CPU metrics obtained from hardware data sources.
cpu-os	All CPU metrics obtained from OS event traces.
device	Device state residency metrics.
gfx	All graphics metrics from hardware and OS. gfx-hw + gfx-os
gfx-hw	Most GPU metrics obtained from hardware data sources.
gfx-os	All GPU metrics obtained from OS event traces.
hw-gfx-cstate	hw-igfx-cstate
hw-gfx-pstate	hw-igfx-pstate
power	Power/energy metrics.
sstate	System Sx state metrics.
sys	Broad spectrum of metrics commonly used to get general information about platform power behavior.
temp	Temperature metrics.
throt	Frequency throttling reason metrics.

Viewing Intel SoC Watch Results with Intel® VTune™ Profiler

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You can analyze Intel SoC Watch data graphically using the Intel® VTune™ Profiler GUI. Intel® VTune™ Profiler provides a dynamic timeline view for interacting with Intel SoC Watch data and provides powerful filtering of data for in-depth analysis of a platform's power management behavior.

For detailed instructions, refer to the Analyze Energy Usage section of the Intel® VTune™ Profiler Help.