



Intel® System Debugger 2019

Release Notes for Windows host*

11 February 2019

Contents

1	Introduction	3
2	Supported Operating Systems	4
3	Supported Platforms	5
4	New in This Release – 2019 Update 2	8
5	Known Issues	9
6	Change History	11
7	Legal Information	12

1 Introduction

This document covers release specific information of all components of Intel® System Debugger 2019 for Windows* host which includes following tools

- Intel® System Debugger - System Debug
- Intel® System Debugger - System Trace
- Intel® Debug Extensions for WinDbg* (for Windows* targets)

2 Supported Operating Systems

Intel® System Debugger 2019 for Windows* host supports the following operating systems:

2.1 Host Operating Systems

- Microsoft Windows* 10

2.2 Target Operating Systems

2.2.1 Linux* Target

- Wind River* Linux* 9
- Yocto project* 2.4
- Ubuntu* 16.04
- Wind River* VxWorks* 7

2.2.2 Windows* Target

- Microsoft Windows* 10

3 Supported Platforms

Each Intel® System Debugger tool has its own supported platforms. Furthermore, the tools can provide several probe options for a connection that are

- Intel® In-Target Probe (Intel® ITP) XDP3
- Intel® Silicon View Technology (Intel® SVT) Closed Chassis Adapter (CCA)
- Intel® Direct Connect Interface (Intel® DCI) USB Debug Class (DbC) cable

The table below lists the platforms and probes supported by each tool of Intel® System Debugger 2019 (Update 2) for Windows* host.

	System Debug			System Trace		
	XDP3	CCA	DbC	XDP3	CCA	DbC
Intel Atom® Processor Z35xx (Anniedale/Moorefield)	✓					
Intel Atom® Processor C2xxx (Avoton)	✓					
5th Gen Intel® Core™ Processor (Broadwell)	✓					
Intel® Xeon® Processor D-15xx (Grangeville)	✓					
Intel Atom® Processors N4200, N3350, x7-E3950, x5-39xx (Apollo Lake)			✓			✓
Intel Atom® Processor N3xxx, J3xxx (Braswell)	✓					
Intel Atom® Processor x5-Z8xxx, x7-Z8700 (Cherry Trail)	✓					
8th Gen Intel® Core™ Processors (Coffee Lake-S) / Intel® H370 Chipset, Intel® H310 Chipset, Intel® B360 Chipset for Consumer (Cannon Lake PCH)	✓	✓	✓		✓	✓
8th Gen Intel® Core™ Processor i7-8565U, i5-8265U, i3-8145U (Whiskey Lake-U)	✓	✓	✓		✓	✓

	System Debug			System Trace		
	XDP3	CCA	DbC	XDP3	CCA	DbC
Intel Atom® Processor C3xxx (Denverton)			✓			✓
Intel® Pentium® Silver Processor N5XXX, J5XXX or Celeron® Processor N4XXX, J4XXX (Gemini Lake)			✓			✓
4th Gen Intel® Core™ Processor (Haswell)	✓					
4th Gen Intel® Core™ U-Processor (Haswell ULT)	✓					
8th Gen Intel® Core™ Processor (Coffee Lake-S) / Intel® Z370 Series Chipset (Kaby Lake PCH-H)	✓	✓	✓		✓	✓
7th Gen Intel® Core™ Processor (Kaby Lake) / Intel® 200 Series Chipset (Kaby Lake PCH-H)	✓	✓	✓		✓	✓
Intel® Xeon® Scalable Processor (Skylake-SP) / Intel® C620 Series Chipset (Lewisburg)	✓	✓	✓		✓	✓
6th Gen Intel® Core™ Processor (Skylake) / Intel® 100 Series Chipset (SunrisePoint PCH-H)	✓	✓			✓	
7th Gen Intel® Core™ Processor (Kaby Lake) / Intel® 100 Series Chipset (SunrisePoint PCH-H)	✓	✓	✓		✓	✓
8th Gen Intel® Core™ Processor (Amber Lake-Y, 2+2, 5W) for Consumer: i7-8500Y, i5-8200Y, m3-8100Y	✓	✓			✓	
8th Gen Intel® Core™ Processor (Kaby Lake R) / 6th Gen Intel® Core™ Platform I/O (SunrisePoint PCH-LP)	✓	✓			✓	
6th Gen Intel® Core™ Processor (Skylake) / 6th Gen Intel® Core™ Platform I/O (SunrisePoint PCH-LP)	✓	✓			✓	
Intel Atom® Processor E6xx (Tunnel Creek)	✓					

	System Debug			System Trace		
	XDP3	CCA	DbC	XDP3	CCA	DbC
Intel Atom® Processor Z36xx, Z37xx - 2 cores (Baytrail / MinnowBoard MAX)	✓					
Intel Atom® Processor E3805, E382x, Z3680 - 2 cores (Valleyview)	✓					
Intel Atom® Processor E384x, Z37xx - 4 cores (Valleyview)	✓					

4 New in This Release – 2019 Update 2

- Intel® System Debugger 2019 Update 2 includes functional and security updates. Users should update to the latest version.
- Python* version distributed by Python.org is replaced with Intel® Distribution for Python* based on Python* version 2.7.15. Space requirement for Intel® Distribution for Python* is approximately 570 MB

4.1 Intel® System Debugger – System Debug

- Migration to new Eclipse (simrel2018-12) and Java11
- Simics demo target is removed

5 Known Issues

- **Connection status indicated by the tool might be incorrect when Intel® DAL is used as an IPC provider**
 - **Issue:** Target connection assistant relies on Intel® DAL to verify the connection status. However, Intel® DAL implementation does not provide all functionality to verify whether a target is connected thus it is possible that the target connection assistant might indicate the connection incorrectly as connected.
 - **Workaround:** In case Intel® DAL is used as an IPC configuration, a manual validation by the user is required.
- **Platform security policy may inhibit debugger operation**
 - **Issue:** In some platforms, the security policy may disable JTAG access to the CPU. This is intended to prevent reverse-engineering. In this case the Intel® System Debugger will successfully connect to the target, however it will not be able to discover any CPUs on the JTAG bus and will warn the user that no CPUs are available.
 - **Workaround:** To resolve this issue please ensure that that platform firmware has enabled access to the CPUs via JTAG. This is typically done by flashing a special “debug” firmware into the target. Also note that in some cases CPU or CPU module may have physically disabled JTAG access, especially in production or near-production versions. In this case please work with the platform business unit to obtain a JTAG-enabled hardware.
- **Usage of Probes**
 - **Issue:** Intel® SVT CCA and Intel® ITP XDP3 probes are supporting hot plug/unplug from a target, whereas for Intel® DCI DbC the connection is bidirectional. In case of losing connection with a probe, the debugger will post a Power Loss event.
 - **Workaround:** If the target was running, probe disconnect would have no effect on the target and for these cases please reconnect the probe to continue debugging. In case target was halted, debugger will lose debug context, which leads to a crashing target.
 - **Issue:** Any accidental probe removals during halt would crash the target
 - **Workaround:** Please reboot target and restart the debugging session.
- **Intel® System Debugger help content cannot be viewed with Microsoft® Edge® browser**
 - **Issue:** Users might face difficulties with viewing Intel® System Debugger documentation in the Microsoft® Edge® browser
 - **Workaround:** Switch to the Internet Explorer® or Google® Chrome® browser.

5.1 Intel® System Debugger – System Debug

- **Files in Intel® System Debugger remains after uninstalling Intel® System Studio**
 - **Issue:** After uninstalling Intel® System Studio, files related to Intel® System Debugger may not be deleted.
 - **Workaround:** Files needs to be removed manually.

5.2 Intel® System Debugger – System Trace

- **Various error messages referring to "Interview_Decoder" or "visa64.dll" (e.g. Failed to load library <path_to_isd>\system_trace\bin\Interview_Decoder.dll)**
 - **Issue:** If the host has IVI tools (http://www.ivifoundation.org/shared_components/) installed, then Intel® System Studio erroneously loads visa64.dll from C:\Windows\System32.
 - **Workaround:** There are two possible workarounds:
 - Temporarily remove C:\Windows\System32\visa64.dll or uninstall the IVI tools
 - Follow <https://docs.microsoft.com/en-us/windows/desktop/dlls/dynamic-link-library-search-order> but be aware of the potential security implications

5.3 Intel® Debug Extensions for WinDbg*

- **Compatibility issues of Intel® Debug Extensions for WinDbg* for Windows* 10 SDK**
 - **Issue:** Windbg* over Intel® DCI plugin is currently not compatible for Windows* 10 SDK starting from version 1809 (10.0.17763).
 - **Workaround:** Install Windows* 10 SDK 1803 (10.0.17134) or older versions.

6 Change History

6.1 2019 Update 1

- 8th Gen Intel® Core™ Processor (Amber Lake-Y, 2+2, 5W) for Consumer: i7-8500Y, i5-8200Y, m3-8100Y support is added
- Target connection editor page is improved, and the size and appearance of the connection dialog wizard are optimized
- OpenIPC version is updated as 1.1839.3251.100
- Intel® DFX Abstraction Layer (Intel® DAL) version is updated as 1.1839.428.110

6.1.1 Intel® System Debugger – System Debug

- Implementation of a save button in PCI Tool dialog window

6.2 2019 Initial Release

- Support 8th Gen Intel® Core™ Processor i7-8565U, i5-8265U, i3-8145U (Whiskey Lake-U)

6.2.1 Intel® System Debugger – System Trace

- TDE runtime error is fixed
- Issues regarding starting traces for several platforms are fixed
- Error seen during capturing traces in Trace Hub Memory is fixed

7 Legal Information

No license (express or implied, by estoppel or otherwise) to any intellectual property rights is granted by this document.

Intel disclaims all express and implied warranties, including without limitation, the implied warranties of merchantability, fitness for a particular purpose, and non-infringement, as well as any warranty arising from course of performance, course of dealing, or usage in trade.

This document contains information on products, services and/or processes in development. All information provided here is subject to change without notice. Contact your Intel representative to obtain the latest forecast, schedule, specifications and roadmaps.

The products and services described may contain defects or errors known as errata which may cause deviations from published specifications. Current characterized errata are available on request.

Intel technologies' features and benefits depend on system configuration and may require enabled hardware, software or service activation. Learn more at Intel.com, or from the OEM or retailer.

Copies of documents which have an order number and are referenced in this document may be obtained by calling 1-800-548-4725 or by visiting <http://www.intel.com/design/literature.htm>.

Intel, the Intel logo, Intel Atom, Pentium, Intel Core, Celeron and Xeon are trademarks of Intel Corporation in the U.S. and/or other countries.

Optimization Notice: Intel's compilers may or may not optimize to the same degree for non-Intel microprocessors for optimizations that are not unique to Intel microprocessors. These optimizations include SSE2, SSE3, and SSSE3 instruction sets and other optimizations. Intel does not guarantee the availability, functionality, or effectiveness of any optimization on microprocessors not manufactured by Intel. Microprocessor-dependent optimizations in this product are intended for use with Intel microprocessors. Certain optimizations not specific to Intel microarchitecture are reserved for Intel microprocessors. Please refer to the applicable product User and Reference Guides for more information regarding the specific instruction sets covered by this notice.

Notice Revision #20110804

*Other names and brands may be claimed as the property of others

© Intel Corporation.