



March 2017 Newsletter

Intel® PCC Members Only Meeting & IXPUG Annual Spring Conference

Join us for the Intel® PCC members only meeting on April 10, 2017 from 1:00PM-5:00PM (GMT) at University of Cambridge in Cambridge, England. This event is by direct invitation only. If you have not received an email please send a message to IPCC.Program.Office@intel.com.

This will be followed by the [IXPUG Annual Spring Conference](#) on April 11-14, 2017, also in Cambridge. The conference is open to the public and requires [registration](#).

Speaking Opportunities

Share the results of your work, and the lessons you've learned, at the following events. Be sure to register and don't forget there are deadlines for abstract submissions.

Events and Details	Event Date	Deadlines	Speaking Opportunities
IXPUG ISC17 Workshop	Jun 22, 2017	Mar 31, 2017	Paper Submission
ACM Gordon Bell Prize	Nov 12-17, 2017	Apr 15, 2017	Nomination Submission Form
IXPUG Working Groups	Monthly	Monthly	Share your real-code experience and/or questions during the meeting or discussion forum.
IXPUG Discussion Forum	Anytime	Anytime	

Hardware Intel® Xeon Phi™ Processor Access

We encourage you to optimize your application for multi-node by testing on the following clusters:

Texas Advanced Computing Center (TACC) Stampede Cluster:

- Click [HERE](#) and create a new account (**do not click on PI-eligible**) and follow the email instructions.
- Register account by emailing ipcc.program.office@intel.com with username.

Intel® Application Development Cluster (Endeavor):

- Request account by completing the form located [HERE](#).

Discount on Intel® Xeon Phi Processors Platforms: Order today and use code "EDUCATION" in the order form for a 10% discount. The systems are fully configured platforms and include CentOS, a 1 year license for Intel® Parallel Studio XE 2017 edition, free parallel programming training, and support from Colfax!

Training Opportunities

Join in the training opportunities below to continue your growth and connect with other HPC developers.

Date	Location	Event
Mar 7, 2017	Virtual	MC2 Series: Learn How They Did It
Mar 8-11, 2017	Seattle, WA	SIGCSE Technical Symposium
Mar 11, 2018	Seoul, South Korea	Intel® HPC Cluster Workshop
Mar 13, 2017	Virtual	HOW Series: Deep Dive
Mar 10, 2017	Seoul, South Korea	Intel HPC Cluster Workshop
Apr 5, 2017	Virtual	Fast Insights to Optimized Vectorization and Memory
Apr 12, 2017	Virtual	Navigate Machine Learning & Deep Learning
Apr 19, 2017	Virtual	Get Onboard with the Intel® Parallel Studio XE 2018 Beta
Apr 24, 2017	Seoul, South Korea	KNL System and Parallel Studio Workshop
Apr 26, 2017	Virtual	Deep Learning At Your Finger Tips
Apr 26, 2017	Seoul, South Korea	KNL System and Parallel Studio Workshop

*NEW: Case Studies

University of Tennessee: [VisIt](#) is an interactive parallel visualization and graphical analysis tool for viewing scientific data. They discuss their latest efforts with respect to the scaling properties on the Intel® Xeon Phi™ processor family.

University of Tsukuba: [Lattice QCD](#) is a non-perturbative approach to solving the quantum chromodynamics theory of quarks and gluons. Using a reverse off-loading technique to reduce overhead, the center achieved a performance of ~200 GFlops sustained on large lattice size.

More News...

Learn more about recent work in the HPC community in the following articles:

- [SDSC Achieves Record Performance in Seismic Simulations With Intel](#)
- Partnership for Advanced Computing in Europe: [Best Practice Guide – Knights Landing](#)
- [Fast Deep Learning at Your Fingertips](#)
- [Recipe: Building and Running MILC on Intel® Xeon® Processors and Intel® Xeon Phi™ Processors](#)
- [Implementation of Classic Gram-Schmidt in a Reservoir Simulator](#)