

INTEL® PARALLEL STUDIO XE 2018

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1 Introduction

Intel® Parallel Studio XE has three editions: Composer Edition, Professional Edition, and Cluster Edition.

Intel® Parallel Studio XE Composer Edition provides a software tools environment for developing Fortran, C, and/or C++ code using Intel® Compilers. Intel® Parallel Studio XE Composer Edition also includes Intel® Math Kernel Library, Intel® Integrated Performance Primitives, Intel® Threading Building Blocks, and Intel® Data Analytics Acceleration Library (Intel® DAAL).

Intel® Parallel Studio XE Professional Edition adds Intel® VTune™ Amplifier for performance analysis, Intel® Inspector for correctness analysis, and Intel® Advisor for parallelism discovery.

Intel® Parallel Studio XE Cluster Edition adds support for distributed memory computing via Intel® MPI Library, Intel® MPI Benchmarks, and Intel® Trace Analyzer and Collector. Intel® Cluster Checker provides cluster health monitoring tools.

On completing the Intel® Parallel Studio XE installation process, locate the `getstart*.htm` file in the `documentation_2018/en/ps2018` folder under the target installation path. This file is a documentation map to navigate to various information resources of Intel® Parallel Studio XE.

For licensing information, please refer to the Intel End User Licensing Agreement (EULA) available at <https://software.intel.com/en-us/articles/end-user-license-agreement>.

When you install Intel® Parallel Studio XE, we collect information that helps us understand your installation status and environment. Information collected is anonymous and is not shared outside of Intel. See <https://software.intel.com/en-us/articles/data-collection> for more information on what is collected and how to opt-out.

1.1 What Every User Should Know About This Release

Intel® VTune™ Amplifier's Application Performance Snapshot is updated to give a fast way to see if your HPC application is making the best use of modern computer hardware. Get a quick overview of MPI, OpenMP*, Memory, and Floating Point metrics to see what kind of optimization will have the most impact.

2 Product Contents

The following table shows which Intel® Software Development Tools are present in each edition of Intel® Parallel Studio XE 2018.

Component	Composer Edition ¹	Professional Edition	Cluster Edition
Intel® C++ Compiler	X	X	X
Intel® Fortran Compiler / Intel® Visual Fortran	X	X	X
Intel® Integrated Performance Primitives (Intel® IPP)	X	X	X
Intel® Math Kernel Library (Intel® MKL)	X	X	X
Intel® Data Analytics Acceleration Library (Intel® DAAL) ²	X	X	X
Intel® Threading Building Blocks (Intel® TBB)	X	X	X
Intel-provided Debug Solutions	X	X	X
Microsoft Visual Studio Shell* for Intel® Visual Fortran (for Windows* OS only)	X	X	X
Intel® Advisor		X	X
Intel® Inspector		X	X
Intel® VTune™ Amplifier		X	X
Intel® Cluster Checker (For Linux* OS only)			X
Intel® MPI Benchmarks			X
Intel® MPI Library			X
Intel® Trace Analyzer and Collector			X

¹ Intel® Parallel Studio XE is only available in Composer Edition for macOS*.

² Intel® Integrated Performance Primitives, Intel® Data Analytics Acceleration Library, and Intel® Threading Building Blocks are not included in Fortran language only editions.

The table below lists the product components and related documentation.

Component	Version	Documentation
Intel® Advisor	2018	get_started.htm
Intel® C++ Compiler	18.0	get_started_wc.htm for Windows* OS get_started_lc.htm for Linux* OS
Intel® Cluster Checker (For Linux* OS only)	2018	get_started.htm
Intel® Data Analytics Acceleration Library (Intel® DAAL)	2018	get_started.htm
Intel® Fortran Compiler / Intel® Visual Fortran Compiler	18.0	get_started_wf.htm for Windows* OS get_started_lf.htm for Linux* OS
Intel® Inspector	2018	get_started.htm
Intel® Integrated Performance Primitives (Intel® IPP)	2018	get_started.htm
Intel® Math Kernel Library (Intel® MKL)	2018	get_started.htm
Intel® MPI Benchmarks	2018	ReadMe_IMB.txt IMB_Users_Guide.htm
Intel® MPI Library	2018	get_started.htm
Intel® Threading Building Blocks (Intel® TBB)	2018	get_started.htm
Intel® Trace Analyzer and Collector	2018	get_started.htm
Intel® VTune™ Amplifier	2018	get_started.htm
Intel-provided Debug Solutions		See below for additional information.
Microsoft Visual Studio Shell* for Intel® Visual Fortran (For Windows* OS; installs only on the master node)		See below for additional information.

2.1 Additional Information for Intel-provided Debug Solutions

The Intel-provided Debug solutions are based on GNU* GDB. Please see <https://software.intel.com/en-us/articles/intel-parallel-studio-xe-2018-composer-edition-fortran-debug-solutions-release-notes> and <https://software.intel.com/en-us/articles/intel-parallel-studio-xe-2018-composer-edition-c-debug-solutions-release-notes> for information specific to this component.

2.2 Additional Information for Microsoft Visual Studio Shell* for Intel® Visual Fortran

A Fortran-only Integrated Development Environment (IDE) based on Microsoft Visual Studio Shell 2015* is provided for systems that do not have a supported Microsoft Visual Studio installed. Installation of the Fortran IDE has the following additional requirements:

- Microsoft Windows 7 SP1* or newer, or Microsoft Windows Server 2012* or newer operating system
 - On Windows 8.1* and Windows Server 2012 R2*, KB2883200 is required
- Microsoft Windows 10 SDK*

2.3 Intel® Software Manager

The installation now provides an Intel® Software Manager to provide a simplified delivery mechanism for product updates and provide current license status and news on all installed Intel® software products.

3 What's New

This section highlights important changes from previous product versions. For more information on what is new in each component, see the individual component release notes. Documentation for all components is online at <https://software.intel.com/en-us/parallel-studio-xe/documentation/view-all>. A current list of deprecated features can be found at <https://software.intel.com/en-us/articles/intel-parallel-studio-xe-deprecation-information>.

Changes since Intel® Parallel Studio XE 2017 Update 4:

- Licensing system has been updated. Floating license servers must be updated to support this change. The current license manager software can be downloaded at <https://registrationcenter.intel.com>.
- Added support for new processors including Intel® Xeon Phi™ processors (code named Knights Landing and Knights Mill) and the Intel® Xeon® Processor Scalable family.
- Support for the Intel® Xeon Phi™ x100 product family coprocessor (formerly code name Knights Corner) is removed in this release. See section 3.1 for more information.
- All components updated to current versions.
- Release Notes merged into a single document for all operating systems.
- Added support for cluster installation on Windows Server* 2016 with HPC Pack 2016.
- Added cross-OS analysis to all license types. This enables data collection on one operating system family with analysis on a different operating system family under one license. To download installation packages for additional operating systems, visit <https://registrationcenter.intel.com>.
- Component documentation has been moved online and is available at <https://software.intel.com/en-us/parallel-studio-xe/documentation/view-all>.
- Intel® Software Manager can now be removed in Windows* via the Add/Remove Programs utility. See https://registrationcenter-ssl.intel.com/Docs/ism_v2.htm for more information.

- The Intel® Integrated Performance Primitives (Intel® IPP) Cryptography Library is now a standalone library and no longer depends on Intel® IPP. See <https://software.intel.com/en-us/get-ipp-cryptography-libraries> for information on how to obtain this library.
- Intel® Advisor:
 - Introduced general availability of the Cache-aware Roofline performance characterization model.
 - Added an experimental feature Hierarchical Roofline which is enabled by setting the environment variable ADVIXE_EXPERIMENTAL=roofline_ex before launching Intel® Advisor.
 - Added experimental support for accessing Intel® Advisor data via a Python* API. Examples are provided in the {install_dir}/pythonapi/examples directory.
- Intel® C/C++ Compiler:
 - Starting with Intel® C++ Compiler 18.0 Gold Release, Intel® Cilk™ Plus will be marked as deprecated and eventually removed in a future release. To learn how to migrate to OpenMP* or Intel® Threading Building Blocks, see [this article](#).
 - Optimizations for latest Intel® Xeon® and Intel® Xeon Phi™ Processors.
 - Initial support for OpenMP* 5.0.
 - Implementation of Parallel STL*.
- Intel® Cluster Checker:
 - Added support for Intel® Xeon® Scalable Processors.
 - Added Framework Definition feature to allow for customization of analysis.
 - Enhanced Intel® Omni-Path Architecture validation.
- Intel® Data Analytics Acceleration Library:
 - New Classification and Regression Decision Tree and Forest.
 - Introduced API modifications to streamline library usage and enable consistency across functionality.
 - New Neural Network optimizations and functionality.
- Intel® Distribution for Python*:
 - Added support for OpenCV*.
 - Updated to the latest Intel® Performance Libraries.
 - Improved NumPY* and SciPy* performance.
- Intel® Fortran Compiler:
 - Optimizations for latest Intel® Xeon® and Intel® Xeon Phi™ Processors.
 - Initial support for OpenMP* 5.0.
 - Full Fortran 2008, initial Fortran 2015 support.
- Intel® Inspector:
 - Added support for C++17 std::shared_mutex and Windows* SRW Locks.
 - Added support for cross-OS analysis to all license types. Installation packages can be downloaded from registrationcenter.intel.com.
 - Added integration and support for Microsoft Visual Studio* 2017.
- Intel® Integrated Performance Primitives:
 - Introduced patch files for the GraphicsMagick* source to provide drop-in optimization with Intel® IPP functions.

- Cryptography library has removed dependence on main Intel® Integrated Performance Primitives package.
 - Improved compression performance.
- Intel® Math Kernel Library:
 - Introduced compact GEMM and TRSM functions and optimized specific GEMM functions to work with quantized matrices.
 - Introduced optimizations for tall-and-skinny/short-and-wide matrices for certain matrix functions.
 - Introduced Verbose support for FFT domain.
- Intel® MPI Library:
 - Improved finalization time for OFI and TMI fabrics.
 - Added support for latest Intel® Xeon® Processors, Intel® Xeon Phi™ Processors and Coprocessors, and Intel® Omni-Path Architecture fabric.
- Intel® Threading Building Blocks:
 - Implementation of Parallel Standard Template Library.
 - Adds capability to parallelize and vectorize with compiler with minimal code change.
 - Adds ability to specify execution policies.
- Intel® Trace Analyzer and Collector:
 - Added OpenSHMEM* support.
 - Added support for latest Intel® Xeon® Processors, Intel® Xeon Phi™ Processors and Coprocessors, and Intel® Omni-Path Architecture fabric.
- Intel® VTune™ Amplifier:
 - Python* profiling enhanced with Locks and Waits and Memory Consumption analysis to enable tuning threaded performance of mixed Python* and native code.
 - Adds support for profiling inside Docker* and Mesos* containers.
 - Improved Application Snapshot merges MPI Performance Snapshot with previous Application Performance Snapshot.

Changes since Intel® Parallel Studio XE 2017 Update 2:

- All components updated to current versions.
- Added support for Microsoft Visual Studio 2017*.
- Added support for installing some libraries from yum, apt, or conda. See <https://software.intel.com/en-us/articles/installing-intel-free-libs-and-python-yum-repo>, <https://software.intel.com/en-us/articles/installing-intel-free-libs-and-python-apt-repo>, and <https://software.intel.com/en-us/articles/using-intel-distribution-for-python-with-anaconda> for more information.
- Added support for cluster installation on Microsoft Windows Server 2016*.
- Intel® Math Kernel Library (Intel® MKL), Intel® Integrated Performance Primitives (Intel® IPP) are distributed under Intel Simplified Software License. Intel® Threading Building Blocks (Intel® TBB) for Linux*, and macOS*, Intel® Data Analytics Acceleration Library (Intel® DAAL) for Linux*, and macOS* are distributed under Apache License, Version

2.0. See compilers_and_libraries_2017/licensing/ folder under the target installation path for reference.

- Intel® Advisor:
 - Added recommendation to force scalar remainder for loops with low mask utilization on Intel® AVX-512.
 - Extended “Gather recommendation” with “Constant (non-unit)” pattern.
- Intel® Data Analytics Acceleration Library:
 - License switched to Apache License 2.0*.
- Intel® MPI Library:
 - Improved MPI startup up to 8.5x on large HPC systems.
 - Intel® Omni-Path Architecture is used by default on systems with multiple fabrics.
- Intel® Threading Building Blocks:
 - Adds a graph/matmult example to demonstrate support for compute offload to Intel® Graphics Technology in the flow graph API.
 - The “compiler” build option now allows the user to specify a full path to the compiler.
- Intel® VTune™ Amplifier XE:
 - Added support for Intel® Xeon Phi™ coprocessor code named Knights Landing from Windows* host.
 - Enhanced support for applications using Intel® Threading Building Blocks that helps identify code inefficiencies causing high Overhead and Spin time.
 - Automated installation of collectors on a remote Linux* target system.

Changes since Intel® Parallel Studio XE 2017 Update 1:

- All components updated to current versions.
- Migration to SHA-256 digital signatures on Linux*.
- Intel® Advisor:
 - Roofline Analysis is released as a public feature.
 - Added call stacks for FLOPS and Trip Counts that enable total metrics.
 - Filter by module for Survey, FLOPS, and Trip Counts collections.
- Intel® Cluster Checker:
 - Added additional support for Intel® Xeon Phi™ Product Family x200 processors.
 - Added additional support for Intel® Omni-Path Architecture.
- Intel® Data Analytics Acceleration Library:
 - Added Deep Learning feature extensions.
 - Added API extensions for data parallelism scheme.
- Intel® Inspector:
 - Support for C++17 std::shared_mutex.
- Intel® Integrated Performance Primitives:
 - Introduced support for Intel® Xeon Phi™ processor x200 leverage boot mode in examples.
 - Added new functions in ZLIB to support user-defined Huffman tables.
- Intel® Math Kernel Library:

- Intel® AVX-512 code is dispatched by default on Intel® Xeon® processors.
 - Added support for Intel® Threading Building Blocks in various functions.
- Intel® MPI Library:
 - Added a new environment variable, I_MPI_MEMORY_LOCK, to prevent memory swapping to the hard drive.
- Intel® Threading Building Blocks:
 - Added template class gfx_factory to the flow graph API.
 - Fixed a possible deadlock caused by missed wakeup signals in task_arena::execute().
- Intel® Trace Analyzer and Collector:
 - Improved the color changing scheme.
 - Added Pcontrol support in MPI Performance Snapshot.
 - Added idle time per function in MPI Performance Snapshot.
- Intel® VTune™ Amplifier:
 - Added support for mixed Python* and native code in Locks and Waits analysis.
 - Added support for performance analysis of a guest Linux* operating system via Kernel-based Virtual Machine (KVM) from a Linux* host system with the KVM Guest OS option.
 - Enriched HPC Performance Characterization.

Changes since Intel® Parallel Studio XE 2017:

- All components updated to current versions.
- Japanese localization added for most components.
- Default installation includes components for 32-bit targets on Linux*.
- Intel® Advisor:
 - Extended recommendations for virtual methods in vectorized loops.
- Intel® C/C++ Compiler:
 - Bug fixes.
- Intel® Cluster Checker:
 - Added Intel® Scalable System Framework support.
 - Added additional support for Intel® Xeon Phi™ Product Family x200 processors.
 - Removed heartbeat functionality.
- Intel® Data Analytics Acceleration Library:
 - Added distributed neural network training.
 - Added KNN algorithm for batch computing mode.
 - Added min-max normalization.
- Intel® Integrated Performance Primitives:
 - Added functions for the finite field GF(p) arithmetic, and the elliptic curves over the finite field GF(p)
 - Added ippsECCPBindGxyTblStd functions that allow to control memory size for the elliptic curves over GF(p).
- Intel® Math Kernel Library:
 - Added support of non-square cores of convolution.

- Improved performance of ?GETRF, ?GETRS and ?GETRI for very small matrices via MKL_DIRECT_CALL.
- Improved single thread SGEMM/DGEMM performance on Intel® Advanced Vector Extensions 2 (Intel® AVX2), Intel® Advanced Vector Extensions 512 (Intel® AVX-512), and Intel® Xeon® for Intel® Many Integrated Core Architecture.
- Intel® MPI Library:
 - PMI-2 support for SLURM*.
 - Deprecating support for cross-OS launches.
 - Deprecating support for DAPL, TMI, and OFA fabrics.
- Intel® Threading Building Blocks:
 - Bug fixes.
- Intel® Trace Analyzer and Collector:
 - Introduced mouse wheel zooming support for timelines.
 - MPI Performance Snapshot adds a new diagram Node-to-Node Data Transfers.
 - MPI Performance Snapshot adds support for non-MPI applications.
- Intel® VTune™ Amplifier:
 - Support for locator hardware event metrics for the General Exploration analysis results in the Source/Assembly view that enable you to filter the data by a metric of interest and identify performance-critical code lines/instructions.
 - Summary view of the General Exploration analysis extended to explicitly display measure for the hardware metrics: Clockticks vs. Pipeline Slots.
 - Command line summary report for the HPC Performance Characterization analysis extended to show metrics for CPU, Memory, and FPU performance aspects including issue descriptions for metrics that exceed the predefined threshold.

3.1 Intel® Xeon Phi™ Product Family Updates

3.1.1 Intel® Xeon Phi™ 7200 Coprocessor (codenamed Knights Landing coprocessor)

Intel continually evaluates the markets for our products in order to provide the best possible solutions to our customer's challenges. As part of this on-going evaluation process Intel has decided to not offer Intel® Xeon Phi™ 7200 Coprocessor (codenamed Knights Landing Coprocessor) products to the market.

- Given the rapid adoption of Intel® Xeon Phi™ 7200 processors, Intel has decided to not deploy the Knights Landing Coprocessor to the general market.
- Intel® Xeon Phi™ Processors remain a key element of our solution portfolio for providing customers the most compelling and competitive solutions possible.

3.1.2 Support for the Intel® Xeon Phi™ x100 product family coprocessor (formerly code name Knights Corner) is removed in this release

The Intel® Xeon Phi™ x100 product family coprocessor (former code name Knights Corner) was officially announced end of life in January 2017. As part of the end of life process, the support for this family will only be available in the Intel® Parallel Studio XE 2017 version. Intel® Parallel Studio XE 2017 will be supported for a period of 3 years ending in January 2020

for the Intel® Xeon Phi™ x100 product family. Support will be provided for those customers with active support.

4 System Requirements

4.1 Processor Requirements

Systems based on Intel® 32 architecture are supported as target platforms. Systems based on Intel® 64 architectures below are supported both as host and target platforms.

Systems based on Intel® 64 architecture:

- Intel® Core™ processor family or higher
- Intel® Xeon® E5 v5 processor families recommended
- Intel® Xeon® E7 v5 processor families recommended

NOTE: It is assumed that the processors listed above are configured into homogeneous clusters.

4.2 Disk Space Requirements

12 GB of disk space (minimum) on a standard installation. Cluster installations require an additional 4 GB of disk space.

NOTE: During the installation process, the installer may need up to 12 GB of additional temporary disk storage to manage the intermediate installation files.

4.3 Operating System Requirements

The operating systems listed below are supported by all components on Intel® 64 Architecture. Individual components may support additional operating systems and architecture configurations. See the individual component release notes for full details.

- Debian* 8, 9
- Fedora* 25, 26
- Red Hat Enterprise Linux* 6, 7 (equivalent CentOS versions supported, but not separately tested)
- SUSE Linux Enterprise Server* 11, 12
- Ubuntu* 14.04, 16.04, 17.04
- Microsoft* Windows* 7, 8.x, 10
- Microsoft* Windows* Server 2008, 2008 R2, 2012, 2012 R2, 2016
- macOS* 10.12

The Intel® MPI Library and Intel® Trace Analyzer and Collector are supported on Intel® Cluster Ready systems and HPC versions of the listed versions of Microsoft* Windows* Server. These components are not supported on Ubuntu non-LTS systems.

Installation on IA-32 hosts is no longer supported by any components.

4.4 Memory Requirements

2 GB RAM (minimum)

4.5 Additional Software Requirements

Development for a 32-bit target on a 64-bit host may require optional library components (ia32-libs, lib32gcc1, lib32stdc++6, libc6-dev-i386, gcc-multilib, g++-multilib) to be installed from your Linux distribution.

On Microsoft Windows* OS, the Intel® C/C++ Compiler and Intel® Visual Fortran Compiler require a version of Microsoft Visual Studio* to be installed. The following versions are currently supported:

- Microsoft Visual Studio* 2013, 2015, 2017
- Microsoft Visual Studio Express* (only for command line compilation)

On macOS*, the Intel® C/C++ Compiler and Intel® Fortran Compiler require a version of Xcode* to be installed. The following versions are currently supported:

- Xcode* 8

5 Installation Notes

For instructions on installing and uninstalling the Intel® Parallel Studio XE Cluster Edition on Linux* OS and Windows* OS, see the Installation Guide (Install_Guide.pdf).

The installation of the product requires a valid license file or serial number. If you are evaluating the product, you can also choose the “Evaluate this product (no serial number required)” option during installation. The evaluation option will download an evaluation license, and requires an internet connection.

5.1 Installation on macOS*

If you will be using Xcode*, please make sure that a supported version of Xcode is installed. If you install a new version of Xcode in the future, you must reinstall Intel® Parallel Studio XE afterwards.

The `Command Line Tools` component, required for command-line development, is not installed by default. It can be installed using the Components tab of the Downloads preferences panel.

You will need to have administrative or “sudo” privileges to install, change or uninstall the product.

Follow the prompts to complete installation.

Note that there are several different downloadable files available, each providing different combinations of components. Please read the download web page carefully to determine which file is appropriate for you.

You do not need to uninstall previous versions or updates before installing a newer version – the new version will coexist with the older versions.

5.2 License Changes

The 'named-user' license provisions in the Intel software EULA (available as 'EULA.rtf' or 'EULA.txt' in the same product directory as this release note) changed to only allow the software to be installed on up to three systems, tracked by the system host ID. In order to install on another system after you have reached this limit, you will need to release an old system host ID from the registration system.

As an additional consequence to this change as well as some changes to the license design, you will need an updated license to use the production version of Intel® Parallel Studio XE 2016 or later versions. Additional information is provided [here](#). If you have further questions or concerns, please contact [Technical Support](#).

5.3 Floating License Server Update

Floating licenses of the 2018 version of Intel® Software Development Tools require the latest version of Intel® Software License Manager for successful installation. For the latest version of Intel® Software License Manager, please go to [Intel Registration Center](#). For more details, see [Installation Errors Related to Intel Software License Manager Upgrade](#).

5.4 Online Installation

The electronic installation package for Intel® Parallel Studio XE now offers as an alternative a smaller installation package that dynamically downloads and then installs packages selected to be installed. This requires a working internet connection and potentially a proxy setting if you are behind an internet proxy. Full packages are provided alongside where you download this online install package if a working internet connection is not available. The online installer may be downloaded and saved as an executable file which can then be launched from the command line.

5.5 Silent Install

For information on automated or "silent" install capability, please see <http://intel.ly/nKrzhy>.

5.5.1 Support of Non-Interactive Custom Installation

Intel® Parallel Studio XE supports the saving of user install choices during an 'interactive' install in a configuration file that can then be used for silent installs. This configuration file is created when the following option is used from the command line install:

- `--duplicate=config_file_name`: it specifies the configuration file name. If full path file name is specified, the "`--download-dir`" is ignored and the installable package will be created under the directory where configuration file is.
- `--download-dir=dir_name`: optional, it specifies where the configuration file will be created. If this option is omitted, the installation package and the configuration file will be created under the default download directory:
 - Windows: %Program Files%\Intel\Download\`<package_id>`
 - Linux: /tmp/`<UID>`/`<package_id>`
 - macOS:
 - /Volumes/`<package_id>`/`<package_id>`.app/Contents/MacOS/

For example: `parallel_studio_xe_<version>_setup.exe --duplicate=ic16_install_config.ini --download-dir="C:\temp\custom_pkg_ic16"`
 The configuration file and installable package will be created under "C:\temp\custom_pkg_ic16".

5.6 Using a License Server

If you have purchased a "floating" license, see <http://intel.ly/pjGfwC> for information on how to install using a license file or license server. This article also provides a source for the Intel® License Server that can be installed on any of a wide variety of systems.

6 Documentation

The documentation index file `getstart*.htm` provides more information about Intel® Parallel Studio XE.

Note: Some hyperlinks in HTML documents may not work when you use Internet Explorer*. Try using another browser, such as Chrome* or Firefox*, or right-click the link, select **Copy shortcut**, and paste the link into a new Internet Explorer* window.

7 Issues and Limitations

1. Multiple problems have been encountered regarding integration with Microsoft Visual Studio* 2017. Please see <https://software.intel.com/en-us/articles/intel-software-development-tools-integration-to-vs2017-issue> for a current list of related issues along with known resolutions and workarounds.
2. In certain cases, Microsoft Visual Studio* 2017 is known to not install completely. This can lead to an incomplete install of Intel® Parallel Studio XE 2018. We are aware of this issue and at this time, we do not have a known workaround. Re-installing Visual Studio may correct the situation. Please contact customer support for additional information if you encounter difficulties installing with Visual Studio 2017.

3. There have been situations where during the installation process, `/tmp` has been filled up. We recommend that you have **at least 12 GB of free space** in `/tmp` when installing the Intel® Parallel Studio XE. Also, the installer script `install.sh` has the command-line options:

```
-t [FOLDER]
```

or

```
--tmp-dir [FOLDER]
```

where `[FOLDER]` is a directory path, which can direct the use of intermediate storage to another disk partition referenced by `[FOLDER]`. `[FOLDER]` should be a non-shared storage location on each node of the cluster. Note that `[FOLDER]` should also contain **at least 12 GB of free space**.

4. On Linux* OS, if any software component of the Intel® Parallel Studio XE is detected as pre-installed on the head node, that software component will not be processed by the installer. There is a similar problem on Windows* OS in the 'Modify' mode. For Windows* OS, if some software component of the Intel® Parallel Studio XE is pre-installed on the head node using the installer, that software component will not be installed on the compute nodes of the cluster. For either Linux* OS or Windows* OS, if you already installed some of the software components only on the head node, and you want to install them on the other nodes using the installer, you need to uninstall such components from the head node manually before starting the installer.
5. Intel® Parallel Studio XE for Windows* OS requires **the creation and use of symbolic links for installation of the Intel® software product components**. If you have a File Allocation Table (FAT32) file system deployed on your Windows* OS platform, these symbolic links cannot be created and the integrity of the Intel® Parallel Studio XE installation is compromised.
6. For Intel® MIC Architecture, Intel® MPI Library supports only Intel® Xeon Phi™ Coprocessor.

This release of the Intel® MPI Library for Linux* OS does not support the MPD process manager for Intel® Xeon Phi™ Coprocessor.

Intel® MPI Library for Linux* OS supports multiple DAPL* providers for communication between the host and the Intel® Xeon Phi™ Coprocessor and between several Intel® Xeon Phi™ Coprocessors inside one node.

Currently supported providers are DAPL over InfiniBand* Architecture and DAPL over Intel® Symmetric Communication Interface (Intel® SCI). This feature requires using symbolic names in the host file.

7. Intel® Software Manager will always install to either `/opt` or `$HOME` on Linux* OS even if a custom installation path is chosen. This can slow installation when the destination folder is a slow NFS shared folder, even if locally hosted.
8. In some situations, if a Windows OS computer has been updated but not restarted and the Visual Studio Shell is to be installed, Intel® Parallel Studio XE installation will fail with the error message “Intel(R) Parallel Studio XE 2018 Cluster Edition for Windows* Setup Wizard ended prematurely because of an error(s).” The failing module is `vs_isoshell.exe`. To work around this issue, restart your computer and repeat the installation process.

8 Technical Support

Your feedback is very important to us. To receive technical support for the tools provided in this product and technical information including FAQ's and product updates, you are encouraged to register your product at the Intel® Software Development Products Registration Center.

NOTE: Registering for support varies for release product or pre-release products (alpha, beta, etc.) – only released software products have support web pages at <http://software.intel.com/sites/support/>.

To register for an account, please visit the Intel® Software Development Products Registration Center website at <http://www.intel.com/software/products/registrationcenter/index.htm>. If you have forgotten your password, please follow the instructions on the login page for forgotten password.

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The original versions of the BLAS from which that part of Intel® MKL was derived can be obtained from <http://www.netlib.org/blas/index.html>.

The original versions of LAPACK from which that part of Intel® MKL was derived can be obtained from <http://www.netlib.org/lapack/index.html>. The authors of LAPACK are E. Anderson, Z. Bai, C. Bischof, S. Blackford, J. Demmel, J. Dongarra, J. Du Croz, A. Greenbaum, S. Hammarling, A. McKenney, and D. Sorensen. Our FORTRAN 90/95 interfaces to LAPACK are similar to those in the LAPACK95 package at <http://www.netlib.org/lapack95/index.html>. All interfaces are provided for pure procedures.

The original versions of ScaLAPACK from which that part of Intel® MKL was derived can be obtained from <http://www.netlib.org/scalapack/index.html>. The authors of ScaLAPACK are L. S. Blackford, J. Choi, A. Cleary, E. D'Azevedo, J. Demmel, I. Dhillon, J. Dongarra, S. Hammarling, G. Henry, A. Petit, K. Stanley, D. Walker, and R. C. Whaley.

The Intel® MKL Extended Eigensolver functionality is based on the Feast Eigenvalue Solver 2.0 <http://www.ecs.umass.edu/~polizzi/feast/>.

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