-----

Intel(R) MPI Library 5.1 Update 3 for Linux\* OS
Release Notes

Contents

-----

- Overview
- What's New
- Key Features
- System Requirements
- Installation Notes
- Documentation
- Special Features and Known Issues
- Technical Support
- Copyright and Licenses
- Disclaimer and Legal Information

Overview

-----

The Intel(R) MPI Library for Linux\* OS is a multi-fabric message passing library based on ANL\* MPICH3\* and OSU\* MVAPICH2\*.

The Intel(R) MPI Library for Linux\* OS implements the Message Passing Interface, version 3.0 (MPI-3) specification.

To receive technical support and updates, you need to register your Intel(R) Software Development Product. See the Technical Support section.

**Product Contents** 

\_ \_ \_ \_ \_ \_ \_ \_ \_ \_ \_ \_ \_ \_ \_ \_

The Intel(R) MPI Library Runtime Environment (RTO) contains the tools you need to run programs including scalable process management system (Hydra\*) and supporting utilities, shared (.so) libraries, and documentation.

The Intel(R) MPI Library Development Kit (SDK) includes all of the Runtime Environment components and compilation tools: compiler commands (mpicc, mpiicc, etc.), include files and modules, static (.a) libraries, debug libraries, and test codes.

Related Products and Services

-----

Information on Intel(R) Software Development Products is available at http://www.intel.com/software/products.

What's New

\_\_\_\_\_

See http://intel.ly/10zXhpI for a current list of deprecated features.

Intel(R) MPI Library 5.1 Update 3 Build 223:

- Fix for the MPI\_Abort call with threaded applications issue

# Intel(R) MPI Library 5.1 Update 3:

- Deprecation list updated. New deprecations:
  - SSHM
  - MPD
  - Epoll
  - JMI
  - PVFS2
- Fixed shared memory problem on Intel(R) Xeon Phi (codename: Knights Landing).
- New algorithms and selection mechanism for nonblocking collectives.
- New psm2 option for Intel(R) Omni-Path.
- Added I MPI BCAST ADJUST SEGMENT variable to control MPI Bcast.
- Fixed long count support for some collective messages.
- Binding kit reworked with support for Intel(R) Many Integrated Core Architecture and support for ILP64 on third party compilers.

#### Intel(R) MPI Library 5.1 Update 2:

- Intel(R) MPI Library supports YARN\* cluster manager. Llama\* should be installed on the cluster.
- DAPL library UCM settings are automatically adjusted for MPI jobs of more than 1024 ranks, resulting in more stable job start-up.
- ILP64 support enhancements, support for MPI modules in Fortran 90.
- Added the direct receive functionality for the TMI fabric.
- Single copy intra-node communication using Linux supported cross memory attach (CMA) is now default.

#### Intel(R) MPI Library 5.1 Update 1:

- Change the named-user licensing scheme. See more details in the Installation Instructions section of Intel(R) MPI Library Installation Guide.
- Bug fixes

#### Intel(R) MPI Library 5.1:

- Supported OpenFabrics Interface\* (OFI\*) v1.0 API
- Supported Fortran 2008
- Updated the default value for I\_MPI\_FABRICS\_LIST. See the Intel(R) MPI Library Reference Manual for details
- Added the Troubleshooting chapter to the Intel(R) MPI Library User's Guide.
- Added new application specific features in the Automatic Tuner and Hydra

process manager. See the MPI Tuner Tutorial for details.

- Added the MPI Pcontrol feature for internal statistics.
- Increased the possible space for MPI\_TAG.
- Changed the default product installation directories. See the README document for details.
- Bug fixes

#### Note:

Intel(R) MPI Benchmarks is delivered as part of Intel(R) MPI Library. For the new features of Intel MPI Benchmarks, see the What's New section in Intel(R) MPI Benchmarks README.

# Intel(R) MPI Library 5.0 Update 3:

- Support for IBM\* General Parallel File System\* (GPFS\*) through the new environment variable I MPI EXTRA FILESYSTEM
- Support for the new bootstrap server, pbsdsh
- Support for the rename mechanism for the file, stats.txt, to avoid overwriting
- Native statistics collection can be controlled with MPI Pcontrol
- Bug fixes

#### Intel(R) MPI Library 5.0 Update 2:

- Single copy intra-node communication using Linux supported cross memory attach (CMA)
- Multi-threaded optimized library is used by default for linking with single and multi-threaded MPI applications
- Added gtool option for running external tools
- Tuning for TMI fabrics on processors using the Haswell microarchitecure
- Enhancements to statistics gathering mode
- Bug fixes

# Intel(R) MPI Library 5.0 Update 1:

- Changed settings for non-default installation path. If you choose to install Intel(R) MPI Library to a non-default path, impi/<version>.<package> will be appended to the selected installation path. Use symbolic links to this path if you need a specific pathname.
- Directory structure update. New links have been added to always point to the most recently installed version of the Intel(R) MPI Library.
- Bug fixes, including:
  - o Avoid conflict with TMI ADI messages, resolving hangs for LS-Dyna\*
- Collective performance improvements
- Documentation update
- Man pages copyright updated
- Added support for -fopenmp in mpiicc, mpiicpc, and mpiifort
- Improved pinning under job schedulers

# Intel(R) MPI Library 5.0:

- Improved application binary interface (ABI) compatibility between different

#### MPI implementations

- Improved performance of the OFA fabric on Intel(R) Xeon(TM) E5 V2 and Intel(R) Xeon(TM) E7 V2 family processors
- Add the option I\_MPI\_JOB\_RESPECT\_PROCESS\_PLACEMENT to honour process placement from job schedulers
- Remove all IA-32 architecture support
- Add the debug information into optimized single/multi-threaded libraries
- Implement the MPI-3 standard including but not limited to:
  - o Non-blocking collective operations
  - o Fast one-sided operations
  - o Large counts for messages greater than 2GB
- Support for Parallel Distributed Shell\*
- Allow permuted entries in machine file when running a single instance of pmi-proxy
- Support for mixed operating systems in the Hydra process manager (Linux\* OS and Windows\* OS)
- Make the following changes to the documentation:
  - o Change the Intel(R) MPI Library Getting Started Guide to Intel(R) MPI Library User's Guide
  - o Add the Intel(R) MPI Library Getting Started page
  - o Add the tutorial: MPI Tuner for Intel(R) MPI Library
- Bug fixes
- Deprecate MPD process managers

-----

32 Bit Support of Intel(R) MPI Library & Intel(R) Trace Analyzer and Collector Inclusion of 32-bit binaries in the Intel(R) MPI Library and Intel(R) Trace Analyzer and Collector products is being deprecated. If 32-bit support is required, we advise that you remain on Intel(R) MPI Library version 4.1 Updates and Intel(R) Trace Analyzer and Collector version 8.1 Updates, which continue to include 32-bit binaries. The Intel(R) MPI Library 5.0 and Intel(R) Trace Analyzer and Collector 9.0 releases will not include 32-bit binaries. Many developers have already migrated to 64-bit implementations of both their applications and of Intel libraries and tools. If you have concerns about this deprecation, please send us feedback by submitting an issue at the Intel(R) Premier Customer Support site (http://premier.intel.com) as soon as possible with your contact information.

#### Intel(R) MPI Library 4.1 Build 049:

- Fix the out-of-session integration with IBM\* Platform\* LSF\* for large jobs
- Improve in the memory consumption when using DAPL\* UD fabric
- Fix the segmentation fault when using I MPI WAIT MODE=1

#### Intel(R) MPI Library 4.1 Build 048:

- Fix the crash of multithreaded MPI Bsend with attached buffer
- Fix the crash of MPI\_Alltoall algorithm #1 with special vector datatypes
- Add I\_MPI\_JOB\_RESPECT\_PROCESS\_PLACEMENT environment variable support that

controls respect of the Job Scheduler provided process-per-node parameter

# Intel(R) MPI Library 4.1 Update 3:

- Intel(R) Xeon(TM) E5 V2 and Intel(R) Xeon(TM) E7 V2 family processors additional performance tuning
- New online documentation format
- Bug fixes

# Intel(R) MPI Library 4.1 Update 2:

- Intel(R) Xeon(TM) E5 V2 and Intel(R) Xeon(TM) E7 V2 family processors performance tuning
- Intel(R) Many Integrated Core Architecture (Intel(R) MIC) Architecture performance improvements and heterogeneous performance tuning (MT memcpy)
- Support for Parallel Distributed Shell\*
- Allow permuted entries in machine file when running a single instance of pmi-proxy
- Tag Matching Interface\* (TMI\*) fabric latency and scalability improvements
- Bug fixes

### Intel(R) MPI Library 4.1 Update 1:

- Improved Non-Uniform Memory Access\* (NUMA\*) support
- Advanced pinning support: the excluded processor list and respect CPU affinity
- DAPL\* auto provider selection mechanism enhancements
- New OFA connection manager aimed for scalability improvements
- Tag Matching Interface\* (TMI\*) version 1.1 support
- New checkpoint-restart control options
- Extended control options for Intel(R) Xeon Phi(TM) coprocessor operating system
- Intel(R) Xeon Phi(TM) Coprocessor Communication Link (CCL) proxy support
- Checkpoint-restart support through OFA network module and Hydra\* process manager for Intel(R) Xeon Phi(TM) coprocessors
- Tag Matching Interface (TMI) support with Intel(R) True Scale Fabric (former QLogic\*) performance scaled messaging\* (PSM\*) network fabric for the Intel(R) Xeon Phi(TM) Coprocessor (codename: Knights Corner)
- Backward compatibility with Intel MPI Library 4.1 based applications
- Optional GUI installation
- Bug Fixes

#### Intel(R) MPI Library 4.1:

- Support of the Intel(R) Xeon Phi(TM) Coprocessor (codename: Knights Corner) based on Intel(R) Many Integrated Core Architecture (Intel(R) MIC Architecture). See Release Notes Addendum for more information.
- Support for the MPI-2.2 standard
- Backward compatibility with Intel MPI Library 4.0.x based applications
- Support for clusters with different Intel(R) Architecture Processors
- Support Checkpoint-Restart through OFA network module and Hydra process manager, based on the Berkeley Checkpoint-Restart Library\* (blcr) underlying

system library.

- Support for the PBS Pro\* job management system
- Support for Intel(R) Composer XE 2013
- New documentation in the HTML format
- Bug Fixes

Key Features

This release of the Intel(R) MPI Library supports the following major features:

- MPI-1, MPI-2.2 and MPI-3 specification conformance
- Support for Knights Landing.
- Support for any combination of the following interconnection fabrics:
  - o Shared memory
  - o Network fabrics with tag matching capabilities through Tag Matching Interface (TMI), such as Intel(R) True Scale Fabric, Infiniband\*, Myrinet\* and other interconnects
  - o Native InfiniBand\* interface through OFED\* verbs provided by Open Fabrics Alliance\* (OFA\*)
  - o OpenFabrics Interface\* (OFI\*)
  - o RDMA-capable network fabrics through DAPL\*, such as InfiniBand\* and Myrinet\*
  - o Sockets, for example, TCP/IP over Ethernet\*, Gigabit Ethernet\*, and other interconnects
- Support for the following MPI communication modes related to Intel(R)
   Xeon Phi(TM) coprocessor:
  - o Communication inside the Intel(R) Xeon Phi(TM) coprocessor
  - o Communication between the Intel(R) Xeon Phi(TM) coprocessor and the host CPU inside one node
  - o Communication between the Intel(R) Xeon Phi(TM) coprocessors inside one node
  - o Communication between the Intel(R) Xeon Phi(TM) coprocessors and host CPU between several nodes
- (SDK only) Support for Intel(R) 64 architecture and Intel(R) MIC Architecture clusters using:
  - o Intel(R) C++ Compiler for Linux\* OS version 14.0 through 16.0
  - o Intel(R) Fortran Compiler for Linux\* OS version 14.0 through 16.0
  - o GNU\* C, C++ and Fortran 95 compilers
- (SDK only) C, C++, Fortran 77, Fortran 90, and Fortran 2008 language bindings
- (SDK only) Dynamic or static linking

System Requirements

The following sections describe supported hardware and software.

# Release Notes.txt Supported Hardware Systems based on the Intel(R) 64 architecture, in particular: Intel(R) Core(TM) processor family or higher Intel(R) Xeon(R) E5 v3 processor families recommended Intel(R) Xeon(R) E7 v2 processor families recommended 1 GB of RAM per core 2 GB of RAM per core recommended 1 GB of free hard disk space Intel(R) Xeon Phi(TM) Coprocessor (codename: Knights Corner) based on Intel(R) MIC Architecture Supported Software \_\_\_\_\_\_ Operating Systems: (issues including installation ones are possible for Operating Systems that are not released at the date of Intel(R) MPI Library 5.0 release) Systems based on the Intel(R) 64 architecture: Red Hat\* Enterprise Linux\* 5, 6, 7 Fedora\* 21, 22 CentOS\* 6, 7, SUSE\* Linux Enterprise Server\* 11, 12, Ubuntu\* LTS 12.04, 14.04, Debian\* 7, 8 (SDK only) Compilers: GNU\*: C, C++, Fortran 77 3.3 or higher, Fortran 95 4.0 or higher Intel(R) C++ Compiler for Linux\* OS 13.1 through 15.0 or higher Intel(R) Fortran Compiler for Linux\* OS 13.1 through 15.0 or higher (SDK only) Supported Debuggers: Rogue Wave\* Software TotalView\* 6.8 or higher Allinea\* DDT\* v1.9.2 or higher GNU\* Debuggers

# Batch Systems:

Platform\* LSF\* 6.1 or higher
Altair\* PBS Pro\* 7.1 or higher
Torque\* 1.2.0 or higher
Parallelnavi\* NQS\* for Linux\* OS V2.0L10 or higher
Parallelnavi\* for Linux\* OS Advanced Edition V1.0L10A or higher

NetBatch\* v6.x or higher SLURM\* 1.2.21 or higher Sun\* Grid Engine\* 6.1 or higher IBM\* LoadLeveler\* 4.1.1.5 or higher Platform\* Lava\* 1.0

#### Recommended InfiniBand Software:

- OpenFabrics\* Enterprise Distribution (OFED\*) 1.5.4.1 or higher.
- Intel(R) True Scale Fabric Host Channel Adapter Host Drivers & Software (OFED+) v7.2.0 or higher.
- Mellanox\* OFED\* 1.5.3-4.0.42 or higher.

#### Additional Software:

- Python\* 2.2 or higher, including the python-xml module. Python\* distributions are available for download from your OS vendor or at http://www.python.org (for Python\* source distributions).
- An XML parser such as expat\* or pyxml\*.
- If using InfiniBand\*, Myrinet\*, or other RDMA-capable network fabrics, a DAPL\* 1.2 standard-compliant provider library/driver is required.
   DAPL\* providers are typically provided with your network fabric hardware and software.
- DAPL auto provider selection mechanism requires dapl-2.0.37 or higher for Intel(R) Xeon(R) and Intel(R) Manycore Platform Software Stack (Intel(R) MPSS) gold update 3 or higher for Intel (R) Xeon Phi(tm).
- Improved NUMA support requires dapl-2.0.37 or higher
- Intel(R) Many Integrated Core Platform Software Stack (MPSS) 2.1 Beta or higher for the respective host operating system. MPSS may support specific operating system/kernel only. For most recent specific operating system requirements, see

http://software.intel.com/en-us/articles/intel-mpi-library-for-linux-kb/all/
or http://software.intel.com/mic-developer .

Note that the Intel(R) MPI Library is tuned for optimum performance on the latest MPSS versions as of release time.

See the Intel(R) MPI Library for Linux\* OS Installation Guide for details.

Documentation

Intel(R) MPI Library for Linux\* OS Getting Started page, found in get\_started.htm, contains information on the following subject:

- Compiling and running your MPI program

Intel(R) MPI Library for Linux\* OS User's Guide, found in User\_Guide.htm (HTML Uncompressed Help) and User\_Guide.pdf, contains information on the following subjects:

- First steps using the Intel(R) MPI Library
- First-aid troubleshooting actions

Intel(R) MPI Library for Linux\* OS Reference Manual, found in
Reference\_Manual.htm (HTML Uncompressed Help) and Reference\_Manual.pdf,
contains information on the following subjects:

- Command Reference describes commands, options, and environment variables
- Tuning Reference describes environment variables that influence library behavior and performance

Intel(R) MPI Library for Linux\* OS Installation Guide, found in INSTALL.html, contains information on the following subjects:

- Obtaining, installing, and uninstalling the Intel(R) MPI Library
- Getting technical support

Tutorial: MPI Tuner for Intel(R) MPI Library, found in MPI\_Tuner\_Tutorial.pdf, contains information on the following subjects:

- How to use the MPI Tuner for Intel(R) MPI Library to get optimized configuration files for the runtime library automatically
- How to troubleshoot the common issues with the MPI tuner

Special Features and Known Issues

Note: The following list includes the information until Intel(R) MPI Library 5.1 is released. For the most up-to-date list of known issues, as well as latest tips and tricks on using the library, visit the Intel(R) MPI Library for Linux\* Knowledge Base at

http://software.intel.com/en-us/articles/intel-mpi-library-for-linux-kb/all/

- Intel(R) MPI Library is missing a shortcut switch to enable PSM2. To run PSM2, you must add the following three options to the command line:
  - -genv I\_MPI\_FABRICS tmi
  - -genv I\_MPI\_TMI\_PROVIDER psm2
  - -genv I MPI FALLBACK 0
- Switching on statistics gathering could result in increased time in MPI Finalize.
- Beginning in Intel(R) MPI Library 5.1, in systems where some nodes have only Intel(R) True Scale Fabric available, while others have both Intel(R) True Scale and e.g. Mellanox\* HCAs, automatic fabric detection will lead to a hang or failure, as the first type of nodes will select tmi, and the second type will select dapl as the internode fabric. To avoid this, explicitly specify a fabric that is available on all nodes as the internode fabric.
- Intel(R) MPI Library 5.0 for Linux\* OS is binary compatible with the majority of Intel MPI Library 4.1.x-based applications. Recompile your application only if you use:
  - o MPI\_Dist\_graph\_create, MPI\_Dist\_graph\_create\_adjacent,
     MPI\_Dist\_graph\_neighbors, MPI\_Dist\_graph\_neighbors\_count,
     (C, C++, Fortran)
     MPI::Get\_address (C++ only)
- The Intel(R) MPI Library 5.0 is incompatible with some versions of the Mellanox\* OFED drivers, including but not limited to 2.1-1.0.0, and can cause segmentation faults. Upgrade the driver to at least version 2.1-1.0.6 to correct this.
- Hydra hangs when used under versions 7.2 or 7.3 of the GNU\* Debugger gdb.
- In order to run a mixed operating system job (Linux\* and Windows\*), all binaries must link to the same single or multithreaded MPI library. The single and multithreaded libraries are incompatible with each other and should not be mixed. Note that the pre-compiled binaries for the Intel® MPI Benchmarks are inconsistent (Linux\* version links to multithreaded, Windows\* version links to single threaded) and as such, at least one must be rebuilt to match the other.
- Ubuntu\* does not allow attaching a debugger to a non-child process. In order to use -gdb, this must be disabled by setting the sysctl value /proc/sys/kernel/yama/ptrace\_scope to 0.
- The Intel(R) MPI Library does not support using the OFA fabric over an Intel(R) Symmetric Communications Interface (Intel(R) SCI)

adapter. If you are using an Intel SCI adapter, such as with Intel(R) Many Integrated Core Architecture, you will need to select a different fabric.

- The PSM TMI and OFI providers do not support messages larger than
   2^32 1 bytes. If you have messages larger than this limit, select a different fabric.
- Intel(R) Software Manager will always install to either /opt or \$HOME on Linux\* OS even if a custom installation path is chosen. This can slow installation when the destination folder is a slow NFS shared folder, even if locally hosted.
- Intel(R) MPI Library 4.1 for Linux\* OS is binary compatible with the majority of Intel MPI Library 4.0.x-based applications. Recompile your application only if you use: o MPI C++ binding
- Intel(R) MPI Library 4.1 for Linux\* OS implements the MPI-2.2 standard.

On top of this, the aliasing of the send and receive buffers in the following collective routines will be rejected:

- o MPI\_Gather, MPI\_Gatherv
- o MPI\_Scatter, MPI\_Scatterv
- o MPI Allgather, MPI Allgatherv
- o MPI\_Alltoall, MPI\_Alltoallv, MPI\_Alltoallw

If your application depends on the pre-MPI-2.2 behavior, set the environment variable I\_MPI\_COMPATIBILITY to 4. If your application depends on the pre-MPI-2.1 behavior, set the environment variable I\_MPI\_COMPATIBILITY to 3.

You will see the diagnostic messages below in case of buffer aliasing issue:

MPIR\_Localcopy(381)....: memcpy arguments alias each other

Known applications with such kind of limitations: SPEC MPI2007 V2.0, VASP 5.3

- Intel(R) MPI Library 4.0 for Linux\* OS is binary compatible with the majority of Intel MPI Library 3.x-based applications. Recompile your application only if you use:
  - o MPI one-sided routines in Fortran (mpi\_accumulate(), mpi\_alloc\_mem(),
     mpi\_get(), mpi\_put(), mpi\_win\_create())
  - o MPI C++ binding
- Intel(R) MPI Library 4.0 for Linux\* OS implements the MPI-2.1 standard. The functions of the following MPI routines have changed:

```
o MPI_Cart_create()
o MPI_Cart_map()
o MPI_Cart_sub()
o MPI_Graph_create()
```

If your application depends on the pre-MPI-2.1 behavior, set the environment variable I MPI COMPATIBILITY to "3".

- The following features are currently available only on Intel(R) 64 architecture:
  - o Native InfiniBand\* interface (OFED\* verbs) support
  - o Multirail capability
  - o Tag Matching Interface (TMI) support
  - o Connectionless DAPL\* UD support
- The Intel(R) MPI Library supports the MPI-2 process model for all fabric combinations with the following exceptions:
  - o I\_MPI\_FABRICS is set to <fabric1>:<fabric2>, where <fabric1> is not shm, and <fabric2> is not equal to <fabric1> (for example, dapl:tcp)
- If communication between two existing MPI applications is established using the process attachment mechanism, the library does not control whether the same fabric has been selected for each application. This situation may cause unexpected applications behavior. Set the I\_MPI\_FABRICS variable to the same values for each application to avoid this issue.
- The following restriction exists for the DAPL-capable network fabrics because it relates to support for the MPI-2 process model: if the size of the information about the host used to establish the communication exceeds a certain DAPL provider value, the application fails with an error message similar to:

[0:host1][../../dapl\_module\_util.c:397] error(0x80060028):...: could not\
connect DAPL endpoints: DAT\_INVALID\_PARAMETER(DAT\_INVALID\_ARG5)

- The Intel(R) MPI Library Development Kit package is layered on top of the Runtime Environment package. See the Intel(R) MPI Library for Linux\* OS Installation Guide for more details.
- The SDK installer checks for the existence of the associated RTO package and installs it if the RTO is missing. If the RTO is already present, its location determines the default SDK location.
- The RTO uninstaller checks for SDK presence and proposes to uninstall the SDK and RTO packages.
- The SDK uninstaller asks the user if the RTO is to be uninstalled as

well. The user is able to cancel the uninstallation at this point.

- The Intel(R) MPI Library automatically places consecutive MPI processes onto all processor cores. Use the mpiexec -perhost 1 option or set the I\_MPI\_PERHOST environment variable to 1 in order to obtain the round robin process placement.
- The Intel(R) MPI Library pins processes automatically. Use I\_MPI\_PIN and related environment variables to control process pinning. See the Intel(R) MPI Library for Linux\* OS Reference Manual for more details.
- The Intel(R) MPI Library provides thread-safe libraries up to level MPI\_THREAD\_MULTIPLE. The default level is MPI\_THREAD\_FUNNELED. Follow these rules:
  - o (SDK only) Use the Intel(R) MPI compiler driver option -mt\_mpi to build a thread-safe MPI application.
  - o Do not load thread-safe Intel(R) MPI libraries through dlopen(3).
- Intel(R) MKL 10.0 may create multiple threads depending on various conditions. Follow these rules to correctly use Intel(R) MKL:
  - o (SDK only) Use the thread safe version of the Intel(R) MPI Library in conjunction with Intel(R) MKL
  - o Set the OMP\_NUM\_THREADS environment variable to 1 to run the application if linked with the non-thread-safe version of the Intel(R) MPI Library
- The Intel(R) MPI Library uses dynamic connection establishment by default for 64 and more processes. To always establish all connections upfront, set the I MPI DYNAMIC CONNECTION environment variable to "disable".
- The Intel(R) MPI Library compiler drivers embed the actual Development Kit library path (default /opt/intel/compilers\_and\_libraries\_2016.<update>.<package#>/linux/mpi) and default Runtime Environment library path is under the Intel(R) MPI Library Development Kit (SDK) installation directory.
- Use the LD\_PRELOAD environment variable to pre-load the appropriate Intel(R) MPI binding library to start an MPICH2 Fortran application in the Intel(R) MPI Library environment.
- The Intel(R) MPI Library enhances message-passing performance on DAPL\*-based interconnects by maintaining a cache of virtual-to-physical address translations in the MPI DAPL\* data transfer path.

Set the environment variable LD\_DYNAMIC\_WEAK to "1" if your program dynamically loads the standard C library before dynamically loading the Intel(R) MPI Library. Alternatively, use the environment variable LD\_PRELOAD to load the Intel(R) MPI Library first.

To disable the translation cache completely, set the environment variable I\_MPI\_RDMA\_TRANSLATION\_CACHE to "disable". Note that you do not need to set the aforementioned environment variables LD\_DYNAMIC\_WEAK or LD\_PRELOAD when you disable the translation cache.

- (SDK only) Always link the standard libc libraries dynamically if you use the DAPL, OFA\*, and TMI fabrics, individually or in combination with the shared memory fabric, to avoid possible segmentation faults.

Note: some compilers may use the -static option implicitly, for example, when using the -fast option for the Intel compilers. Therefore, use the ldd command to verify that the final executable is dynamically linked with the standard libc libraries.

It is safe to link the Intel(R) MPI Library statically through the -static\_mpi option of the compiler drivers. This option does not affect the default linkage method for other libraries.

- Certain DAPL\* providers may not work or provide worthwhile performance with the Intel(R) MPI Library for Linux\* OS, for example:
  - o Intel(R) True Scale Fabric. Use the TMI libraries included with the Intel(R) MPI Library when running over the Intel(R) True Scale Fabric PSM\* interconnect interface for best performance.
  - o Myricom\*. Use the TMI libraries included with the Intel(R) MPI Library when running over the Myricom\* MX\* interconnect interface for best performance.
    - Alternatively, contact Myricom\* or download the DAPL\* provider at http://sourceforge.net/projects/dapl-myrinet which supports both the GM\* and MX\* interfaces.
- Depending on Intel(R) True Scale Fabric hardware, PSM\* may not support CPU over-subscription of the node. The maximum amount of processes which can be run on the node is limited and depends on particular Intel(R) True Scale Fabric hardware and amount of CPU cores. The GM DAPL\* provider may not work with the Intel(R) MPI Library for Linux\* OS using some versions of the GM\* drivers. Set I MPI RDMA RNDV WRITE=1 to avoid this issue.
- Certain DAPL\* providers may not function properly if your application uses system(3), fork(2), vfork(2), or clone(2) system calls. Do not use these system calls or functions based upon them. For example, system(3), with: o OFED\* DAPL\* provider with Linux\* kernel version earlier than official version 2.6.16. Set the RDMAV\_FORK\_SAFE environment variable to enable
- MPI MPROBE and MPI IMPROBE are not supported by TMI or OFI fabrics.
- MPI Cancel functionality is not supported by TMI or OFI fabrics.

the OFED workaround with compatible kernel version.

- The Intel(R) MPI Library requires Python\* 2.2 or higher for process management.
- The Intel(R) MPI Library requires the python-xml\* package or its equivalent on each node in the cluster for process management.
- The Intel(R) MPI Library requires the expat\* or pyxml\* package, or an equivalent XML parser on each node in the cluster for process management.
- The following MPI-2.2 features are not supported by the Intel(R) MPI Library:
  - o Passive target one-sided communication when the target process does not call any MPI functions
- If installation of the Intel(R) MPI Library package fails and shows the error message: "Intel(R) MPI Library already installed" when a package is not actually installed, try the following:
  - 1. Determine the package number that the system believes is installed by typing:

```
# rpm -qa | grep intel-mpi
```

This command returns an Intel(R) MPI Library <package name>.

2. Remove the package from the system by typing:

```
# rpm -e <package name>
```

3. Re-run the Intel(R) MPI Library installer to install the package.

#### TIP:

To avoid installation errors, always remove the Intel(R) MPI Library packages using the uninstall script provided with the package before trying to install a new package or reinstall an older one.

- Due to an installer limitation, avoid installing earlier releases of the Intel(R) MPI Library packages after having already installed the current release. It may corrupt the installation of the current release and require that you uninstall/reinstall it.
- Certain operating system versions have a bug in the rpm command that prevents installations other than in the default install location. In this case, the installer does not offer the option to install in an alternate location.
- If the mpdboot command fails to start up the MPD, verify that the Intel(R) MPI Library package is installed in the same path/location on all the

nodes in the cluster. To solve this problem, uninstall and re-install the Intel(R) MPI Library package while using the same <installdir> path on all nodes in the cluster.

- If the mpdboot command fails to start up the MPD, verify that all cluster nodes have the same Python\* version installed. To avoid this issue, always install the same Python\* version on all cluster nodes.
- Presence of environment variables with non-printable characters in user environment settings may cause the process startup to fail. To work around this issue, the Intel(R) MPI Library does not propagate environment variables with non-printable characters across the MPD ring.
- A program cannot be executed when it resides in the current directory but "." is not in the PATH. To avoid this error, either add "." to the PATH on ALL nodes in the cluster or use the explicit path to the executable or ./<executable> in the mpiexec command line.
- You may get an error message at the end of a Checkpoint-Restart enabled application, if some of the application processes exit in the middle of taking a checkpoint image. Such error does not impact the application, thus you can ignore it. To avoid this error, set a larger number than your previous configured value for the <sec> argument of the -checkpoint-interval option. The following error message details are for your reference:
  [proxy:0:0@hostname] HYDT\_ckpoint\_blcr\_checkpoint (./tools/ckpoint/blcr/ckpoint\_blcr.c:313): cr\_poll\_checkpoint failed: No such process
  [proxy:0:0@hostname] ckpoint\_thread (./tools/ckpoint/ckpoint.c:559): blcr checkpoint returned error
  [proxy:0:0@hostname] HYDT\_ckpoint\_finalize (./tools/ckpoint/ckpoint.c:878): Error in checkpoint thread 0x7
- The Intel(R) MPI Library 2.0 and higher supports PMI wire protocol version 1.1. Note that this information is specified as

```
pmi_version = 1
pmi_subversion = 1
instead of
pmi_version = 1.1
as done by the Intel(R) MPI Library 1.0.
```

- The Intel(R) MPI Library requires the presence of the /dev/shm device in the system. To avoid failures related to the inability to create a shared memory segment, make sure the /dev/shm device is set up correctly.

- The Intel(R) MPI Library uses TCP sockets to pass stdin stream to the application. If you redirect a large file, for example, 5KB, the transfer could take a long time and cause things to hang on the remote side. To avoid this issue, pass large files to the application as command line options.
- (SDK only) Certain operating systems use GNU\* compilers version 4.2 or higher that is incompatible with Intel(R) Professional Edition Compiler 9.1. Use Intel(R) Professional Edition Compilers 11.1 or later on the respective operating systems, for example: o SuSE\* Linux Enterprise Server\* 11
- (SDK only) Certain GNU\* C compilers may generate code that leads to inadvertent merging of some output lines at runtime. This happens when different processes write simultaneously to the standard output and standard error streams. In order to avoid this, use the -fno-builtin-printf option of the respective GNU\* compiler while building your application.
- (SDK only) Certain versions of the GNU\* LIBC library define free()/realloc() symbols as non-weak. Use the --allow-multiple-definition GNU\* linker option to link your application.
- (SDK only) A known exception handling incompatibility exists between GNU C++ compilers version 3.x and version 4.x. Use the special -gcc-version=<nnn> option for the compiler drivers mpicxx and mpiicpc to link an application when running in a particular GNU\* C++ environment. The valid <nnn> values are:

```
o 320 if GNU* C++ version is 3.2.x
o 330 if GNU* C++ version is 3.3.x
o 340 if GNU* C++ version is 3.4.x
o 400 if GNU* C++ version is 4.0.x
o 410 if GNU* C++ version is 4.1.x
o 420 if GNU* C++ version is 4.2.x
o 430 if GNU* C++ version is 4.3.x
```

A library compatible with the detected version of the GNU\* C++ compiler is used by default. Do not use this option if the gcc version is older than 3.2.

- (SDK only) The Fortran 77 and Fortran 90 tests in the <installdir>/test directory may produce warnings when compiled with the mpif77, etc. compiler commands. You can safely ignore these warnings, or add the -w option to the compiler command line to suppress them.
- (SDK only) In order to use GNU Fortran compiler version 4.0 and higher use the mpif90 compiler driver.

- (SDK only) A known module file format incompatibility exists between the GNU Fortran 95 compilers. Use Intel(R) MPI Library mpif90 compiler driver to automatically uses the appropriate MPI module.
- (SDK only) Perform the following steps to generate bindings for your compiler that is not directly supported by the Intel(R) MPI Library:
  - 1. Go to the binding directory
    - # cd <installdir>/binding
  - 2. Extract the binding kit
    - # tar -zxvf intel-mpi-binding-kit.tar.gz
  - 3. Follow instructions in the README-intel-mpi-binding-kit.txt
- (SDK only) Use the following command to launch an Intel MPI application with Valgrind\* 3.3.0:

- Intel(R) MPI LIbrary doesn't support symbol ":" within filenames for file manipulation routines.
- Note: any routines in the libmpigi library (shipped with the Intel(R) MPI Library) are more highly optimized for Intel microprocessors than for non-Intel microprocessors.
- DAPL auto provider selection mechanism requires dapl-2.0.37 or higher for Intel(R) Xeon(R) and Intel(R) Manycore Platform Software Stack (Intel(R) MPSS) gold update 3 or higher for Intel (R) Xeon Phi(tm)
- Improved NUMA support requires dapl-2.0.37 or higher
- Intel(R) MPI Library supports only Intel(R) Xeon Phi(TM) Coprocessor (codename: Knights Corner) based on Intel(R) MIC Architecture.
- This release of the Intel(R) MPI Library 5.0 for Linux\* OS does not support certain features for Intel(R) Xeon Phi(TM) coprocessor: o MPD process manager

- Intel(R) MPI Library 5.0 for Linux\* OS supports multiple DAPL\* providers for communication between host and Intel(R) Xeon Phi(TM) coprocessor and between several Intel(R) Xeon Phi(TM) coprocessors inside one node. Currently supported providers are DAPL over InfiniBand\* Architecture and DAPL over Intel(R) Symmetric Communications Interface (Intel(R) SCI). This feature requires using symbolic names in the hostfile.
- DAPL auto provider selection mechanism requires dapl-2.0.37 or higher for Intel(R) Xeon(R) and Intel(R) Manycore Platform Software Stack (Intel(R) MPSS) gold update 3 or higher for Intel (R) Xeon Phi(tm)
- Coarray Fortran (CAF) with Intel(R) Fortran Compiler 14 is incompatible with Intel(R) MPI Library 5.0. If using CAF, ensure that either Intel(R) Fortran Compiler 15 or higher is used, or use a 4.x version of Intel(R) MPI Library.
- If you set I\_MPI\_SHM\_LMT=direct, the setting has no effect on Intel(R) MPI Library for Linux\* OS if the Linux\* kernel version is lower than 3.2.
- The direct copy LMT does not support messages larger than 2<sup>31</sup> 4K bytes.
   If you have messages larger than this limit, select a different LMT mechanism.
- IMB-MPI1 pingpong test shows poor performance between host and Intel(R) Xeon Phi(TM) Coprocessor in off cache mode (-off\_cache -1) on message range from 4 to 256 kilobytes. Try I\_MPI\_MIC\_DAPL\_DIRECT\_COPY\_THRESHOLD=131072 for optimization in case of unexpected slow performance for your application.
- ILP64 is not supported by MPI modules for Fortran\* 2008.

Technical Support

Your feedback is very important to us. To receive technical support for the tools provided in this product and technical information including FAQ's and product updates, you need to register for an Intel(R) Premier Support account at the Registration Center.

This package is supported by Intel(R) Premier Support. Direct customer support requests at:

https://premier.intel.com

General information on Intel(R) Software Development Products support offerings may be obtained at:

http://www.intel.com/software/products/support

The Intel(R) MPI Library home page can be found at: http://www.intel.com/go/mpi

The Intel(R) MPI Library support web site, http://software.intel.com/en-us/articles/intel-mpi-library-for-linux-kb/all/provides the latest top technical issues, frequently asked questions, product documentation, and product errata.

Requests for licenses can be directed to the Registration Center at: http://www.intel.com/software/products/registrationcenter

Before submitting a support issue, see the Intel(R) MPI Library for Linux\* OS User's Guide for details on post-install testing to ensure that basic facilities are working.

When submitting a support issue to Intel(R) Premier Support, please provide specific details of your problem, including:

- The Intel(R) MPI Library package name and version information
- Host architecture (for example, Intel(R) 64 architecture)
- Compiler(s) and versions
- Operating system(s) and versions
- Specifics on how to reproduce the problem. Include makefiles, command lines, small test cases, and build instructions.
   Use <installdir>/test sources as test cases, when possible.

You can obtain version information for the Intel(R) MPI Library package in the file mpisupport.txt.

#### Submitting Issues

-----

- Go to https://premier.intel.com
- Log in to the site. Note that your username and password are case-sensitive.
- Click on the "Submit Issue" button in the top right.
- Search for and select "Intel(R) MPI Library, Linux\*" from the product list, then click "Next".
- Complete the fields describing your issue and attach any supporting files.
- Review the issue and complete submission.

Note: Notify your support representative prior to submitting source code where access needs to be restricted to certain countries to determine if this request can be accommodated.

Copyright and Licenses

The Intel(R) MPI Library is based on MPICH2\* from Argonne National Laboratory\* (ANL) and MVAPICH2\* from Ohio State University\* (OSU).

See the information below for additional licenses of the following third party tools used within the Intel(R) MPI Library:
Silicon Graphics Inc.\* STL, libc, gdf, BOOST\*, my\_getopt, Python\*, AVL Trees\*, Direct Access Programming Library, InfiniBand Verbs Library, and libnuma\*.

```
Silicon Graphics, Inc.* Standard Template Library
_____
* Copyright (c) 1996,1997
 * Silicon Graphics Computer Systems, Inc.
* Permission to use, copy, modify, distribute and sell this software
* and its documentation for any purpose is hereby granted without fee,
* provided that the above copyright notice appear in all copies and
* that both that copyright notice and this permission notice appear
* in supporting documentation. Silicon Graphics makes no
* representations about the suitability of this software for any
 * purpose. It is provided "as is" without express or implied warranty.
*/
libc
_ _ _ _ _
 * Copyright (c) 1988 Regents of the University of California.
 * All rights reserved.
* Redistribution and use in source and binary forms, with or without
* modification, are permitted provided that the following conditions
* are met:
 * 1. Redistributions of source code must retain the above copyright
      notice, this list of conditions and the following disclaimer.
 * 2. Redistributions in binary form must reproduce the above copyright
      notice, this list of conditions and the following disclaimer in the
      documentation and/or other materials provided with the distribution.
 * 3. Neither the name of the University nor the names of its contributors
     may be used to endorse or promote products derived from this software
     without specific prior written permission.
 * THIS SOFTWARE IS PROVIDED BY THE REGENTS AND CONTRIBUTORS ``AS IS'' AND
* ANY EXPRESS OR IMPLIED WARRANTIES, INCLUDING, BUT NOT LIMITED TO, THE
* IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE
* ARE DISCLAIMED. IN NO EVENT SHALL THE REGENTS OR CONTRIBUTORS BE LIABLE
* FOR ANY DIRECT, INDIRECT, INCIDENTAL, SPECIAL, EXEMPLARY, OR CONSEQUENTIAL
* DAMAGES (INCLUDING, BUT NOT LIMITED TO, PROCUREMENT OF SUBSTITUTE GOODS
```

\* OR SERVICES; LOSS OF USE, DATA, OR PROFITS; OR BUSINESS INTERRUPTION)

\* HOWEVER CAUSED AND ON ANY THEORY OF LIABILITY, WHETHER IN CONTRACT, STRICT

\* LIABILITY, OR TORT (INCLUDING NEGLIGENCE OR OTHERWISE) ARISING IN ANY WAY

\* OUT OF THE USE OF THIS SOFTWARE, EVEN IF ADVISED OF THE POSSIBILITY OF

```
* SUCH DAMAGE.
 */
gdf
/**
 * This is copy of the code which implements the GFD(32) hashing of datatypes
 * described in this paper:
 * Julien Langou, George Bosilca, Graham Fagg and Jack Dongarra (2005).
 * Hash functions for MPI datatypes.
 * In the Proceedings of the 12th European PVM/MPI Users' Group Meeting, Sorrento,
 * Italy, September 2005.
 * Springer's Lecture Notes in Computer Science, LCNS-3666:76-83, 2005.
 * related software.
 * http://www.cs.utk.edu/~library/TechReports/2005/ut-cs-05-552.pdf
 * http://www.cs.utk.edu/~langou/articles/LBFD:05/2005-LBFD.html
 * The code is used with permission of the author and was released under the
 * "Modified BSD" license (no need to mention in advertising material). Here's
 * a copy of the complete COPYING file that came with the source:
Copyright (c) 1992-2006 The University of Tennessee. All rights reserved.
$COPYRIGHT$
Additional copyrights may follow
$HEADER$
Redistribution and use in source and binary forms, with or without
modification, are permitted provided that the following conditions are
met:
- Redistributions of source code must retain the above copyright
  notice, this list of conditions and the following disclaimer.
- Redistributions in binary form must reproduce the above copyright
  notice, this list of conditions and the following disclaimer listed
  in this license in the documentation and/or other materials
  provided with the distribution.
- Neither the name of the copyright holders nor the names of its
  contributors may be used to endorse or promote products derived from
  this software without specific prior written permission.
THIS SOFTWARE IS PROVIDED BY THE COPYRIGHT HOLDERS AND CONTRIBUTORS
"AS IS" AND ANY EXPRESS OR IMPLIED WARRANTIES, INCLUDING, BUT NOT
LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR
A PARTICULAR PURPOSE ARE DISCLAIMED. IN NO EVENT SHALL THE COPYRIGHT
```

OWNER OR CONTRIBUTORS BE LIABLE FOR ANY DIRECT, INDIRECT, INCIDENTAL, SPECIAL, EXEMPLARY, OR CONSEQUENTIAL DAMAGES (INCLUDING, BUT NOT LIMITED TO, PROCUREMENT OF SUBSTITUTE GOODS OR SERVICES; LOSS OF USE, DATA, OR PROFITS; OR BUSINESS INTERRUPTION) HOWEVER CAUSED AND ON ANY THEORY OF LIABILITY, WHETHER IN CONTRACT, STRICT LIABILITY, OR TORT (INCLUDING NEGLIGENCE OR OTHERWISE) ARISING IN ANY WAY OUT OF THE USE

OF THIS SOFTWARE, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGE.

#### BOOST\*

-----

Boost Software License - Version 1.0 - August 17th, 2003
Permission is hereby granted, free of charge, to any person or organization obtaining a copy of the software and accompanying documentation covered by this license (the "Software") to use, reproduce, display, distribute, execute, and transmit the Software, and to prepare derivative works of the Software, and to permit third-parties to whom the Software is furnished to do so, all subject to the following:

The copyright notices in the Software and this entire statement, including the above license grant, this restriction and the following disclaimer, must be included in all copies of the Software, in whole or in part, and all derivative works of the Software, unless such copies or derivative works are solely in the form of machine-executable object code generated by a source language processor.

THE SOFTWARE IS PROVIDED "AS IS", WITHOUT WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, INCLUDING BUT NOT LIMITED TO THE WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, TITLE AND NON-INFRINGEMENT. IN NO EVENT SHALL THE COPYRIGHT HOLDERS OR ANYONE DISTRIBUTING THE SOFTWARE BE LIABLE FOR ANY DAMAGES OR OTHER LIABILITY, WHETHER IN CONTRACT, TORT OR OTHERWISE, ARISING FROM, OUT OF OR IN CONNECTION WITH THE SOFTWARE OR THE USE OR OTHER DEALINGS IN THE SOFTWARE.

# my\_getopt

,\_0 .

my\_getopt - a command-line argument parser Copyright 1997-2001, Benjamin Sittler Permission is hereby granted, free of charge, to any person obtaining a copy of this software and associated documentation files (the "Software"), to deal in the Software without restriction, including without limitation the rights to use, copy, modify, merge, publish, distribute, sublicense, and/or sell copies of the Software, and to permit persons to whom the Software is furnished to do so, subject to the following conditions: The above copyright notice and this permission notice shall be included in all copies or substantial portions of the Software. THE SOFTWARE IS PROVIDED "AS IS", WITHOUT WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, INCLUDING BUT NOT LIMITED TO THE WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE AND NONINFRINGEMENT. IN NO EVENT SHALL THE AUTHORS OR COPYRIGHT HOLDERS BE LIABLE FOR ANY CLAIM, DAMAGES OR OTHER LIABILITY, WHETHER IN AN ACTION OF CONTRACT, TORT OR OTHERWISE, ARISING FROM, OUT OF OR IN CONNECTION WITH THE SOFTWARE OR THE USE OR OTHER DEALINGS IN THE SOFTWARE.

Python\*

-----

#### PSF LICENSE AGREEMENT FOR PYTHON 2.3

-----

- 1. This LICENSE AGREEMENT is between the Python Software Foundation ("PSF"), and the Individual or Organization ("Licensee") accessing and otherwise using Python 2.3 software in source or binary form and its associated documentation.
- 2. Subject to the terms and conditions of this License Agreement, PSF hereby grants Licensee a nonexclusive, royalty-free, world-wide license to reproduce, analyze, test, perform and/or display publicly, prepare derivative works, distribute, and otherwise use Python 2.3 alone or in any derivative version, provided, however, that PSF's License Agreement and PSF's notice of copyright, for example, "Copyright (c) 2001, 2002, 2003, 2004 Python Software Foundation; All Rights Reserved" are retained in Python 2.3 alone or in any derivative version prepared by Licensee.
- 3. In the event Licensee prepares a derivative work that is based on or incorporates Python 2.3 or any part thereof, and wants to make the derivative work available to others as provided herein, then Licensee hereby agrees to include in any such work a brief summary of the changes made to Python 2.3.
- 4. PSF is making Python 2.3 available to Licensee on an "AS IS" basis. PSF MAKES NO REPRESENTATIONS OR WARRANTIES, EXPRESS OR IMPLIED. BY WAY OF EXAMPLE, BUT NOT LIMITATION, PSF MAKES NO AND DISCLAIMS ANY REPRESENTATION OR WARRANTY OF MERCHANTABILITY OR FITNESS FOR ANY PARTICULAR PURPOSE OR THAT THE USE OF PYTHON 2.3 WILL NOT INFRINGE ANY THIRD PARTY RIGHTS.
- 5. PSF SHALL NOT BE LIABLE TO LICENSEE OR ANY OTHER USERS OF PYTHON 2.3 FOR ANY INCIDENTAL, SPECIAL, OR CONSEQUENTIAL DAMAGES OR LOSS AS A RESULT OF MODIFYING, DISTRIBUTING, OR OTHERWISE USING PYTHON 2.3, OR ANY DERIVATIVE THEREOF, EVEN IF ADVISED OF THE POSSIBILITY THEREOF. 6. This License Agreement will automatically terminate upon a material
- 6. This License Agreement will automatically terminate upon a material breach of its terms and conditions.
- 7. Nothing in this License Agreement shall be deemed to create any relationship of agency, partnership, or joint venture between PSF and Licensee. This License Agreement does not grant permission to use PSF trademarks or trade name in a trademark sense to endorse or promote products or services of Licensee, or any third party.
- 8. By copying, installing or otherwise using Python 2.3, Licensee agrees to be bound by the terms and conditions of this License Agreement.

# AVL Trees\*

-----

Copyright (c) 1989-1997 by Brad Appleton, All rights reserved. This software is not subject to any license of the American Telephone and Telegraph Company or of the Regents of the University of California.

Permission is granted to anyone to use this software for any purpose on any computer system, and to alter it and redistribute it freely, subject to the following restrictions:

- Neither the authors of the software nor their employers (including any of the employers' subsidiaries and subdivisions) are responsible for maintaining & supporting this software or for any consequences resulting from the use of this software, no matter how awful, even if they arise from flaws in the software.
- 2. The origin of this software must not be misrepresented, either by explicit claim or by omission. Since few users ever read sources, credits must appear in the documentation.
- 3. Altered versions must be plainly marked as such, and must not be misrepresented as being the original software. Since few users ever read sources, credits must appear in the documentation.
- 4. This notice may not be removed or altered.

The Intel MPI library includes altered AVL Trees\* source codes.

Direct Access Programming Library (DAPL)

-----

This software is used under the Berkeley Software Distribution\* (BSD\*) License.

Copyright (c) 2002-2005, Network Appliance, Inc. All rights reserved.

Copyright (c) 2005 Voltaire Inc. All rights reserved.

Copyright (c) 2005-2010 Intel Corporation. All rights reserved.

Copyright (c) 2004-2005, Mellanox Technologies, Inc. All rights reserved.

Copyright (c) 2003 Topspin Corporation. All rights reserved.

Copyright (c) 2005 Sun Microsystems, Inc. All rights reserved.

This Software is licensed under one of the following licenses:

- 1) under the terms of the "Common Public License 1.0" a copy of which is in the file LICENSE.txt in the root directory.
- 2) under the terms of the "The BSD License" a copy of which is in the file LICENSE2.txt in the root directory.
- 3) under the terms of the "GNU General Public License (GPL) Version 2" a copy of which is in the file LICENSE3.txt in the root directory.

Licensee has the right to choose one of the above licenses.

Redistributions of source code must retain the above copyright notice and one of the license notices.

Redistributions in binary form must reproduce both the above copyright notice,

one of the license notices in the documentation and/or other materials provided with the distribution.

Copyright (c) 2002-2005, Network Appliance, Inc. All rights reserved.

Redistribution and use in source and binary forms, with or without modification, are permitted provided that the following conditions are met:

Redistributions of source code must retain the above copyright notice, this list of conditions and the following disclaimer.

Redistributions in binary form must reproduce the above copyright notice, this list of conditions and the following disclaimer in the documentation and/or other materials provided with the distribution.

Neither the name of the Network Appliance, Inc. nor the names of its contributors may be used to endorse or promote products derived from this software without specific prior written permission.

THIS SOFTWARE IS PROVIDED BY THE COPYRIGHT HOLDERS AND CONTRIBUTORS "AS IS" AND ANY EXPRESS OR IMPLIED WARRANTIES, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE ARE DISCLAIMED. IN NO EVENT SHALL THE COPYRIGHT OWNER OR CONTRIBUTORS BE LIABLE FOR ANY DIRECT, INDIRECT, INCIDENTAL, SPECIAL, EXEMPLARY, OR CONSEQUENTIAL DAMAGES (INCLUDING, BUT NOT LIMITED TO, PROCUREMENT OF SUBSTITUTE GOODS OR SERVICES; LOSS OF USE, DATA, OR PROFITS; OR BUSINESS INTERRUPTION) HOWEVER CAUSED AND ON ANY THEORY OF LIABILITY, WHETHER IN CONTRACT, STRICT LIABILITY, OR TORT (INCLUDING NEGLIGENCE OR OTHERWISE) ARISING IN ANY WAY OUT OF THE USE OF THIS SOFTWARE, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGE.

# InfiniBand Verbs Library

\_\_\_\_\_

This software is used under the Berkeley Software Distribution\* (BSD\*) License.

This software is available to you under a choice of one of two licenses. You may choose to be licensed under the terms of the the OpenIB.org BSD license or the GNU General Public License (GPL) Version 2, both included below.

Copyright (c) 2004 Topspin Communications. All rights reserved.

#### Cloudera\* CDH

-----

All software developed by Cloudera for CDH is released with an Apache 2.0

license. Please let us know if you find any file that doesn't explicitly state the Apache license at the top and we'll immediately fix it.

Apache License Version 2.0, January 2004 http://www.apache.org/licenses/

Copyright 2010-2013 Cloudera

Licensed under the Apache License, Version 2.0 (the "License"); you may not use this file except in compliance with the License. You may obtain a copy of the License at:

http://www.apache.org/licenses/LICENSE-2.0

Unless required by applicable law or agreed to in writing, software distributed under the License is distributed on an "AS IS" BASIS, WITHOUT WARRANTIES OR CONDITIONS OF ANY KIND, either express or implied. See the License for the specific language governing permissions and limitations under the License.

Apache Thrift\*

Apache License
Version 2.0, January 2004
http://www.apache.org/licenses/

TERMS AND CONDITIONS FOR USE, REPRODUCTION, AND DISTRIBUTION

#### Definitions.

"License" shall mean the terms and conditions for use, reproduction, and distribution as defined by Sections 1 through 9 of this document.

"Licensor" shall mean the copyright owner or entity authorized by the copyright owner that is granting the License.

"Legal Entity" shall mean the union of the acting entity and all other entities that control, are controlled by, or are under common control with that entity. For the purposes of this definition, "control" means (i) the power, direct or indirect, to cause the direction or management of such entity, whether by contract or otherwise, or (ii) ownership of fifty percent (50%) or more of the outstanding shares, or (iii) beneficial ownership of such entity.

"You" (or "Your") shall mean an individual or Legal Entity exercising permissions granted by this License.

"Source" form shall mean the preferred form for making modifications, including but not limited to software source code, documentation source, and configuration files.

"Object" form shall mean any form resulting from mechanical transformation or translation of a Source form, including but not limited to compiled object code, generated documentation, and conversions to other media types.

"Work" shall mean the work of authorship, whether in Source or Object form, made available under the License, as indicated by a copyright notice that is included in or attached to the work (an example is provided in the Appendix below).

"Derivative Works" shall mean any work, whether in Source or Object form, that is based on (or derived from) the Work and for which the editorial revisions, annotations, elaborations, or other modifications represent, as a whole, an original work of authorship. For the purposes of this License, Derivative Works shall not include works that remain separable from, or merely link (or bind by name) to the interfaces of, the Work and Derivative Works thereof.

"Contribution" shall mean any work of authorship, including the original version of the Work and any modifications or additions to that Work or Derivative Works thereof, that is intentionally submitted to Licensor for inclusion in the Work by the copyright owner or by an individual or Legal Entity authorized to submit on behalf of the copyright owner. For the purposes of this definition, "submitted" means any form of electronic, verbal, or written communication sent to the Licensor or its representatives, including but not limited to communication on electronic mailing lists, source code control systems, and issue tracking systems that are managed by, or on behalf of, the Licensor for the purpose of discussing and improving the Work, but excluding communication that is conspicuously marked or otherwise designated in writing by the copyright owner as "Not a Contribution."

"Contributor" shall mean Licensor and any individual or Legal Entity on behalf of whom a Contribution has been received by Licensor and subsequently incorporated within the Work.

- 2. Grant of Copyright License. Subject to the terms and conditions of this License, each Contributor hereby grants to You a perpetual, worldwide, non-exclusive, no-charge, royalty-free, irrevocable copyright license to reproduce, prepare Derivative Works of, publicly display, publicly perform, sublicense, and distribute the Work and such Derivative Works in Source or Object form.
- 3. Grant of Patent License. Subject to the terms and conditions of

this License, each Contributor hereby grants to You a perpetual, worldwide, non-exclusive, no-charge, royalty-free, irrevocable (except as stated in this section) patent license to make, have made, use, offer to sell, sell, import, and otherwise transfer the Work, where such license applies only to those patent claims licensable by such Contributor that are necessarily infringed by their Contribution(s) alone or by combination of their Contribution(s) with the Work to which such Contribution(s) was submitted. If You institute patent litigation against any entity (including a cross-claim or counterclaim in a lawsuit) alleging that the Work or a Contribution incorporated within the Work constitutes direct or contributory patent infringement, then any patent licenses granted to You under this License for that Work shall terminate as of the date such litigation is filed.

- 4. Redistribution. You may reproduce and distribute copies of the Work or Derivative Works thereof in any medium, with or without modifications, and in Source or Object form, provided that You meet the following conditions:
  - (a) You must give any other recipients of the Work or Derivative Works a copy of this License; and
  - (b) You must cause any modified files to carry prominent notices stating that You changed the files; and
  - (c) You must retain, in the Source form of any Derivative Works that You distribute, all copyright, patent, trademark, and attribution notices from the Source form of the Work, excluding those notices that do not pertain to any part of the Derivative Works; and
  - (d) If the Work includes a "NOTICE" text file as part of its distribution, then any Derivative Works that You distribute must include a readable copy of the attribution notices contained within such NOTICE file, excluding those notices that do not pertain to any part of the Derivative Works, in at least one of the following places: within a NOTICE text file distributed as part of the Derivative Works; within the Source form or documentation, if provided along with the Derivative Works; or, within a display generated by the Derivative Works, if and wherever such third-party notices normally appear. The contents of the NOTICE file are for informational purposes only and do not modify the License. You may add Your own attribution notices within Derivative Works that You distribute, alongside or as an addendum to the NOTICE text from the Work, provided that such additional attribution notices cannot be construed as modifying the License.

You may add Your own copyright statement to Your modifications and may provide additional or different license terms and conditions for use, reproduction, or distribution of Your modifications, or for any such Derivative Works as a whole, provided Your use, reproduction, and distribution of the Work otherwise complies with the conditions stated in this License.

- 5. Submission of Contributions. Unless You explicitly state otherwise, any Contribution intentionally submitted for inclusion in the Work by You to the Licensor shall be under the terms and conditions of this License, without any additional terms or conditions. Notwithstanding the above, nothing herein shall supersede or modify the terms of any separate license agreement you may have executed with Licensor regarding such Contributions.
- 6. Trademarks. This License does not grant permission to use the trade names, trademarks, service marks, or product names of the Licensor, except as required for reasonable and customary use in describing the origin of the Work and reproducing the content of the NOTICE file.
- 7. Disclaimer of Warranty. Unless required by applicable law or agreed to in writing, Licensor provides the Work (and each Contributor provides its Contributions) on an "AS IS" BASIS, WITHOUT WARRANTIES OR CONDITIONS OF ANY KIND, either express or implied, including, without limitation, any warranties or conditions of TITLE, NON-INFRINGEMENT, MERCHANTABILITY, or FITNESS FOR A PARTICULAR PURPOSE. You are solely responsible for determining the appropriateness of using or redistributing the Work and assume any risks associated with Your exercise of permissions under this License.
- 8. Limitation of Liability. In no event and under no legal theory, whether in tort (including negligence), contract, or otherwise, unless required by applicable law (such as deliberate and grossly negligent acts) or agreed to in writing, shall any Contributor be liable to You for damages, including any direct, indirect, special, incidental, or consequential damages of any character arising as a result of this License or out of the use or inability to use the Work (including but not limited to damages for loss of goodwill, work stoppage, computer failure or malfunction, or any and all other commercial damages or losses), even if such Contributor has been advised of the possibility of such damages.
- 9. Accepting Warranty or Additional Liability. While redistributing the Work or Derivative Works thereof, You may choose to offer, and charge a fee for, acceptance of support, warranty, indemnity, or other liability obligations and/or rights consistent with this License. However, in accepting such obligations, You may act only

on Your own behalf and on Your sole responsibility, not on behalf of any other Contributor, and only if You agree to indemnify, defend, and hold each Contributor harmless for any liability incurred by, or claims asserted against, such Contributor by reason of your accepting any such warranty or additional liability.

END OF TERMS AND CONDITIONS

APPENDIX: How to apply the Apache License to your work.

To apply the Apache License to your work, attach the following boilerplate notice, with the fields enclosed by brackets "[]" replaced with your own identifying information. (Don't include the brackets!) The text should be enclosed in the appropriate comment syntax for the file format. We also recommend that a file or class name and description of purpose be included on the same "printed page" as the copyright notice for easier identification within third-party archives.

Copyright [yyyy] [name of copyright owner]

Licensed under the Apache License, Version 2.0 (the "License"); you may not use this file except in compliance with the License. You may obtain a copy of the License at

http://www.apache.org/licenses/LICENSE-2.0

Unless required by applicable law or agreed to in writing, software distributed under the License is distributed on an "AS IS" BASIS, WITHOUT WARRANTIES OR CONDITIONS OF ANY KIND, either express or implied. See the License for the specific language governing permissions and limitations under the License.

.----

#### SOFTWARE DISTRIBUTED WITH THRIFT:

The Apache Thrift software includes a number of subcomponents with separate copyright notices and license terms. Your use of the source code for the these subcomponents is subject to the terms and conditions of the following licenses.

-----

Portions of the following files are licensed under the MIT License:

lib/erl/src/Makefile.am

Please see doc/otp-base-license.txt for the full terms of this license.

For the aclocal/ax\_boost\_base.m4 and contrib/fb303/aclocal/ax\_boost\_base.m4 components:

# Copyright (c) 2007 Thomas Porschberg <thomas@randspringer.de>

#

# Copying and distribution of this file, with or without

# modification, are permitted in any medium without royalty provided

# the copyright notice and this notice are preserved.

For the compiler/cpp/src/thrift/md5.[ch] components:

/\*

Copyright (C) 1999, 2000, 2002 Aladdin Enterprises. All rights reserved.

This software is provided 'as-is', without any express or implied warranty. In no event will the authors be held liable for any damages arising from the use of this software.

Permission is granted to anyone to use this software for any purpose, including commercial applications, and to alter it and redistribute it freely, subject to the following restrictions:

- 1. The origin of this software must not be misrepresented; you must not claim that you wrote the original software. If you use this software in a product, an acknowledgment in the product documentation would be appreciated but is not required.
- 2. Altered source versions must be plainly marked as such, and must not be misrepresented as being the original software.
- 3. This notice may not be removed or altered from any source distribution.
- L. Peter Deutsch
  ghost@aladdin.com

\*/

-----

For the lib/rb/setup.rb: Copyright (c) 2000-2005 Minero Aoki, lib/ocaml/OCamlMakefile and lib/ocaml/README-OCamlMakefile components: Copyright (C) 1999 - 2007 Markus Mottl

Licensed under the terms of the GNU Lesser General Public License 2.1 (see doc/lgpl-2.1.txt for the full terms of this license)

\_\_\_\_\_\_

OpenIB.org BSD license

Redistribution and use in source and binary forms, with or without modification, are permitted provided that the following conditions are met:

- \* Redistributions of source code must retain the above copyright notice, this list of conditions and the following disclaimer.
- \* Redistributions in binary form must reproduce the above copyright notice, this list of conditions and the following disclaimer in the documentation and/or other materials provided with the distribution.

THIS SOFTWARE IS PROVIDED BY THE COPYRIGHT HOLDERS AND CONTRIBUTORS "AS IS" AND ANY EXPRESS OR IMPLIED WARRANTIES, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE ARE DISCLAIMED. IN NO EVENT SHALL THE COPYRIGHT OWNER OR CONTRIBUTORS BE LIABLE FOR ANY DIRECT, INDIRECT, INCIDENTAL, SPECIAL, EXEMPLARY, OR CONSEQUENTIAL DAMAGES (INCLUDING, BUT NOT LIMITED TO, PROCUREMENT OF SUBSTITUTE GOODS OR SERVICES; LOSS OF USE, DATA, OR PROFITS; OR BUSINESS INTERRUPTION) HOWEVER CAUSED AND ON ANY THEORY OF LIABILITY, WHETHER IN CONTRACT, STRICT LIABILITY, OR TORT (INCLUDING NEGLIGENCE OR OTHERWISE) ARISING IN ANY WAY OUT OF THE USE OF THIS SOFTWARE, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGE.

libnuma*
libnuma is under the GNU Lesser General Public License, v2.1.
Libfabric* and OpenFabrics Interfaces* (OFI*)
Converight (c) 2015 Intel Componetion All nights reconved
Copyright (c) 2015 Intel Corporation. All rights reserved. Copyright (c) 2015 Cisco Systems, Inc. All rights reserved.

BSD license

Redistribution and use in source and binary forms, with or without modification, are permitted provided that the following conditions are met:

\* Redistributions of source code must retain the above copyright notice, this list of conditions and the following disclaimer.

\* Redistributions in binary form must reproduce the above copyright notice, this list of conditions and the following disclaimer in the documentation and/or other materials provided with the distribution.

THIS SOFTWARE IS PROVIDED BY THE COPYRIGHT HOLDERS AND CONTRIBUTORS "AS IS" AND ANY EXPRESS OR IMPLIED WARRANTIES, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE ARE DISCLAIMED. IN NO EVENT SHALL THE COPYRIGHT OWNER OR CONTRIBUTORS BE LIABLE FOR ANY DIRECT, INDIRECT, INCIDENTAL, SPECIAL, EXEMPLARY, OR CONSEQUENTIAL DAMAGES (INCLUDING, BUT NOT LIMITED TO, PROCUREMENT OF SUBSTITUTE GOODS OR SERVICES; LOSS OF USE, DATA, OR PROFITS; OR BUSINESS INTERRUPTION) HOWEVER CAUSED AND ON ANY THEORY OF LIABILITY, WHETHER IN CONTRACT, STRICT LIABILITY, OR TORT (INCLUDING NEGLIGENCE OR OTHERWISE) ARISING IN ANY WAY OUT OF THE USE OF THIS SOFTWARE, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGE.

------

Disclaimer and Legal Information

INFORMATION IN THIS DOCUMENT IS PROVIDED IN CONNECTION WITH INTEL PRODUCTS. NO LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE, TO ANY INTELLECTUAL PROPERTY RIGHTS IS GRANTED BY THIS DOCUMENT. EXCEPT AS PROVIDED IN INTEL'S TERMS AND CONDITIONS OF SALE FOR SUCH PRODUCTS, INTEL ASSUMES NO LIABILITY WHATSOEVER AND INTEL DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY, RELATING TO SALE AND/OR USE OF INTEL PRODUCTS INCLUDING LIABILITY OR WARRANTIES RELATING TO FITNESS FOR A PARTICULAR PURPOSE, MERCHANTABILITY, OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

A "Mission Critical Application" is any application in which failure of the Intel Product could result, directly or indirectly, in personal injury or death. SHOULD YOU PURCHASE OR USE INTEL'S PRODUCTS FOR ANY SUCH MISSION CRITICAL APPLICATION, YOU SHALL INDEMNIFY AND HOLD INTEL AND ITS SUBSIDIARIES, SUBCONTRACTORS AND AFFILIATES, AND THE DIRECTORS, OFFICERS, AND EMPLOYEES OF EACH, HARMLESS AGAINST ALL CLAIMS COSTS, DAMAGES, AND EXPENSES AND REASONABLE ATTORNEYS' FEES ARISING OUT OF, DIRECTLY OR INDIRECTLY, ANY CLAIM OF PRODUCT LIABILITY, PERSONAL INJURY, OR DEATH ARISING IN ANY WAY OUT OF SUCH MISSION CRITICAL APPLICATION, WHETHER OR NOT INTEL OR ITS SUBCONTRACTOR WAS NEGLIGENT IN THE DESIGN, MANUFACTURE, OR WARNING OF THE INTEL PRODUCT OR ANY OF ITS PARTS. Intel may make changes to specifications and product descriptions at any time, without notice. Designers must not rely on the absence or characteristics of any features or instructions marked "reserved" or "undefined". Intel reserves these for future definition and shall have no responsibility whatsoever for conflicts or incompatibilities arising from future changes to them. The information here is subject to change without notice. Do not finalize a design with this information.

The products described in this document may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request. Contact your local Intel sales office or your distributor to obtain the latest specifications and before placing your product order. Copies of documents which have an order number and are referenced in this document, or other Intel literature, may be obtained by calling 1-800-548-4725, or go to: http://www.intel.com/design/literature.htm

MPEG-1, MPEG-2, MPEG-4, H.261, H.263, H.264, MP3, DV, VC-1, MJPEG, AC3, AAC, G.711, G.722, G.722.1, G.722.2, AMRWB, Extended AMRWB (AMRWB+), G.167, G.168, G.169, G.723.1, G.726, G.728, G.729, G.729.1, GSM AMR, GSM FR are international standards promoted by ISO, IEC, ITU, ETSI, 3GPP and other organizations. Implementations of these standards, or the standard enabled platforms may require licenses from various entities, including Intel Corporation.

BlueMoon, BunnyPeople, Celeron, Celeron Inside, Centrino, Centrino Inside, Cilk, Core Inside, E-GOLD, Flexpipe, i960, Intel, the Intel logo, Intel AppUp, Intel Atom, Intel Atom Inside, Intel Core, Intel Inside, Intel Insider, the Intel Inside logo, Intel NetBurst, Intel NetMerge, Intel NetStructure, Intel SingleDriver, Intel SpeedStep, Intel Sponsors of Tomorrow., the Intel Sponsors of Tomorrow. logo, Intel StrataFlash, Intel vPro, Intel XScale, InTru, the InTru logo, the InTru Inside logo, InTru soundmark, Itanium, Itanium Inside, MCS, MMX, Moblin, Pentium, Pentium Inside, Puma, skoool, the skoool logo, SMARTi, Sound Mark, Stay With It, The Creators Project, The Journey Inside, Thunderbolt, Ultrabook, vPro Inside, VTune, Xeon, Xeon Inside, X-GOLD, XMM, X-PMU and XPOSYS are trademarks of Intel Corporation in the U.S. and/or other countries.

\* Other names and brands may be claimed as the property of others.

Microsoft, Windows, and the Windows logo are trademarks, or registered trademarks of Microsoft Corporation in the United States and/or other countries.

Java is a registered trademark of Oracle and/or its affiliates.

No computer software can provide absolute security. End users are responsible for securing their own deployment of computer software in any environment.

Copyright (C) 2003-2016, Intel Corporation. All rights reserved.

# Optimization Notice

-----

Intel's compilers may or may not optimize to the same degree for non-Intel microprocessors for optimizations that are not unique to Intel microprocessors. These optimizations include SSE2, SSE3, and SSSE3 instruction sets and other optimizations. Intel does not guarantee the availability, functionality, or

effectiveness of any optimization on microprocessors not manufactured by Intel. Microprocessor-dependent optimizations in this product are intended for use with Intel microprocessors. Certain optimizations not specific to Intel microarchitecture are reserved for Intel microprocessors. Please refer to the applicable product User and Reference Guides for more information regarding the specific instruction sets covered by this notice.

Notice revision #20110804