With each model year, your automotive applications are expected to deliver increasingly sophisticated and integrated functionality. From navigation to driver assistance, networking to infotainment, automotive systems call for high-quality, highly reliable underlying technology, as well as design flexibility to meet emerging needs. For more and more automotive electronics designers, that underlying technology is programmable logic.

In response to rapid changes in consumer electronics as well as government mandates, turn to programmable logic devices (PLDs) to get your unique products out the door faster. When you need to prototype and demonstrate your designs to OEMs, count on PLDs to help you meet your timeline—and even let you make last-minute modifications without changing your board. Without the design respins common in ASIC development, PLDs can also present a cost and time advantage. What’s more, PLDs enable a single “platform” design which can easily be modified to suit the needs of different vehicle classes, from mass market to luxury models.

Altera is known and trusted for offering the market’s broadest range of high-quality, automotive-grade PLDs and structured ASICs. Our commitment to the automotive industry is demonstrated not only by our reliable devices but also the reference designs, intellectual property (IP), software tools, and development kits that help you get off the ground fast.

“...The Cyclone® II FPGA and the Nios® II embedded processor offer a high level of functionality at a tremendous value, enabling us to implement advanced signal processing functions more efficiently than with alternative components….the benefits of Altera’s solution played a key role in our delivery of the industry’s first off-the-shelf automotive camera with features previously available only in luxury model vehicles.”

Hitoshi Hongo, Manager, Automotive Company Advanced Technology Center, SANYO Electric Co., Ltd.

### Altera® automotive product families

<table>
<thead>
<tr>
<th>Device category</th>
<th>Product</th>
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<tbody>
<tr>
<td>CPLD</td>
<td>MAX® series devices*</td>
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<tr>
<td>FPGA</td>
<td>Cyclone series devices*</td>
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<tr>
<td>Structured ASICs</td>
<td>HardCopy® structured ASICs*</td>
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*Selected device members in each family are available in automotive grade

Our automotive-grade devices support a junction temperature range of -40°C to +125°C (MAX 7000 AE devices support up to +130°C). Contact your local Altera sales representative for a list of automotive-grade ordering codes.
Design flexibility and long product life cycles

PLDs provide a high level of design flexibility—critical in the automotive industry and not possible with ASICs or ASSPs. The high NRE costs and long lead times typical of ASIC design don’t encourage flexibility. With their fixed functionality, ASSPs inhibit the product differentiation that is so critical in an automotive market increasingly influenced by consumer applications.

Programmable logic is a different story. You can prototype with Cyclone FPGAs or MAX CPLDs, or go into production with Cyclone or MAX devices. When your product is ready for high-volume production or in a mature market, you can migrate your design to HardCopy structured ASICs. Pin-compatible with our Stratix series FPGAs, HardCopy structured ASICs reduce power consumption, your design risks, and total cost while enabling faster time to market. Programmable solutions also give you the advantage of long product life cycles, so you don’t have to worry about product obsolescence.

Driving quality into your designs

- Aggressive zero parts-per-million roadmap
- Best-in-class track record for shipping quality products
- Close collaboration with long-time manufacturing partner, Taiwan Semiconductor Manufacturing Company (TSMC), on the latest processing technologies to ensure defect-free, on-time silicon
- Member of the Automotive Electronics Council (AEC), tested according to AEC-Q100 specifications
- All automotive-grade products manufactured at TS16949-compliant facilities
- More than 70 Production Part Approval Process (PPAP) submissions to detail all of our manufacturing quality processes

How PLDs enable automotive electronics platform development

Whether you develop driver assistance, networking, or infotainment applications, there’s a programmable solution that fits in each stage in a typical automotive product life cycle.
On the road to productivity

Take advantage of design resources that can help you work smarter:

- The PARIS (Platform ASSP Replacement Infotainment System) scalable controller development platform, available from Altera partner TRS-STAR. This reference platform supports coprocessor and system-on-a-chip architecture. Providing full scalability on a single FPGA-based platform, the PARIS solution can speed development time and lower your total cost of ownership for head-unit, driver assistance, and navigation systems. Designs can be quickly and easily migrated to HardCopy structured ASICs incorporated onto real working boards, or ported to Cyclone FPGAs.

- Automotive development platform with a Cyclone II FPGA-based board and a graphics accelerator

- Nios II Embedded Evaluation Kit, a flexible graphics acceleration platform featuring a Cyclone III FPGA-based board and multiple design examples. No FPGA experience is required to use this kit, as the design examples are enabled via an interactive menu through the kit’s touch-panel LCD.

- An array of automotive reference designs, such as the automotive graphics controller reference design with Cyclone FPGAs and Nios II embedded processors

- Our Quartus® II design software, which is easy to use with all of our devices, and delivers fast compile times to get you on your way. Quartus software provides a simple development environment for designing with all of Altera’s automotive products—CPLDs, FPGAs, HardCopy structured ASICs, Nios II 32-bit embedded processors, and a large number of IP blocks (i.e., memory controllers, interface controllers, digital signal processing, and video/imaging functions).

HardCopy structured ASICs, in particular, can provide a substantial productivity boost in your design effort. After utilizing Stratix series FPGAs in the prototype stage (via the PARIS platform), experience a seamless, guaranteed migration path to pin- and package-compatible HardCopy structured ASICs at a fraction of the cost and risk of traditional custom ASIC solutions. Altera is currently the only PLD vendor offering automotive-grade ASICs. With this development approach, you use a single design, methodology, design tool, and IP set to create an implementation for an FPGA and one for an ASIC.

### Altera device benefits

<table>
<thead>
<tr>
<th>Device</th>
<th>Concept to emerging market benefits</th>
<th>Aggressive growth to mature market benefits</th>
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<tbody>
<tr>
<td>MAX series CPLDs</td>
<td>• Industry’s lowest-cost CPLD solution&lt;br&gt;• Non-volatile&lt;br&gt;• Reprogrammable&lt;br&gt;• Enables rapid innovation for automotive OEMs</td>
<td>• Industry’s lowest-cost CPLD solution&lt;br&gt;• Eliminates need and cost of migrating to standard product solution&lt;br&gt;• Enables continued innovation while volume ramps significantly: feature updates and differentiation</td>
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<tr>
<td>Cyclone series FPGAs</td>
<td>• Industry’s lowest-cost FPGA solution&lt;br&gt;• Reprogrammable&lt;br&gt;• Supports rapid prototyping as well as high-volume production requirements&lt;br&gt;• Allows you to differentiate as you go into production without cost pressure of a custom ASIC or ASSP&lt;br&gt;• Cyclone III FPGAs are the first 65-nm products offered in automotive grade</td>
<td>• Industry’s lowest-cost FPGA solution&lt;br&gt;• Increases product functionality at fraction of cost of ASIC solutions&lt;br&gt; • Enables rapid innovation: feature updates and differentiation</td>
</tr>
<tr>
<td>HardCopy series structured ASICS</td>
<td>• Low power&lt;br&gt;• Reduced risk&lt;br&gt;• Reduced total cost&lt;br&gt;• Faster time to market&lt;br&gt;• Faster time to profit&lt;br&gt;• ASIC-strength single event upset (SEU) and design security&lt;br&gt;• HardCopy derivative devices are applicable for higher volume production products&lt;br&gt;• HardCopy structured ASICS are applicable for medium-volume production products</td>
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**Want to dig deeper?**

To learn more about Altera’s automotive-grade products, contact your local FAE or sales representative. You can download automotive handbooks, white papers, and application notes; register for webcasts; purchase development kits; and more from our website at [www.altera.com/automotive](http://www.altera.com/automotive).