Pre-Release Schematic
DO NOT COPY

1.8V IO

SDM NAND Boot Flash Daughter Card

SDM Connector
NAND Flash
NAND_RSTn

SDM NAND Boot Flash Daughter Card

NOTES:
1. Project Drawing Numbers:
   - Raw PCB
   - Gerber Files
   - PCB Design Files
   - Assembly Drawing
   - Fab Drawing
   - Schematic Drawing
   - PCB Film
   - Bill of Materials
   - Schematic Design Files
   - Functional Specification
   - PCB Layout Guidelines
   - Assembly Rework

2. This board was designed for Stratix 10 SoC Development Kit.