



ALARIC

Instant-DevKit ARRIA 10 SoC FMC IDK

The ARRIA 10 SoC FMC Instant-Development Kit provides to developers the best Out-Of-The box experience combining the Best-In Class compact hardware platform and the most efficient intuitive software environment.

Dual-core ARM® Cortex™-A9 MPCore™ hard processor system (HPS).

Its unique install and GUI interface allows an immediate start, and its reference designs enable fast turn-around designs shortening and securing the developments.

The target markets are as wide as Video capture and processing, Industrial, Test & Measurement and Medical.

DevKitInvasion



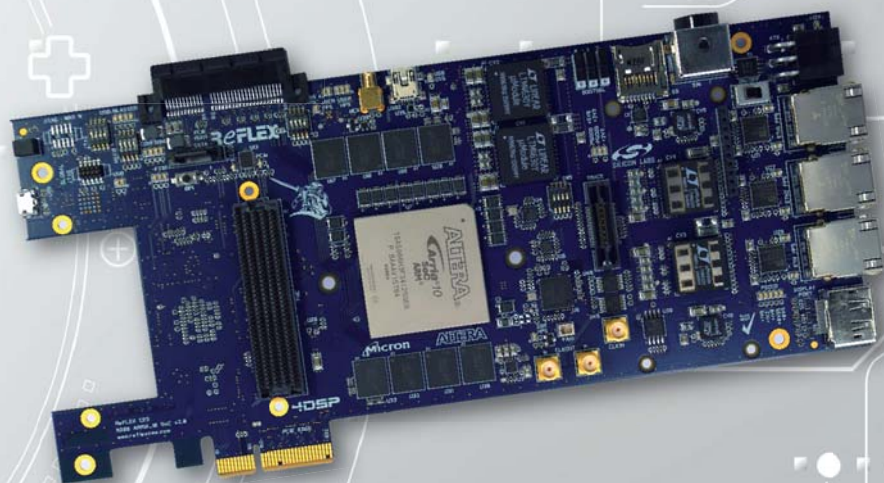
ATTILA
ARRIA 10 FPGA FMC IDK



ALARIC
ARRIA 10 SoC FMC IDK



ARMINIUS
ARRIA 10 FPGA JESD204B
Reference Design



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Featured Device

ARRIA 10 SoC 660 KLEs

- Devkit based on Altera ARRIA 10 SoC 660KLEs
- F34 package is compatible for ARRIA 10 SoC 270/320/480KLEs
- PCIe end point edge connector for Gen3 x4 (32Gb/s)
- PCIe root Complex edge connector for Gen3 x4 (32Gb/s)
- FMC High Pin Count fully compliant:
 - 168 LVCMOS (1.8V) or 84 LVDS (1.8V,2.5V)
 - 10 XCVR (up to 14.2Gb/s)
- One serial over USB High speed link (through USB Hub)
- 3 x RJ45 copper connector 10/100/1000Base-T Ethernet
- On board DDR3 for HPS (2GB) speed up to 2133 MT/s
- On board DDR3 for FPGA (2GB) speed up to 2133 MT/s

- DisplayPort Rev1.2 (up to 5.4Gbit/s)
- μSD Card connector
- USB 2.0 OTG (USB Mini connector AB type)
- MAX 10 Power supplies monitoring with its ADCs solution
- Innovative clock tree (Silicon Labs)
- +12V on a JACK/Edge PCIe connector / ATX connector
- Active Heat sink
- PCIe Bracket, LED
- SATA connector to FPGA 1 x XCVR up to 14.2Gbps
- MCX connector for PPS interface
- Mictor connector for HPS debug trace

Board Programming

- JTAG : On-board USB-Blaster II MAX V (reachable by USB Hub)
- Fast passive parallel (FPP) configuration via MAX10 device and flash memory
- AS configuration supported with NOR FLASH EPCQL512
- Boot selection for HPS through user dipswitch

Clock Circuitry

- 3 x SMA connectors (1 main input clock + 1 diff. output programmable clock)
- 9 x FPGA XCVR programmable inputs Clocks (up to 800 MHz to reach different communication protocol)

Board Size

- Length : 240 mm (9.45 inches)
- Height : 111.15 mm (4.38 inches) / Standard Height PCIe
- PCB thickness : 1.57 mm (0.062 inches) / Standard Thickness PCIe

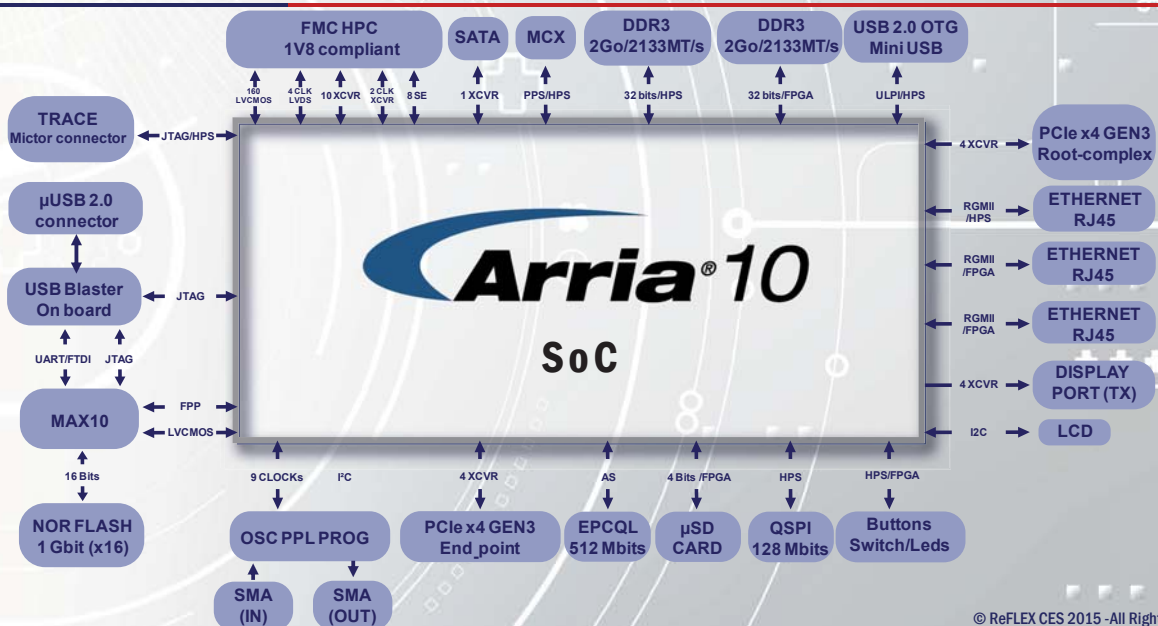
Kit Content

- Power adapter (US, UK, EU, JP) and cables (USB, Ethernet, DisplayPort, SMA)
- FMC and PCIe Loop back boards
- LCD (2 lines x 16 characters, I2C, 5V)
- Starter Guide
- ReFLEX CES innovative software interface (GUI) on USB Key/Windows64 with Test Designs

Kit Ordering informations

- RXCA10S066AF34-IDK00A (Q3-2015, Engineering version - ask for erratas)
- RXCA10S066PF34-IDK00A (Q4-2015, Production version)

Partners



Contact us :

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