

Product Brief:

HSMC Broadcast Video Daughter Card

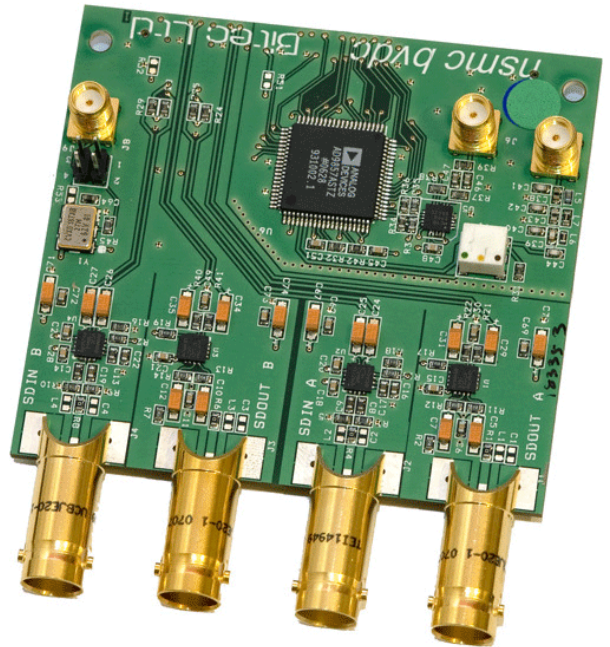


Features

- Dual ASI/SD-SDI transmitter/receiver
- Adaptive cable equalizers
- Multi-rate cable drivers
- 27 Mhz VCXO
- 200Mhz Quadrature Modulating 14-bit DAC
- Flexible clocking options for DAC

Applications

- ASI/SDI transceiver
- Low IF broadcast modulation
- Wireless Base Station
- Low frequency syntheses
- Broadband communications



The BVDC is designed for professional video equipment developers. The dual ASI/SD-SDI interfaces allows FPGA designs access to industry standard video transport signals. Based on the latest adaptive cable equalizers and drivers, the ASI/SDI interfaces produces excellent noise immunity up to cable lengths of 350m. A VCXO allows precise synchronisation to incoming ASI signals.

A Quadrature Digital Up-converter is also included on the BVDC board. Based on the Analog AD9857 14-bit DAC, the DUC circuit allows flexible modulation schemes such as those used for DVB-T and DVB-C etc. The DAC output incorporates an anti-alias filter and can drive in single-ended or differential mode.

For more information www.bitec-dsp.com

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