Product Summary
The proFPGA uno SG 280 system is a complete and modular FPGA solution, which meets highest requirements in the area of FPGA based Prototyping. It addresses customers who need a scalable and flexible high speed ASIC Prototyping and IP verification solution for early software development and real time system verification. The innovative system concept offers highest flexibility and reusability reconfigurability for several projects, which guarantees the best return on investment.

Highest Flexibility
The system architecture is based on a modular system concept. The FPGAs are assembled on dedicated single-chip FPGA modules, which will be plugged on the proFPGA uno, duo or quad motherboard. This offers the highest flexibility to use for example different FPGA types on the same motherboard. The user has access to almost all I/Os of the FPGA, which gives him maximum freedom regarding the FPGA interconnection structure. This way the prototyping system can be adapted in the best way to any user design. Furthermore the system offers a total of 8 extension sites on the top and bottom site for standard or user specific extension boards like DDR4 memory, PCIe gen1/2/3, Gigabit Ethernet, USB 3.0 or other high speed interface.

Maximum Performance
The well designed boards of the proFPGA system are optimized and trimmed to guarantee best signal integrity and to achieve highest performance. The high speed boards together with specific high speed connectors allow a maximum point-to-point speed of up to 1.0 Gbps single ended over the standard FPGA I/Os and up to 17.4 Gbps differential over the high speed serial transceivers of the FPGA. This performance allows the designer to run his design at the highest possible speed on the proFPGA system.

Biggest Capacity
Equipped with one Intel® Stratix® 10 FPGA module, the proFPGA uno system can handle up to 20 M ASIC gates on one board.

Very User Friendly
The proFPGA prototyping system provides an extensive set of features and tools, like remote system configuration, integrated self and performance test, automatic board detection, automatic I/O voltage programming, system scan and safety mechanism, which simplifies the usage of the FPGA based system tremendously.

Key Features
- 20 M ASIC gates capacity on one board
- 1026 user I/Os
- 24 dedicated high speed serial transceivers
- 8 individually adjustable voltage regions
- Up to 1.0 Gbps single ended point-to-point speed
- Up to 17.4 Gbps with high speed transceivers
- Smart Stacking Technology (SST)
- High performance host interface (DMBI)
- proFPGA Builder Software
Innovative Technologies

Smart Stacking Technology
- Board detection when boards are plugged
- Automatic and right I/O voltage setting and programming with conflict detection
- Integrated interconnection self and performance test
- Smart I/O resource management. No I/O resources get lost or blocked by connectors

FPGA Mixing Technology
- Easy plugging and unplugging of FPGA modules on motherboards
- Various FPGAs from different vendors can be mixed
- Automatic scan and detection of FPGA modules, when plugged
- Different FPGA configurations are controlled by proFPGA Builder software

Device Message Box Interface
- High speed, low latency data exchange system
- Enables various use modes and functionalities like remote system configuration and monitoring, debug, application level programming and co-simulation
- Up to 3.5 Gbps data transfer rate
- Runs over USB, Ethernet or PCIe

Advanced Clock Management
- 6 fixed clocks
- Run-time configurable local clocks
- Each clock with sync signals

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### profpga uno SG 280 Specification

**FPGAs**
- 1 x Intel® Stratix® 10 SG 280 FPGA Module

**Capacity**
- 20 M ASIC gates on one board

**FPGA-internal memory**
- 229 Mbit

**Signaling rate**
- Up to 1.0 Gbps single ended (standard I/O) / up to 17.4 Gbps differential

**Extension sites**
- 8 Extension sites with high speed connectors

**I/O resources**
- Overall 1028 signals for I/O and inter FPGA connection
  - 4x141 I/Os to top side connectors
  - 3x141 and 1 x 39 I/Os top side connectors

**High speed I/O transceivers**
- 24 dedicated transceivers running up to 17.4 Gbps
  - 1 x 12 and 2 x 6 transceivers to top side connectors

**Available interface boards**
- USB 3.0, PCIe Gen2/Gen3, MIPI, DVI, DDR3 memory, Gb Ethernet, etc.

**Voltage regions**
- 8 individually adjustable I/O voltage regions
  - Stepless from 1.2V up 1.8V on 8 extension sites
  - Automated detection of daughter cards and setting of right voltages

**Clocking**
- 6 fixed global clock and sync inputs per connector
  - use of up to 6 clock-capable I/Os per connector

**Configuration**
- From host via Ethernet, USB or standalone over USB stick or Xilinx JTAG

**Data exchange**
- On board DMBI (Device Message Box Interface)
  - Data exchange rate:
    - Ethernet (up to 100 Mbps), USB (480 Mbps)

**Power**
- External (optional) ATX Power Supply (12 V, 24 - 35 A output)

**Dimensions**
- 5.91” x 0.95” x 5.91” / 150 mm x 24 mm x 150 mm (width x height x depth)
- 0.5 kg weight

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### profpga uno SG 280 I/O and Clock Architecture