



Intel SpeedStep® Technology Applet

Release Notes

December 2006



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Revision History

Revision Number	Description	Revision Date
1.0	Initial release.	December 2006

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1 Introduction

This document contains the release notes for the Intel SpeedStep® technology applet. It describes recently added, changed or deleted features. This document is available from your Intel field service representative.

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2 Release Notes

2.1 Release Notes for the v3.2 Release

The Intel SpeedStep technology applet v3.2 release features the following:

- The v3.2 release has support for the new Intel® Core™ Solo processors (CPUID: 0x6EX). These processors are supported in single core mode only and cannot be used with the processor in dual core mode.
- The v3.2 release supports only Microsoft Windows* 2000. For older operating systems, Intel SpeedStep technology applet v3.1 must be used.

2.2 Release Notes for the v3.1 Release

The Intel SpeedStep technology applet v3.1 release features the following:

- The v3.1 release has updated the trademark from the original Intel® SpeedStep™ technology to Intel Speedstep® technology. This change applies to occurrences in both the code as well as the documentation.
- The v3.1 release fixes some of the localization issues that existed with previous versions.
- The v3.1 release supports a new consolidated End-User installation package which includes the installs of all the languages and does a dynamic install of the applicable language depending on the language of the installed OS.
- All other features and functionalities are the same as v3.0.

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