



Pat Gelsinger

Senior Vice President General Manager Digital Enterprise Group

Intel Developer FORUM

Risk Factors

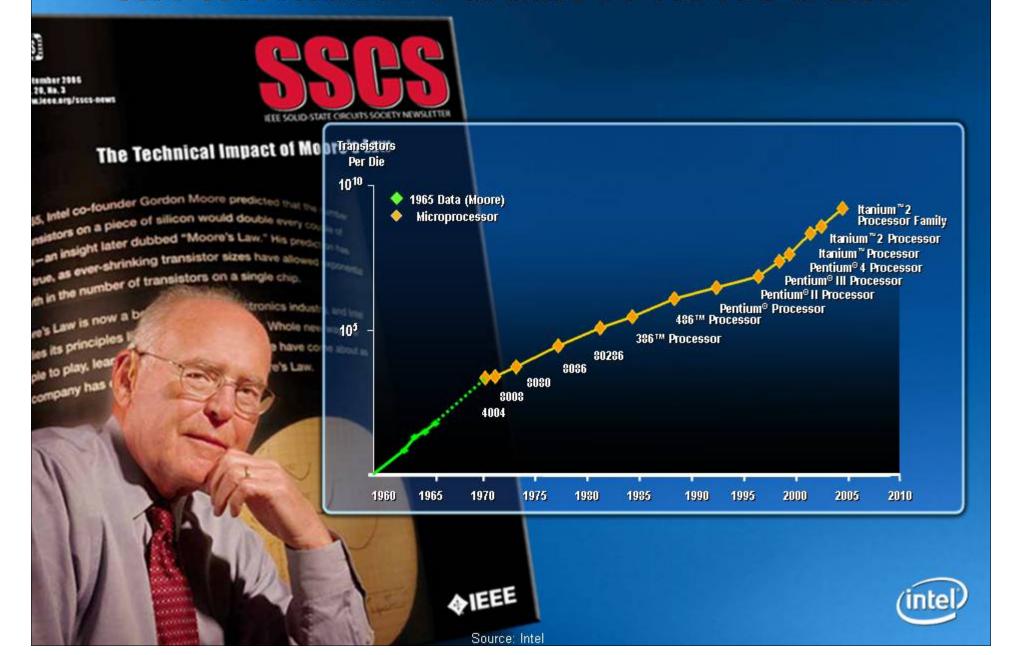
Today's presentations contain forwardlooking statements. All statements made that are not historical facts are subject to a number of risks and uncertainties, and actual results may differ materially. Please refer to our most recent Earnings Release and our most recent Form 10-Q or 10-K filing available on our website for more information on the risk factors that could cause actual results to differ.



Performance tests and ratings are measured using specific computer systems and/or components and reflect the approximate performance of Intel products as measured by those tests. Any difference in system hardware or software design or configuration may affect actual performance. Buyers should consult other sources of information to evaluate the performance of systems or components they are considering purchasing. For more information on performance tests and on the performance of Intel products, visit Intel Performance Benchmark Limitations (http://www.intel.com/performance/resources/limits.htm).



The Relentless Pursuit of Moore's Law





in 1965, Intel co-founder Gordon Moore predicted that the red transistors on a piece of silicon would double every couply years—an insight later dubbed "Moore's Law." His predictor held true, as ever-shrinking transistor sizes have allowed exemptowth in the number of transistors on a single chip.

Moore's Law is now a beautiful applies its principles little people to play, lear the company has

Amazing products

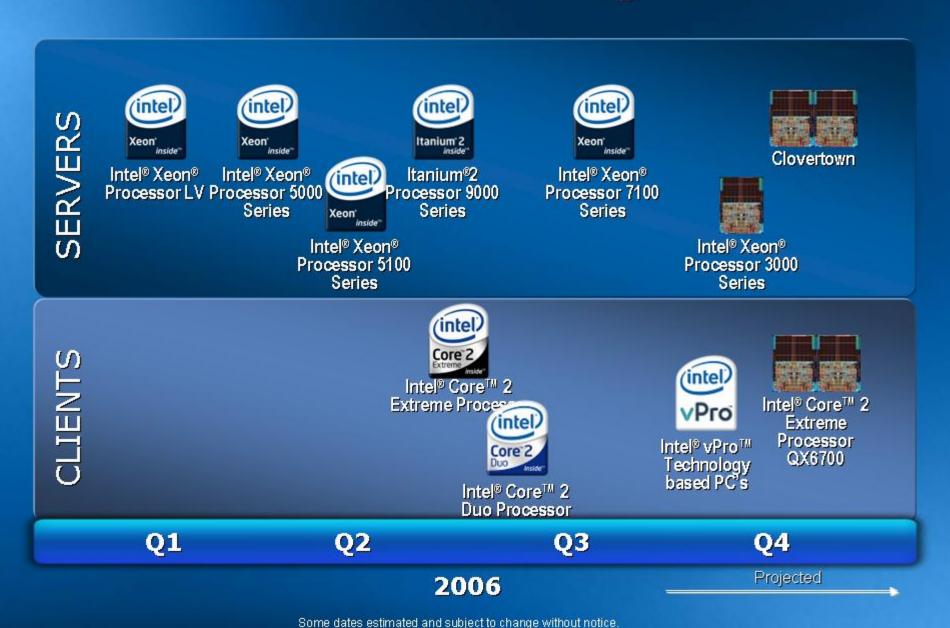
... done efficiently

... with the industry

... for the customer.



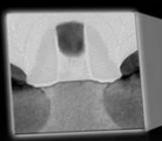
2006 - An Amazing Year



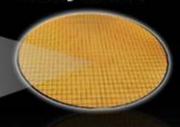
Intel[®] Core[™] 2 Duo Launch July 27, 2006

The Core 2 Contribution

World's Best Process Technology World's Best Microprocessor Design World's Biggest Factory Network











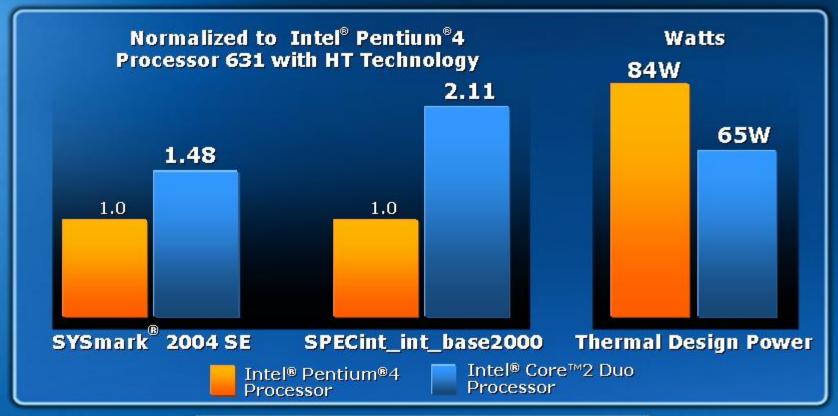


World's Best Processors





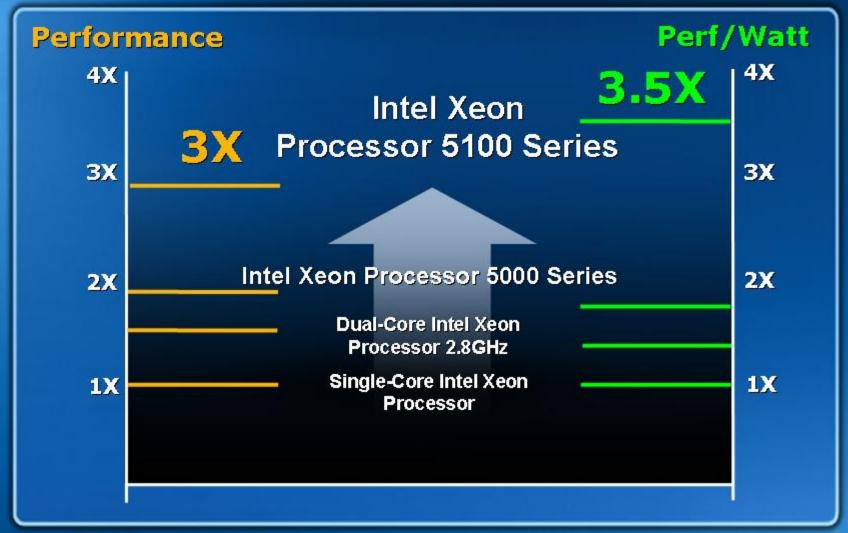
Intel® Core™ 2 Duo Technology







Intel[®] Xeon[®] Processor 5000 & 5100 Series







IBM Systems Agenda/ Intel Relationship



Collaborative Innovation

- BladeCenter* design
- Performance, scalability
- Power & cooling



Openness

- IBM BladeCenter open spec.
- Blade.org
- PCI & PCI Express



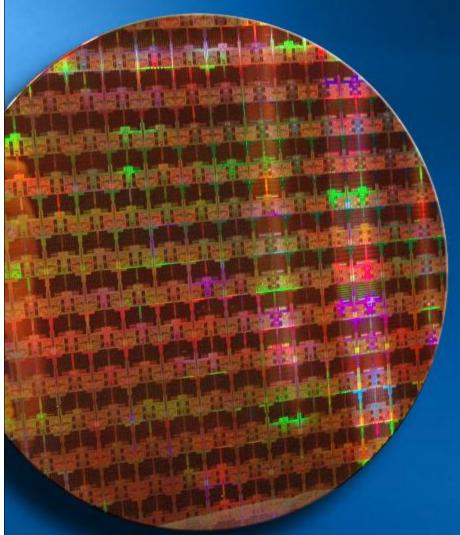
Virtualization

- Intel® Xeon® Processor 5100 3X performance
- Intel[®] Xeon[®] Processor 7100 enterprise scalability
- Peak performance factor





Intel® Xeon® Processor 7100 Series



Up to 2X Performance Improvement

Leading TPC-H & SAP SD 2-tier Results²

Dual Core w/16MB Cache

Up to 3X Performance per Watt Improvement

Platform Compatible

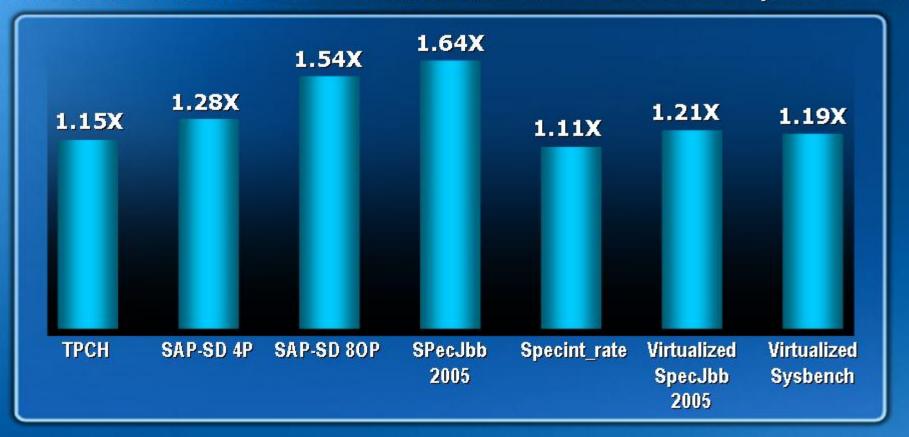
Advanced RAS Features

Intel® Cache Safe Technology



Breakthrough 4-Way Performance

Performance: Intel® Xeon® Processor 7140M vs. Best Published 4P AMD Opteron Result





IBM Investment In Innovation

- Intel® Xeon® 7100-based systems:
 - 20% better TPC-C performance than any other 4-socket x86 server
- X-Architecture 101st #1 leadership score
- Commitment to 4th gen. technology – supporting quad-core Xeon[®] MP





CoolBlue* Energy Management Framework

Power Configurator



PowerExecutive™



Rear Door Heat Exchanger PowerExecutive™

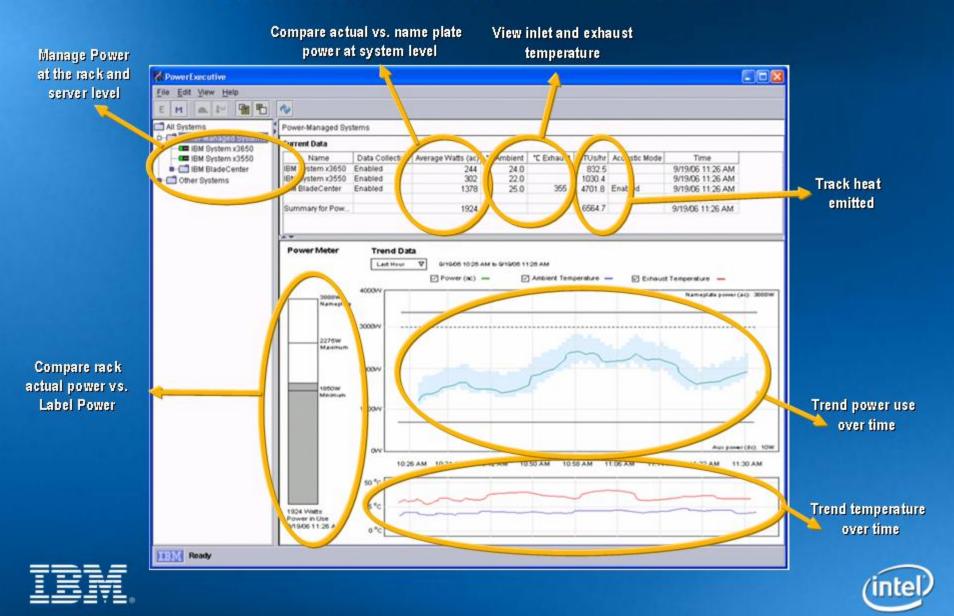


Built Upon Intel Demand Based Switching Technology





PowerExecutive* in Action!





Up to 55% Performance Increase

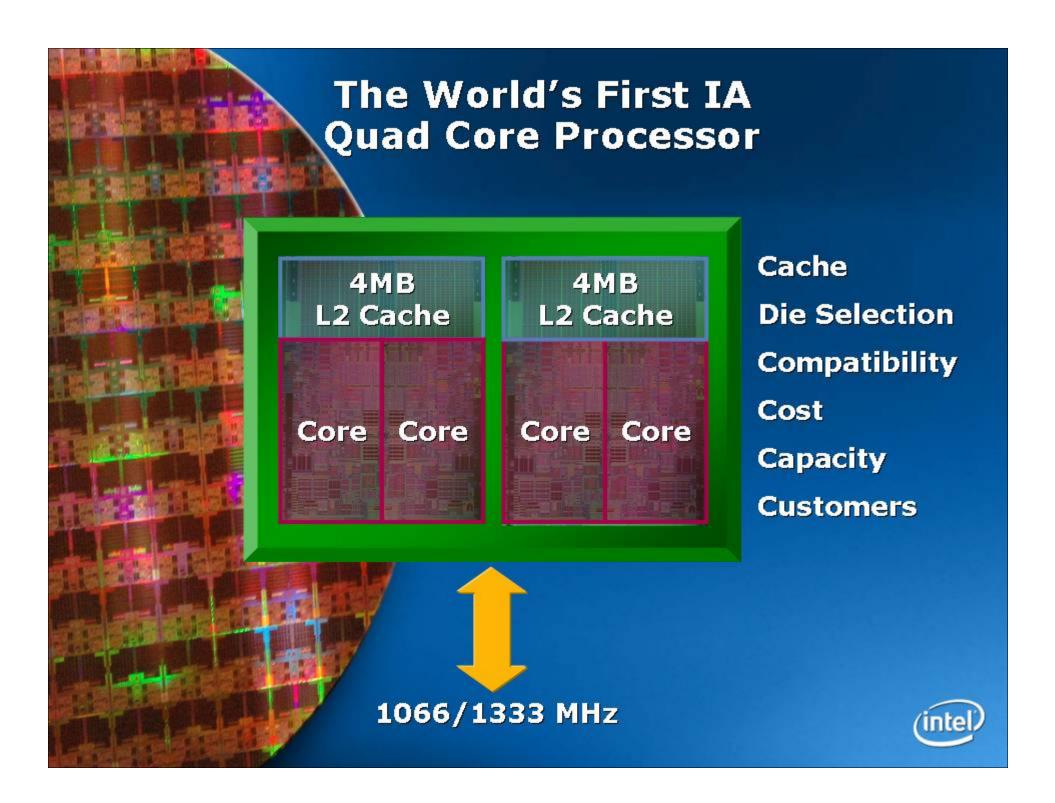
Socket & Platform Compatible

Intel® Core Micro-architecture

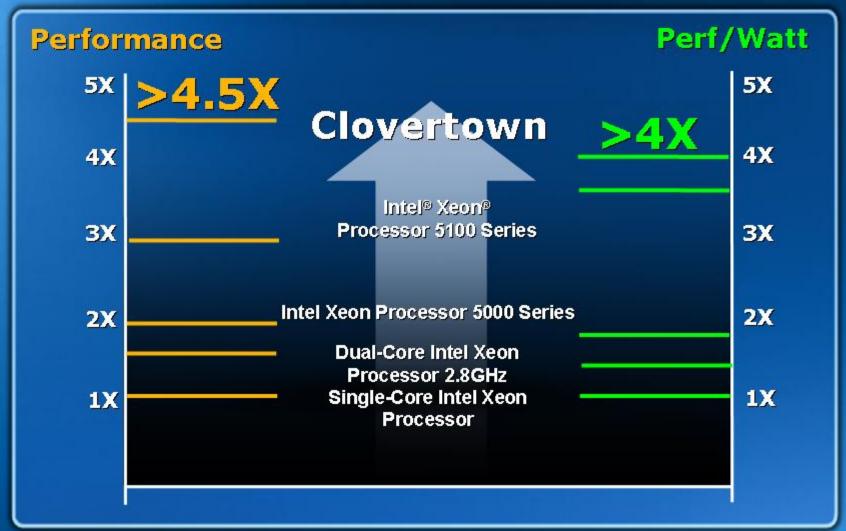
High Volume Server & Desktop

Available in Q4'06



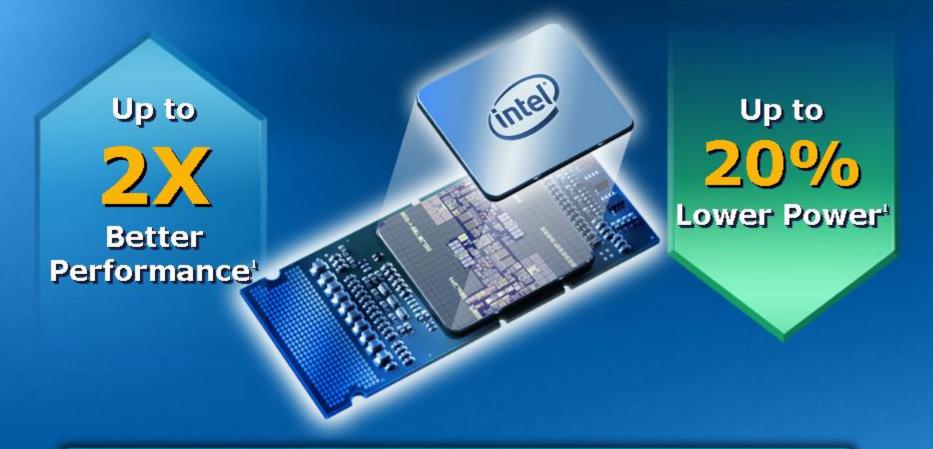


Exceptional Performance And Energy Efficiency





Itanium® 2 Processor 9000 Series



Up to 2.5X Better Performance per Watt



Itanium® Solutions Alliance 1 Year Anniversary

\$10,000,000,000 **ISA Investment**

2x Itanium Performance

> 100,000 **End-User** Deployments





























Hyperion











SYBASE















>10,000 Apps 2x YoY

100th ISA **ISV Member**

2.5x Itanium Perf/Watt

Itanium® 2 Flexibility & TCO Advantage

Seven SQL Server DB's

Sharing Processor & I/O Resources





#1 Oracle IA64 Native

#2 Oracle Clients SPARC

#3 IBM DB2/MQ SPARC

#4 DB2 Clients SPARC

#5 Apache IA64 Native

Hitachi Server





in 1965, Intel co-founder Gordon Moore predicted that the red transistors on a piece of silicon would double every couply years—an insight later dubbed "Moore's Law." His prediction held true, as ever-shrinking transistor sizes have allowed expension of transistors on a single chip.

Moore's Law is now a be applies its principles life people to play, lear the company has

Amazing products

... done efficiently

... with the industry

... for the customer



Benchmarking: Performance & Energy Efficiency MobileMark* 2002 - 2005 *Other names and brands may be claimed as the property of others

Benchmarking: Performance & Energy Efficiency





EECoMark*

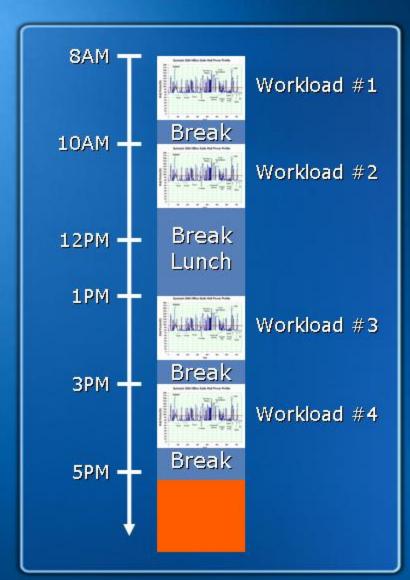
MobileMark* 2002 - 2005



Spec* "Power" (1H'2007)



Benchmarking: Performance & Energy Efficiency



"EDS is aligned with Intel in the need for a methodology to measure meaningful energy efficiency and performance based on the way people actually use PCs."

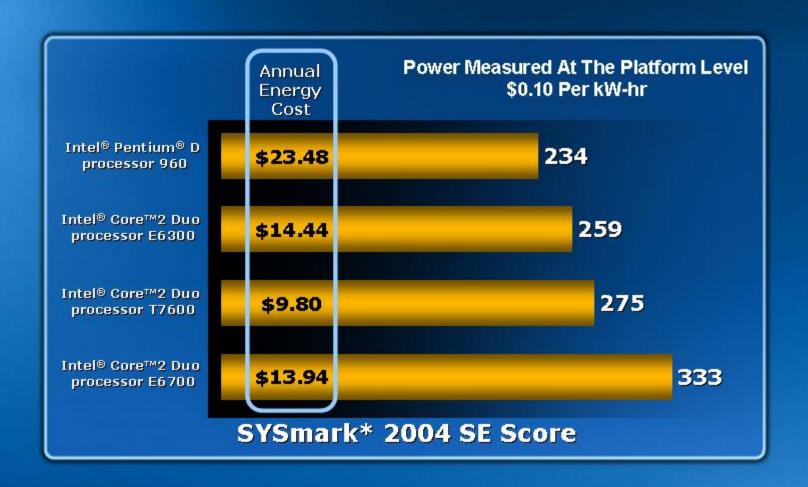
Matt Trevorrow

Vice President of Workplace Services





Performance and Energy Cost





"PG&E is looking forward to working with Intel and its server providers to deliver programs that educate customers about energy efficiency, and to financially support their purchasing decisions."

Mark Bramfitt

High Tech Segment Supervisor



Pacific Gas and Electric Company®





Dell* PowerEdge 2950



HP* ProLiant* BL460c



Other names and brands may be claimed as the property of others



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www.MPRonline.com

THE INSIDER'S GUIDE TO MICROPROCESSOR HARDWARE

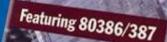
Can the 386 Architecture Keep Up?

John Hennessy and Pat Gelsinger Debate the Future of RISC vs. CISC

December 4, 1991









PROGRAMMING

John H. Crawford Patrick P. Gelsinger

Evolution of the ISA

80386

Full 32 bit architecture Paging SW debug

80386 SL

Chipset integration System Management Mode

80486

Integrated FPU

Pentium MMX

Intel MMX™ SIMD instructions

Pentium

VM86 model

Symmetric multi-

processing

Performance

monitors

Pentium II, III

Power management SSE

Pentium 4

SSE2 & SSE3

Hyperthreading Technology

Extended Memory 64 Technology

80286

Expanded Addressability Protected Mode Virtual Memory

8086

floating point



16 bit ISA





190

195





Announcing the Next Generation



White Paper

Extending the World's Most Popular Processor Architecture

New innovations that improve the performance and energy efficiency of Intel® architecture





New Instructions

Vectorizing Compiler

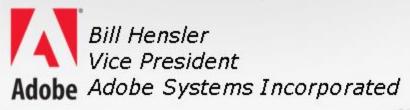
Media

String and Text Processing

Application Targeted
Accelerators



"Intel's instruction innovation offers Adobe the opportunity to reap significant performance improvements to our products. Adobe plans to utilize new instruction capabilities in our future products."



"Intel and Microsoft have a long history of working together on Instruction Set optimizations. We look forward to continuing our work to harness the benefits of these new instructions in future releases of Windows OS, tools and applications."

Amir Majidimehr Corporate Vice President Consumer Media Technologies Microsoft Corporation





Virtualization Programs

Novell.

Novell Introduces New Virtualization Solution Optimized for Intel Technology

Press Release

Novell and Intel collaborate to deliver first Xen-enabled Linux enterprise solution and drive virtualized Linux solution deployments

NEW YORK (InfoWorld Virtualization Executive Forum)—25 Sep 2006—Novell today announced the industry's first enterprise Linux-based virtualization solution built on Xen*, optimized for Intel* Virtualization Technology. SUSE® Linux Enterprise Server 10 from Novell® running on Dual-Core Intel Xeon* platforms will provide customers with a low-cost, high-performing virtualization solution that has the ability to host Linux* environments without the need to modify the guest operating systems. With the integration of Intel Virtualization Technology within Xen. Novell also announced plans to offer enterprise support for virtualized SUSE Linux Enterprise Server 9 and Red Hat* Enterprise Linux 4 running on SUSE Linux Enterprise Server 10, allowing Red Hat customers to migrate to Novell service and support while still running Red Hat Enterprise Linux in a virtualized environment.

"Being first to provide Xen virtualization with enterprise Linux means our customers will be first to reap the benefits of running virtualized Linux platforms, including Red Hat, on SUSE Linux Enterprise Server 10," said Jeff Jaffe, Novell executive vice president and chief technology officer. "This cross-platform approach to virtualization means both Novell and Red Hat customers will be able to take advantage of the cost and flexibility benefits of virtualization at a fraction of the cost of existing virtualization solutions."



Virtualize ASAP Program





Virtualize ASAP Software Vendors



http://www.VirtualizeASAP.com

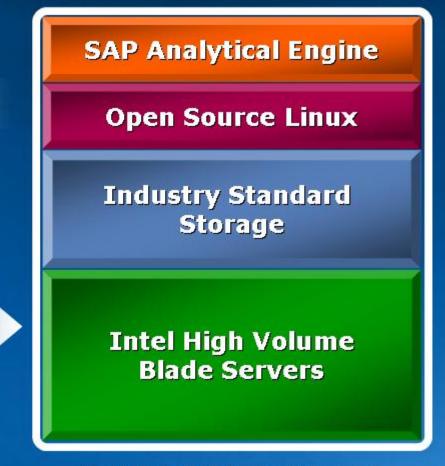




Information Appliances







Information Appliance



SAP NetWeaver

Looking Ahead



Multi-Core and Many-Core

Virtualization

Manageability

Security





Enabling Innovation — FSB Licensing



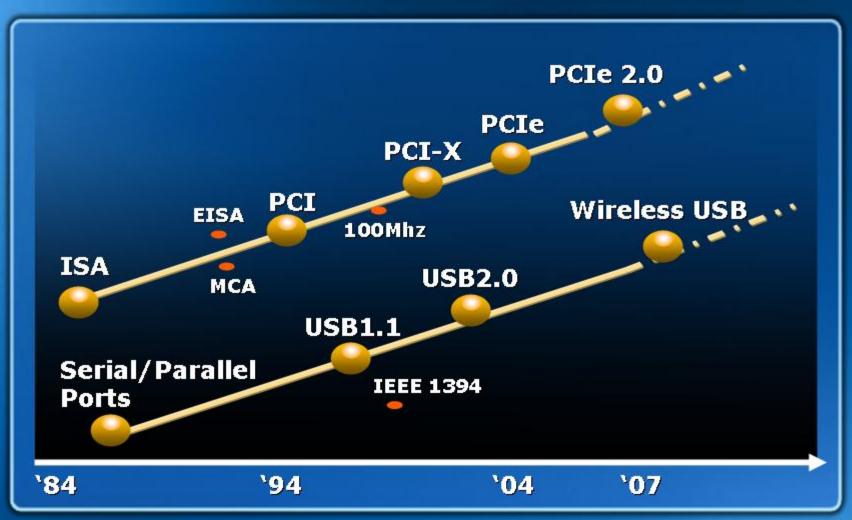
"Altera believes that FSB support, as demonstrated by Intel on Altera's Stratix FPGA, can enable exciting acceleration for applications such as medical imaging, financial services, and data analytics."

"Our continued alliance with Intel in support of next-generation interfaces, such as the Front Side Bus, will lead to new opportunities for Xilinx FPGA products and solutions to address and accelerate critical computing applications."



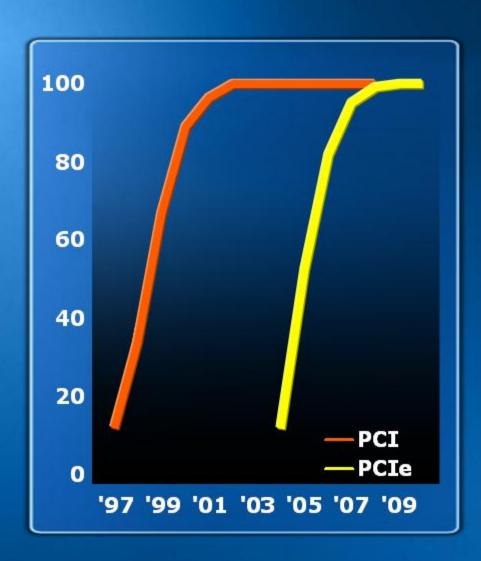


I/O for Compute Platforms





PCI & PCIe Growth



100% of Graphics Suppliers
Fiber Channel Storage Products
SCSI/RAID
SAS

SATA

Dual and Multiport 1Gb

All 10Gb

Television Tuners

Accelerators





Growing Interest in Accelerators



Math-Intensive Transactions
Financial, scientific, economic models



Visualization and Media Processing Graphics, video, speech



Embedded Content Processing
Data mining, encryption, compression, XML



Broad Industry Support



































Myricom:



























As a leading provider in high performance networking, QLogic supports Intel in its pursuit to provide higher bandwidth and lower latency access to processor platforms in order to address the I/O interconnect requirements of the future.





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... for the customer



Years of Research

Customer Concerns Go Beyond Price and Performance



Security

- \$81M in losses reported in 2004 due to virus & DoS attacks?
- Time to exploit after fix availability rapidly shrinking³



TCO

- Over 15% of assets not discoverable down-the-wire due to removed agents, OS/power states
 - 5% of support incidents are desk side visits resulting in 52% of support costs



Complexity

- Cutting hardware configurations in half can lower support costs by 10% to 55%5
- Additional OS version can increase TCO by \$135 to \$203 per PC/year1



• 90% of business process lost to lag time-task travel from one person to anothers







Intel® vPro™ Technology-based Platform



vPro: The Next Generation of Business PCs



Fight Worms While You Sleep





VARs, MSPs Bank on Intel's vPro To Reduce On-Site Service Calls





Intel vPro Promises IT Managers Less Needy Desktops



2006 Usages

Audit a Powered-down PC

Remotely Repair A Down System

Keep Software Agents Active

Securely Wake & Update Systems

Virtual Security and Management Appliances

Stunning Performance

Energy Efficiency



Broad Industry Support









































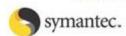




HP OpenView





















LANDesk



















































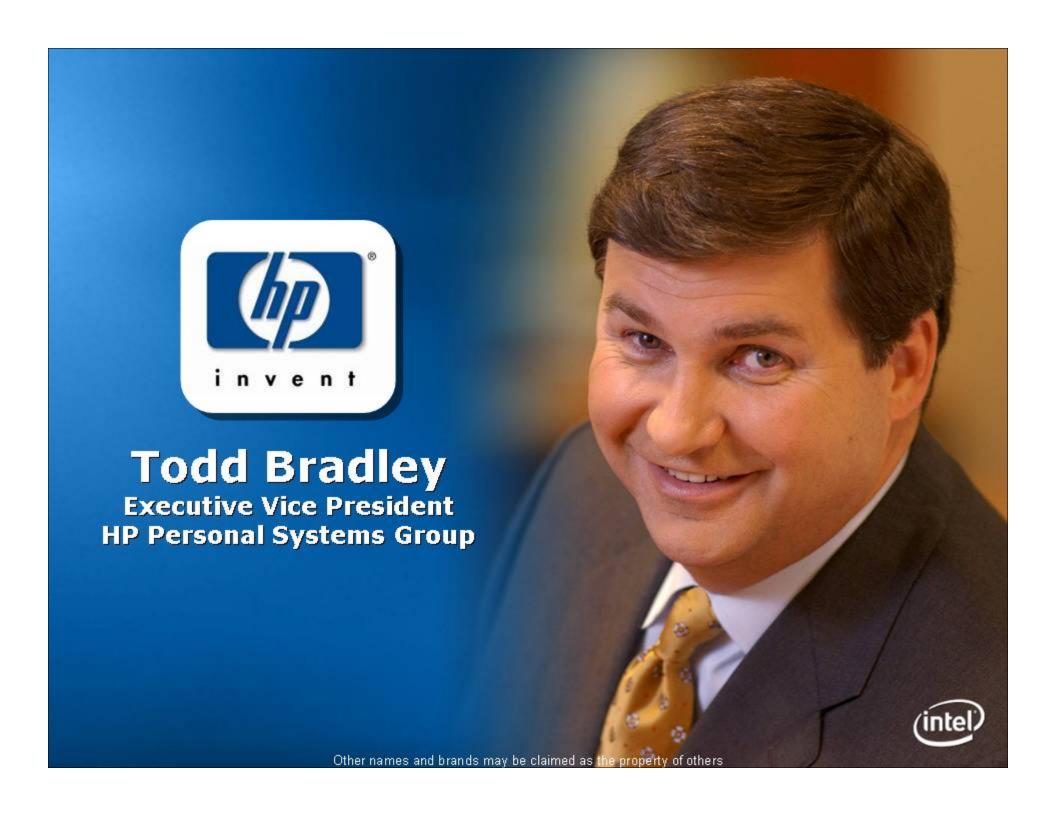












"...adoption of Intel vPro technology will significantly improve (our) ability to cost-effectively, more securely, and efficiently manage our global fleet."



BMW Group

"There's a whole range of things that Symantec and Intel can accomplish given the new Intel vPro technology ..."



symantec..

"Capgemini is well positioned to take advantage of the remote management capabilities embedded in Intel® vPro™ technology. We believe that it will provide our customers with an exceptional user experience with less downtime, lower support costs, increased performance and a significant reduction in power consumption."







Traditional Client Compute Models

Thin Clients
Terminal/Server

Claims:

Data security
TCO
Reduced complexity

SaaS Enabled Client

Thick Client
Desktops/Notebooks

Claims:

Increased Productivity
Rich User Experience
Mobility



SaaS Enabling Architecture

Client PCs

Authenticate with Service

Data Center Server





On Exit, Data Saved To Server

... with the industry

















... for the customers



















Rich Internet Applications with Apollo





SaasCon



The Business to Business event for Software as a Service

SEPTEMBER 25-26, 2006
SAN FRANCISCO MARRIOTT IN SAN FRANCISCO, CA

Cornerstone Sponso

Platinum Sponsors



QUALYS

Chmcstware

Microsoft









Enhancing SaaS Through Platform Innovation

09/26/2006, 2:15 PM - 3:00 PM

Steva Grobman, Director, Strategic Planning - Digital Office Platform Division, Intel Corporation.

Software as a Service is changing the landscape of enterprise computing. Streaming OS and applications to clients promises to reduce total cost of ownership (TCO) of managing business clients. Through this change there are a number of challenges to insure secure and manageable clients. Intel has developed a number of advances in its server and business client lines of products to address these challenges. This session will highlight the challenges of sass. overview Intel platform innovations and deliver on the promise of improved security and advances overcome the challenges and deliver on the promise of improved security and

manageability for reduced TCO.



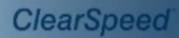
















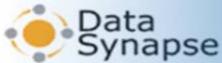












echogent Amazing products





















SSAS SYBALL with the industry











