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## 2008 Performance Summary

In 2008, we extended our climate change strategy by becoming the largest purchaser of green power in the U.S., according to the U.S. EPA, and installing solar systems at three Intel sites. To help focus all of our employees on environmental sustainability, we aligned a portion of their variable compensation with environmental criteria. We continued to face challenges in achieving absolute reductions in our environmental impact due in part to the increasing complexity of our manufacturing processes, recording increases in both water use and chemical waste generated.

Subsequent to setting new 2012 environmental goals at the beginning of 2008, we sold our NOR flash business to Numonyx B.V. To avoid the possibility of overstating reductions by including amounts that would be attributed to the sale of these operations, we created a revised 2007 baseline for the goals, with the Numonyx data removed. We believe that using this revised baseline allows us to better track results arising from the direct actions we are taking in our operations to reduce our environmental footprint. Percentages in the following table show our progress to date against the revised 2007 baseline; tables, graphs, and data featured throughout the rest of the report feature the historical 2007 figures.

Environment Goals and Performance		
2012 Goals	2008 Progress Against Goals	
Reduce water use per chip <sup>1</sup> below 2007 levels by 2012.	Tracking against our 2007 baseline, our water use was up 2% on a per chip basis. We are taking steps in 2009 to correct this trend and expect to meet our 2012 goal.	
Reduce absolute global-warming gas footprint by 20% by 2012 from 2007 levels.	Total emissions were down 27% on an absolute basis compared to our 2007 baseline, keeping us on track for our goal, even with expected growth during the next five years.	
Reduce energy consumption per chip 5% per year from 2007 through 2012.	Per chip energy use was down just 1% compared to our 2007 baseline, but we still expect to achieve an average annual reduction of 5% by 2012.	
Reduce generation of chemical waste per chip by 10% by 2012 from 2007 levels.	Chemical waste generated per chip was up 20% over our 2007 baseline, putting us at risk of not meeting our 2012 goal. To drive reductions going forward, we have set additional internal waste goals for processes that we are bringing online in the next two years, and we set up a team to study ways to reduce the use of certain chemicals.	
Recycle 80% of chemical and solid waste generated per year.	We recycled 84% of our chemical waste and 88% of our solid waste in 2008.	
Achieve engineering and design milestones to ensure that Intel products keep the energy-efficiency lead in the market for our next two product generations.	We met our energy-efficiency product targets in 2008.	

**Achieved**  
 **Partially Achieved**  
 **Not Met**

<sup>1</sup> Assuming a typical chip size of approximately 1 cm<sup>2</sup> (chips vary in size depending on the specific product).

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## Looking Ahead: Our 2012 Environmental Goals

In 2009, we will continue to work toward achieving the five-year goals that we set at the beginning of 2008, placing a strong emphasis on our water and chemical waste generation goals. We will continue our collaborations with external organizations on sustainability issues, particularly in the area of the role that ICT can play in addressing the issue of climate change. We will also drive increased employee engagement on environmental sustainability issues through challenges, recognitions, and the creation of a new employee intranet site focused on the environment.

Environment Goals for 2009 and Beyond
Reduce water use per chip <sup>1</sup> below 2007 levels by 2012.
Reduce our absolute global-warming gas footprint by 20% by 2012 from 2007 levels.
Reduce energy consumption per chip 5% per year from 2007 through 2012.
Reduce generation of chemical waste per chip by 10% by 2012 from 2007 levels.
Recycle 80% of chemical and solid waste generated per year.
Achieve engineering and design milestones to ensure that Intel products keep the energy-efficiency lead in the market for our next two product generations.
<small><sup>1</sup> Assuming a typical chip size of approximately 1 cm<sup>2</sup> (chips vary in size depending on the specific product).</small>

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2008 Corporate Responsibility Report  
[www.intel.com/go/responsibility](http://www.intel.com/go/responsibility)