

AHCI 1.3

Errata 003 Draft



AHCI 1_3 Errata_003.doc

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1 D2H FIS Received without an Active Command Slot Clarification

1.1 Description of Technical Issue

In Section 6.1.12 the specification describes how a host should behave if it receives a DMA Setup FIS without an active command slot associated with it. The section incorrectly states 'with' instead of 'without'.

1.2 Description of Correction to Specification

Modify section 6.1.12 D2H FIS Received without active command slot

When a DMA Setup FIS is received ~~with~~ without an active command slot for the device, the HBA sets PxIS.IFS to '1'. For all other D2H FISes received without an active command slot for the device, the HBA drops the FIS quietly (not setting any error bits), does not post the FIS, and does not make any register updates based on the FIS.

DMA Setup is treated as a fatal error since these FISes could be due to rogue devices trying to gain access to memory space that does not belong to that device. For other D2H FISes, these may be received due to lingering FISes on the link after error handling and thus they are dropped quietly to avoid creating additional error handling situations.